## LESD11D5.0CT5G ESD PROTECTION DIODE <br> Discription

The LESD11D5.0CT5G is designed to protect voltage sensitive components from ESD.
Excellent clamping capability, low leakage, and fast response time ,make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

## Applications

I Cellular phones audio
| MP3 players
I Digital cameras
I Portable applicationss
I mobile telephone

## Features

, Small Body Outline Dimensions:
$0.61 \mathrm{~mm} \times 0.31 \mathrm{~mm}$

LESD11D5.0CT5G


DFN0603-D

PIN 1
$A \Sigma$

A = Specific Device Code M = Month Code

Ordering information

| Device | Marking | Shipping |
| :---: | :---: | :---: |
| LESD11D5.0CT5G | A | 15000/Tape\&Reel |

, Low Body Height: 0.28 mm
। Low Leakage
) Response Time is Typically < 1 ns
1 ESD Rating of Class 3 (> 16 kV ) per Human Body Model
। IEC61000-4-2 Level 4 ESD Protection
, These are $\mathrm{Pb}-$ Free Devices
) We declare that the material of product compliance with RoHS requirements.

## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :--- | :--- | :--- |
| IEC 61000-4-2 (ESD)Air discharge <br> Contact discharge |  | $\pm 15$ <br> $\pm 8$ | kV <br> kV |
| ESD Voltage Per Human Body Model |  | 16 | kV |
| Total Power Dissipation on FR-5 Board (Note 1) <br> @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | PD | 200 | mW |
| Junction and Storage Temperature Range | TJ,TSTG | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| Lead Solder Temperature <br> Second Duration) | 260 | ${ }^{\circ} \mathrm{C}$ |  |

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. $F R-5=1.0 * 0.75 * 0.62 \mathrm{in}$.

## ELECTRICAL CHARACTERISTICS

( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise noted)

| Symbol | Parameter |
| :---: | :--- |
| $I_{P P}$ | Maximum Reverse Peak Pulse Current |
| $\mathrm{V}_{\mathrm{C}}$ | Clamping Voltage @ IPp |
| $\mathrm{V}_{\mathrm{RWM}}$ | Working Peak Reverse Voltage |
| $\mathrm{I}_{\mathrm{R}}$ | Maximum Reverse Leakage Current @ $\mathrm{V}_{\mathrm{RWM}}$ |
| $\mathrm{V}_{\mathrm{BR}}$ | Breakdown Voltage @ $\mathrm{I}_{\mathrm{T}}$ |
| $\mathrm{I}_{\mathrm{T}}$ | Test Current |
| $\mathrm{P}_{\mathrm{pk}}$ | Peak Power Dissipation |
| C | Capacitance @ $\mathrm{V}_{\mathrm{R}}=0$ and $\mathrm{f}=1.0 \mathrm{MHz}$ |



ELECTRICAL CHARACTERISTICS

|  | $V_{\text {RWM }}$ (V) | $\begin{array}{r} I_{R 1}(\mu A) \\ @ V_{R W M} \end{array}$ | $\begin{gathered} \mathbf{I}_{\mathbf{R 2}}(\mu \mathrm{A}) \\ @ \mathbf{V}_{\mathbf{R}}=3.5 \mathrm{~V} \end{gathered}$ | $\begin{aligned} & \mathbf{V}_{\mathrm{BR}}(\mathrm{~V}) @ \mathrm{I}_{\mathrm{T}} \\ & \text { (Note 2) } \end{aligned}$ | $\mathrm{I}_{\mathbf{T}}$ |  | $\begin{gathered} \mathbf{V}_{\mathbf{C}}(\mathbf{V}) \\ @ \text { MAX IPP } \\ \text { (Note 3) } \end{gathered}$ | $I_{P P}(A)$ <br> (Note 3) | $\begin{aligned} & \mathbf{P P K}_{\mathbf{P K}}(\mathbf{W}) \\ & \text { (Note 3) } \end{aligned}$ | C (pF) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Device | Max | Max | Max | Min | mA | Max | Max | Max | Max | Max |
| LESD11D5.0CT5G | 5.0 | 0.5 | 0.3 | 5.6 | 1.0 | 9.8 | 12.5 | 5.5 | 69 | 15 |

Other voltage available upon request.
2. $\mathrm{V}_{\mathrm{BR}}$ is measured with a pulse test current IT at an ambient temperature of $25^{\circ} \mathrm{C}$
3. Surge current waveform per Figure 3.


Fig1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000-4-2


Fig2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2


Fig3. Pulse Waveform

## DFN0603-D

## DIMENSION OUTLINE:



Soldering Footprint


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