

1A Bipolar Linear Regulator

LR1117D

DESCRIPTION

LR1117D is a series of low dropout three- terminal regulators with a dropout of 1.3V at 1A load current. LR1117D features a very low standby current 2mA compared to 5mA of competitor.

Other than a fixed version, Vout = 1.2V, 1.8V, 2.5V, 2.85V, 3.3V, 5V, LR1117D has an adjustable version, which can provide an output voltage from 125V to 12V with only two external resistors.

LR1117D offers thermal shut down functions, to assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$. Other output voltage accuracy can be customized on demand, such as $\pm 1\%$

LR1117D is available in SOT-223, TO-252 power package.

FEATURES

- Other than a fixed version and an adjustable version, output value can be customized on demand.
- Maximum output current : 1.4A
- Range of operation input voltage: Max 15V
- Standby current: 2mA (typ.)
- Line regulation: 0.03%/V (typ.)
- Load regulation: 0.2%/A (typ.)
- Environment Temperature: -40°C~85°C

APPLICATIONS

- Power Management for Computer Mother Board, Graphic Card
- BLD Monitor and BLD TV
- DVD Decode Board
- ADSL Modem
- Post Regulators for Switching Supplies

TYPICAL APPLICATION



Application circuit of LR1117D fixed version

NOTE: Input capacitor (Cin=10uF) and Output capacitor (Cout=10uF) are recommended in all application circuit. Tantalum capacitor is recommended.

LR1117D - ADJ Vout Vs. Temp

TYPICAL ELECTRICAL CHARACTERISTIC





ORDERING INFORMATION

LR1117D <u>X</u> <u>XX X</u>	Temp. Range & Rohs Std. X: 85C & Pb-free Rohs Std,Output voltage accuracy within ±2%
I	Output Voltage:
	12·····1.2V
	181.8V
	252.5V
	282.85V
	333.V
	505.0V
	·····ADJ
	Package Type: S: SOT-223 D: TO-252

PIN CONFIGURATION AND MARKING

Pin Description: SOT-223

2 3

TO-252

2

3

1

Fixed Version

Pin No.	Symbol	Definition
1	Gnd	Ground
2	Vout	Output
3	Vin	Input

Adjustable Version

Pin No.	Symbol	Definition
1	1 Adj. Ad	
2	Vout	Output
3	Vin	Input
		•

SOT-223	Marking	
1117 B	1117	B
VWXX YYZ	VWXX	YYZ

OT-223	Marking	Designator	Description
117 B VXX YYZ		1117 B	Product code
	1117 D	V W	assemble year and week
	III/ B	XX	Manufacture Lot No. (the end two number)
	VWAA 112	YY	Output Voltage
		Z	Version please fixed

ABSOLUTE MAXIMUM RATING

Parameter	Value		
Max Input Voltage		18V [®]	
Max Operating Junction Temperature(Tj)		150°C	
Ambient Operating Temperature(Ta)		-40°C – 85°C	
Package Thermal Resistance	SOT-223	20°C / W	
TO-252		10°C / W	
Storage Temperature(Ts)		-40°C - 150°C	
Lead Temperature & Time		260°C, 10S	

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMENDED WORK CONDITIONS

Parameter	Value
Input Voltage Range	Max. 15V
Operating Junction Temperature(Tj)	-20°C –125°C



ELECTRICAL CHARACTERISTICS

Tj=25°c

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Vref	Reference Voltage	LR1117D-ADJ	1.225	1.25	1.275	V
		10mA≤lout≤1A, Vin=3.25V				
		LR1117D-1.2V	1.176	1.2	1.224	V
		USIOUTSIA, VIN=3.2V				
		LKIII/D-1.8V $0 \le 0.001 \le 14$ Vin=3.8V	1.764	1.8	1.836	V
		IR1117D-2.5V				
		$0 \le 10 \le 10$ Vin=4.5V	2.45	2.5	2.55	V
		LR1117D-2.85V				
Vout	Output Voltage	0≤lout≤1A , Vin=4.85V	2.793	2.85	2.907	V
		LR1117D-3.3V	2 224	2.2	2 266	V
		0≤lout≤1A , Vin=5.3V	5.254	5.5	5.500	v
		LR1117D-5.0V	49	5	51	V
		0≤lout≤1A , Vin=7.0V				-
		LR1117D-ADJ		0.03	0.2	%/V
		lout=10mA, $2.75V \le Vin \le 12V$				
		LR111/D-1.2V		0.03	0.2	%/V
		$100t=10mA$, $2.7v \ge vin \ge 10v$				
		LR111/D-1.8V		0.03	0.2	%/V
	Line Regulation			0.03	0.2	%/V
		$L_{1117}D_{-2.5}V$				
AVout		IB1117D-2 85V		0.03	0.2	%/V
		$lout=10m\Delta 4.0V \le Vin \le 12V$				
		IB1117D-3 3V			0.2	%/V
		$lout = 10mA / 8V \le Vin \le 12V$		0.03		
						%/V
				0.03	0.2	
		$IOUT=10MA, 6.5V \ge VIN \ge 12V$				
		$V_{in} = 2.75V_{in} 10mA \le lout \le 1A$		2	8	mV
				2	8	mV
		$VIn = 2.7V$, $IOMA \ge IOUT \ge IA$				
		LKIII/D-1.8V Vin=2.0V $10mA \le lout \le 1A$		3	12	mV
		$V_{11}=3.00, 1000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 10000 = 100000 = 10000 = 10000 = 10000 = 100000 = 1000000 = 10000 $				
				4	16	mV
ΔVout	Load Regulation	$VIn=3.3V$, $IUMA \ge IOUT \ge IA$				
		LR111/D-2.85V		5	20	mV
		Vin=4.0V, 10mA \leq lout \leq 1A				
		LR1117D-3.3V		6	24	mV
		Vin=4.8V, 10mA \leq lout \leq 1A	Ű			
		LR1117D-5.0V		9	36	m\/
		Vin=6.5V, 10mA \leq Iout \leq 1A		9	50	111V



ELECTRICAL CHARACTERISTICS continued

Tj=25°C

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
		lout=100mA		1.15	1.3	V	
varop	Dropout voltage	lout=1A		1.3	1.5	V	
SVR	SVR Supply Voltage Rejection f = 120Hz, VIN – VOUT = 3V + 1VPP Ripple			60		dB	
Imin	Minimum Load Current	LR1117D-ADJ		2	10	mA	
		LR1117D-1.2V, Vin =10V	1	2	5	mA	
		LR1117D-1.8V, Vin =11V	1	2	5	mA	
		LR1117D-2.5V, Vin =12V	1	2	5	mA	
lq	Quiescent Current	LR1117D-2.85V,Vin =12V	1	2	5	mA	
		LR1117D-3.3V, Vin =12V	1	2	5	mA	
		LR1117D-5.0V, Vin =12V	1	2	5	mA	
IAdj	Adjust Pin Current	LR1117D-ADJ Vin = 5V, 10mA \leq lout \leq 1A	35	55	120	uA	
Ichange	ladj change	LR1117D-ADJ Vin =5V, 10mA \leq lout \leq 1A		0.2	10	uA	
ΔV/ΔΤ	Temperature coefficient			±100		ppm	
		SOT-223		20		°C/W	
θ_{JC}	Thermal Resistance	TO-252		10			
						1	
	Thermal Resistance	SOT-223 (No heat sink)		136			
θ_{JA}	Junction-to-Ambient (No air flow)	TO-252 (No heat sink)		92		°C/W	

Note1: All test are conducted under ambient temperature 25°C and within a short period of time 20ms

Note2: Load current smaller than minimum load current of LR1117D-ADJ will lead to unstable or oscillation output.

BLOCK DIAGRAM





DETAILED DESCRIPTION

LR1117D is a series of low dropout voltage, three terminal regulators. Its application circuit is very simple: the fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, bandgap, thermal shutdown, power transistors and its driver circuit and so on.

The thermal shut down modules can assure chip and its application system working safety when the junction temperature is larger than 140°C.

The bandgap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The temperature coefficient is under 100 ppm/°C. And the accuracy of output voltage is guaranteed by trimming technique.

TYPICAL APPLICATION

LR1117D has an adjustable version and ix fixed versions (1.2V, 1.8V, 2.5V, 2.85V, 3.3V, 5V)

Fixed Output Voltage Version



Application circuit of LR1117D fixed version

- 1) Recommend using 10uF tan capacitor or MLCC capacitor as bypass capacitor (C1) for all application circuit.
- 2) Recommend using 10uF tan capacitor MLCC capacitor to assure circuit stability.
- 3) Capacitor ESR range: $3m\Omega \sim 22\Omega$

Adjustable Output Voltage Version

LR1117D-ADJ provides a 1.25V reference voltage. Any output voltage between 1.25V~12V can be achievable by choosing two external resistors (schematic is shown below), R1 and R2



Application Circuit of LR1117D-ADJ

The output voltage of adjustable version follows the equation: Vout= $1.25 \times (1+R2/R1)+IAdj \times R2$. We can ignore IAdj because IAdj (about 50uA) is much less than the current of R1 (about 2~10mA).



- 1) To meet the minimum load current (>10mA) requirement, R1 is recommended to be 125ohm or lower. As LR1117D-ADJ can keep itself stable at load current about 2mA, R1 is not allowed to be higher than 625ohm.
- 2) Using a bypass capacitor (C_{ADJ}) between the ADJ pin and ground can improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. The impedance of C_{ADJ} should be less than R1 to prevent ripple from being amplified. As R1 is normally in the range of $100\Omega^{\sim}500\Omega$, the value of C_{ADJ} should satisfy this equation: $1/(2\pi x f_{ripple} \times C_{ADJ})$
R1.

THERMAL CONSIDERATIONS

We have to take heat dissipation into great consideration when output current or differential voltage of input and output voltage is large. Because in such cases, the power dissipation consumed by LR1117D is very large. LR1117D series uses SOT-223 package type and its thermal resistance is about 20°C/W. And the copper area of application board can affect the total thermal resistance. If copper area is 5cm*5cm (two sides), the resistance is about 30°C/W. So the total thermal resistance is about 20°C/W + 30°C/W. We can decrease total thermal resistance by increasing copper area in application board. When there is no good heat dissipation copper are in PCB, the total thermal resistance will be as high as 120°C/W, then the power dissipation of LR1117D could allow on itself is less than 1W. And furthermore, LR1117D will work at junction temperature higher than 125°C under such condition and no lifetime is guaranteed.

TYPICAL PERFORMANCE CHARACTERISTICS

T=25°C unless specified.

Line Regulation



Dropout Voltage





Thermal performance with OTP





PACKAGE OUTLINE







0	Dimensions Ir	n Millimeters	Dimensions	In Inches	
SYMDOI	Min	Max	Min	Max	
А	1.520	1.800	0.060	0.071	
A1	0.000	0.100	0.000	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.820	0.026	0.032	
С	0.250	0.350	0.010	0.014	
D	6.200	6.400	0.244	0.252	
D1	2.900	3.100	0.114	0.122	
E	3.300	3.700	0.130	0.146	
E1	6.830	7.070	0.269	0.278	
е	2.300	(BSC)	0.091(E	BSC)	
e1	4.500	4.700	0.177	0.185	
	0.900	1.150	0.035	0.045	
θ	0°	10°	0°	10°	





TO-252-2L PACKAGE OUTLINE DIMENSIONS

Cumhal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 REF.		0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600	REF.	0.063	REF.
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350	REF.	0.211 REF.	



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