

## Bias Resistor Transistor

### PNP Silicon Surface Mount Transistor with Monolithic Bias Resistor Network

- Applications

Inverter, Interface, Driver

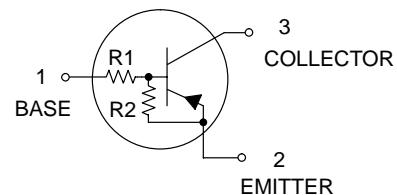
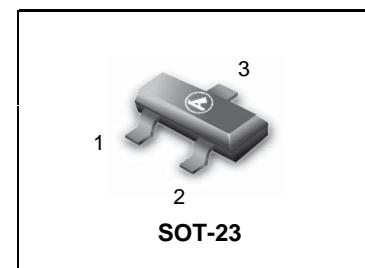
- Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
  - 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
  - 3) Only the on/off conditions need to be set for operation, making the device design easy.
- We declare that the material of product compliance with RoHS requirements.
  - S - Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

- **Absolute maximum ratings (Ta=25°C)**

Parameter	Symbol	Limits		Unit
Supply voltage	V <sub>cc</sub>	-50		V
Input voltage	V <sub>in</sub>	-10 to +5		V
Output current	I <sub>c</sub>	-500		mA
Power dissipation	P <sub>d</sub>	200		mW
Junction temperature	T <sub>j</sub>	150		°C
Storage temperature	T <sub>stg</sub>	-55 to +150		°C

**LDTB113ZLT1G  
S-LDTB113ZLT1G**



#### DEVICE MARKING AND RESISTOR VALUES

Device	Marking	R1 (K)	R2 (K)	Shipping
LDTB113ZLT1G S-LDTB113ZLT1G	K8	1	10	3000/Tape & Reel
LDTB113ZLT3G S-LDTB113ZLT3G	K8	1	10	10000/Tape & Reel

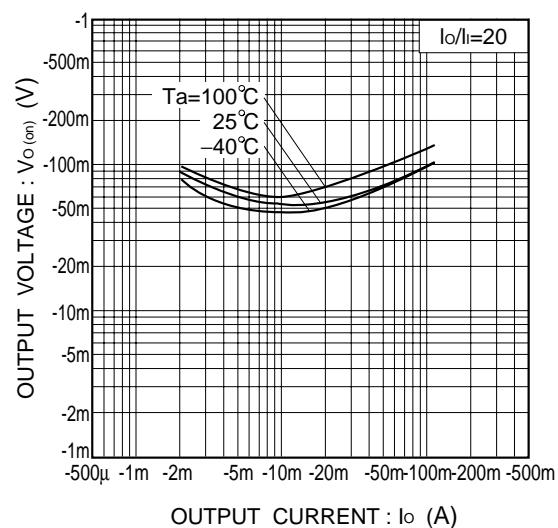
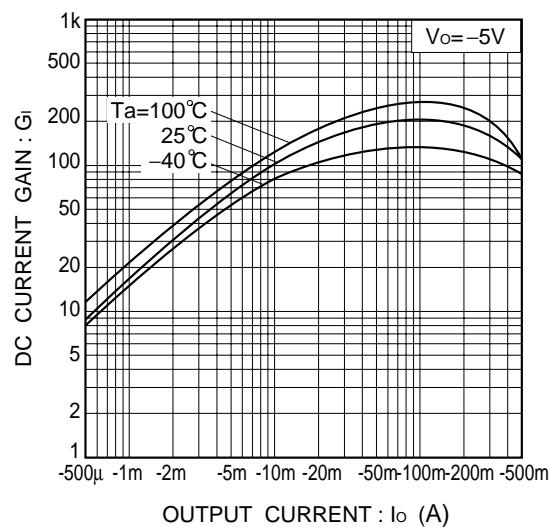
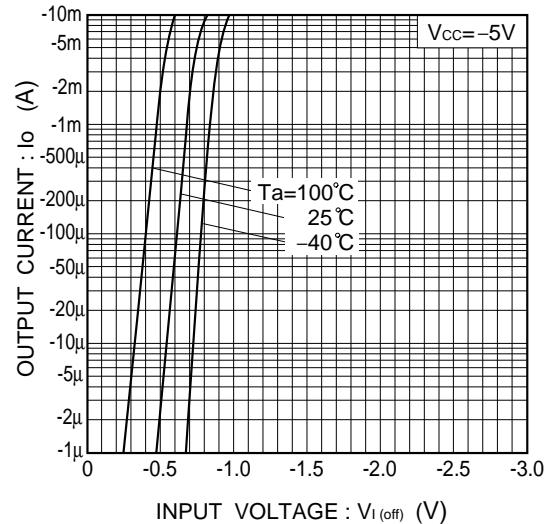
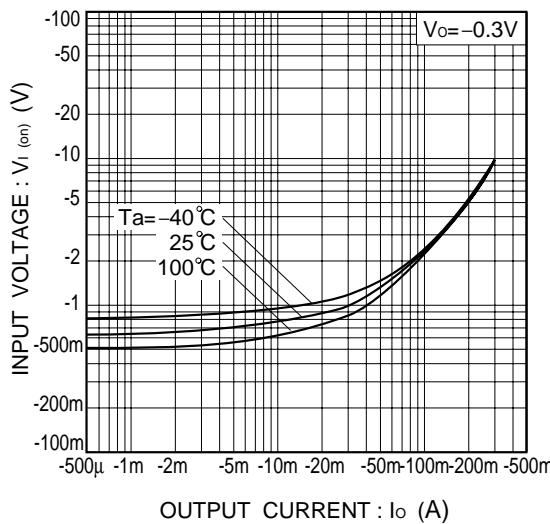
- **Electrical characteristics (Ta=25°C)**

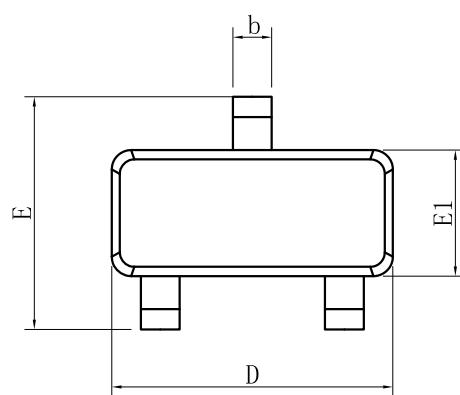
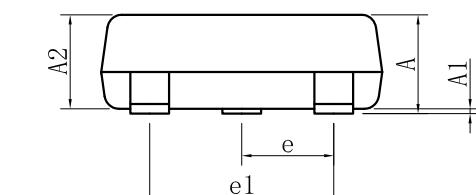
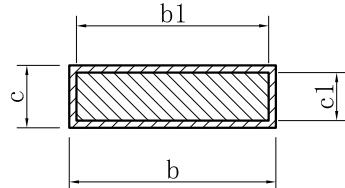
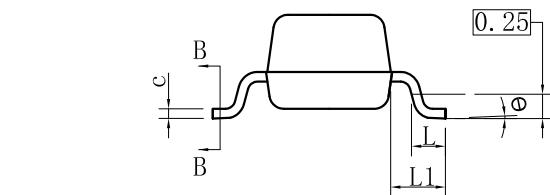
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>i(off)</sub>	—	—	-0.3	V	V <sub>cc</sub> =-5V, I <sub>o</sub> =-100μA
	V <sub>i(on)</sub>	-3	—	—		V <sub>o</sub> =-0.3V, I <sub>o</sub> =-20mA
Output voltage	V <sub>o(on)</sub>	—	—	-0.3	V	I <sub>o</sub> /I <sub>l</sub> =-50mA/-2.5mA
Input current	I <sub>i</sub>	—	—	-7.2	mA	V <sub>i</sub> =-5V
Output current	I <sub>o(off)</sub>	—	—	-0.5	μA	V <sub>cc</sub> =-50V, V <sub>i</sub> =0V
DC current gain	G <sub>i</sub>	56	—	—	—	V <sub>o</sub> =-5V, I <sub>o</sub> =-50mA
Input resistance	R <sub>i</sub>	0.7	1	1.3	kΩ	—
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	8	10	12	—	—
Transition frequency	f <sub>t</sub>	—	200	—	MHz	V <sub>ce</sub> =-10V, I <sub>e</sub> =50mA, f=100MHz *

\* Transition frequency of the device

## LDTB113ZLT1G;S-LDTB113ZLT1G

### ●Electrical characteristic curves

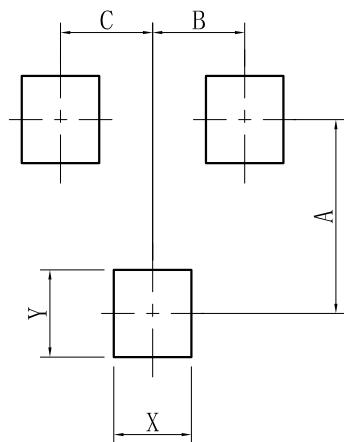


**LDTB113ZLT1G;S-LDTB113ZLT1G**
**OUTLINE AND DIMENSIONS**


SOT23			
DIM	MIN	NOR	MAX
A	0.89	-	1.12
A1	0.01	-	0.10
A2	0.88	0.95	1.02
b	0.30	-	0.50
b1	0.30	0.40	0.45
c	0.08	-	0.20
c1	0.08	0.10	0.16
D	2.80	2.90	3.04
E	2.10	-	2.64
E1	1.20	1.30	1.40
e	0.95BSC		
e1	1.90BSC		
L	0.40	0.46	0.60
L1	0.54REF		
$\theta$	0°	-	8°
All Dimensions in mm			

**GENERAL NOTES**

1. Top package surface finish  $Ra0.4 \pm 0.2\mu m$
2. Bottom package surface finish  $Ra0.7 \pm 0.2\mu m$
3. Side package surface finish  $Ra0.4 \pm 0.2\mu m$

**SOLDERING FOOTPRINT**


SOT-23	
DIM	(mm)
X	0.80
Y	0.90
A	2.00
B	0.95
C	0.95



**LESHAN RADIO COMPANY, LTD.**

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## **DISCLAIMER**

- Curve guarantee in the specification. The curve of test items with electric parameter is used as quality guarantee. The curve of test items without electric parameter is used as reference only.
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