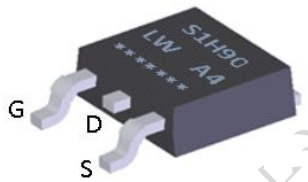
V_{DSS}	-100	V
I_D	-25	A
P_D	80	W
$R_{DS(ON)\ TYPE}$	72	mΩ

General Description:

The LWS1H90A4 uses advanced SGT technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications. The package form is TO-252, which accords with the ROHS standard and Halogen Free standard.

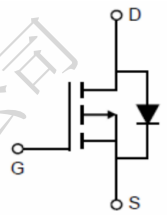
Marking and Pin Assignment



Features:

- Fast Switching
- Low Gate Charge and $R_{DS(ON)}$
- Low Reverse transfer capacitances

Inner Equivalent Principium Chart



Applications:

- DC-DC Converter
- Portable Equipment
- Power Management

100% DVDS Tested

100% Avalanche Tested

RoHS HF

Package Marking and Ordering Information:

Marking	Part Number	Package	Packing	Qty.
S1H90/LW A4/D.C.	LWS1H90A4	TO-252	Reel	2500 Pcs

Absolute Maximum Ratings:

Symbol	Parameter		Value	Units
V_{DSS}	Drain-to-Source Voltage		-100	V
I_D	Continuous Drain Current	T_C=25℃	-25	A
	Continuous Drain Current	T_C=100℃	-15.8	A
I_{DM} [a1]	Pulsed Drain Current		-100	A
E_{AS} [a2]	Single pulse avalanche energy		200	mJ
V_{GS}	Gate-to-Source Voltage		±20	V
P_D	Power Dissipation		80	W
T_J, T_{STG}	Operating Junction and Storage Temperature Range		150, -55 to 150	℃
T_L	Maximum Temperature for Soldering		260	℃

Thermal Characteristics:

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.56	℃/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	91	℃/W

Electrical Characteristic (T_C = 25 °C, unless otherwise specified)**:**

Static Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
V_{DSS}	Drain to Source Breakdown Voltage	V_{GS}=0V, I_D=-250μA	-100	--	--	V
I_{DSS}	Drain to Source Leakage Current	V_{DS}=-100V, V_{GS}=0V	--	--	1.0	μA
$I_{GSS(F)}$	Gate to Source Forward Leakage	V_{GS}=-20V, V_{DS}=0V	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	V_{GS}=+20V, V_{DS}=0V	--	--	-100	nA
$V_{GS(TH)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250μA	-1.5	-2	-2.5	V
$R_{DS(ON)1}$	Drain-to-Source On-Resistance	V_{GS}=-10V, I_D=-10A	--	72	89	mΩ
$R_{DS(ON)2}$	Drain-to-Source On-Resistance	V_{GS}=-4.5V, I_D=-5.0A	--	87	105	mΩ

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} =-50V f = 1.0MHz	--	1305	--	pF
C_{oss}	Output Capacitance		--	97.5	--	
C_{rss}	Reverse Transfer Capacitance		--	9.1	--	
R_g	Gate resistance	V_{GS} = 0V, V_{DS} Open	--	75	--	Ω

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
$t_{d(ON)}$	Turn-on Delay Time	I_D =-10A V_{DS} =-50V V_{GS} =-10V R_G =5.0Ω	--	8.8	--	ns
t_r	Rise Time		--	16	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	60	--	
t_f	Fall Time		--	41	--	
Q_g	Total Gate Charge	V_{GS} =-10V V_{DS} = -50V I_D =-10A	--	23.27	--	nC
Q_{gs}	Gate Source Charge		--	4.59	--	
Q_{gd}	Gate Drain Charge		--	4.53	--	

Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
I_S	Diode Forward Current	T_C =25 °C	--	--	-25	A
V_{SD}	Diode Forward Voltage	I_S=-10A, V_{GS}=0V	--	--	-1.2	V
t_{rr}	Reverse Recovery time	IS=-10A, VDD=-50V dI/dt=100A/μs	--	30	--	ns
Q_{rr}	Reverse Recovery Charge		--	61	--	nC

a1：Repetitive rating; pulse width limited by maximum junction temperature

a2：V_{DD}=-50V,L=5.0mH, R_g=25Ω, Starting T_J=25 °C

Characteristics Curve:

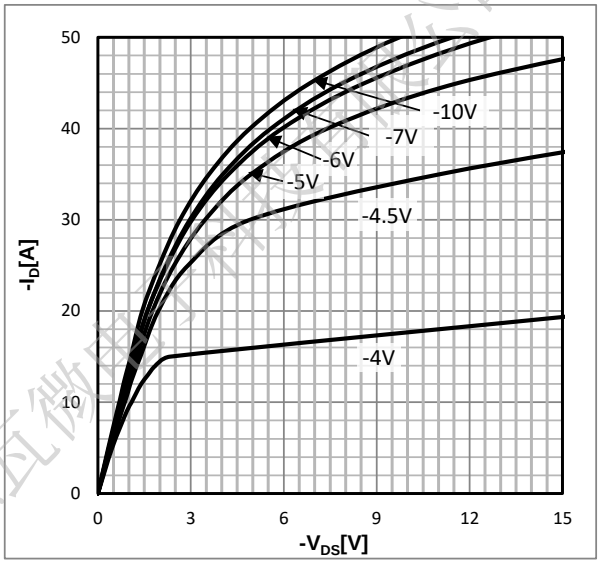
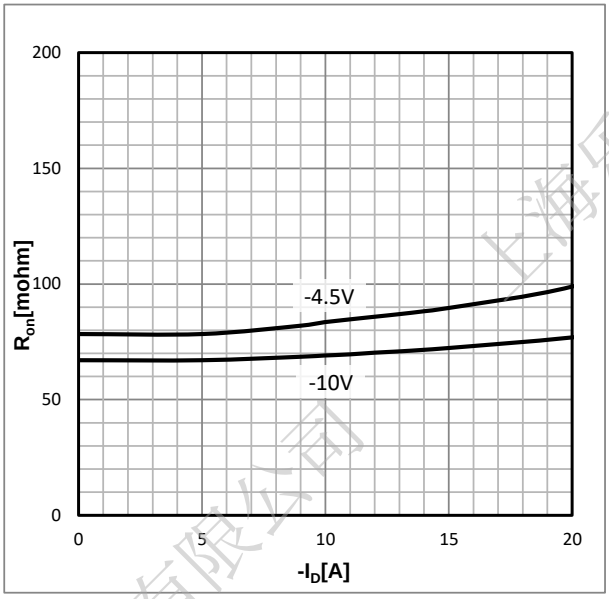
Figure 1: Typ. output characteristics	Figure 2: Typ. drain-source on resistance
	
$I_D=f(V_{DS})$, T_j=25 °C; parameter: V_{GS}	$R_{DS(on)}=f(I_D)$, T_j=25 °C; parameter: V_{GS}

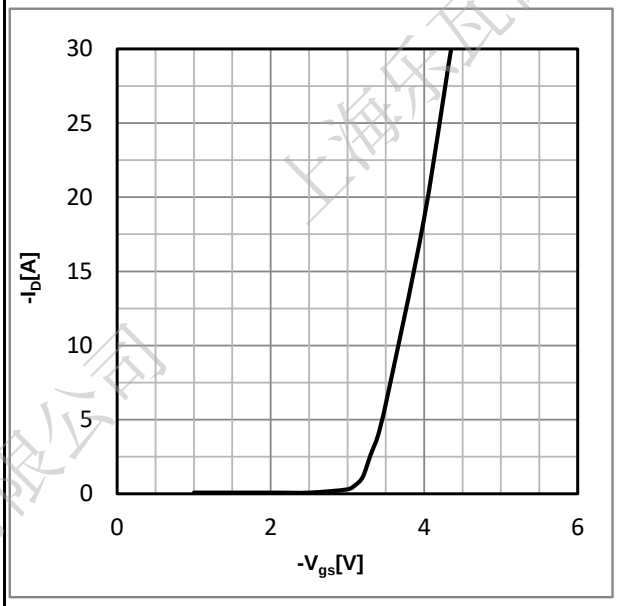
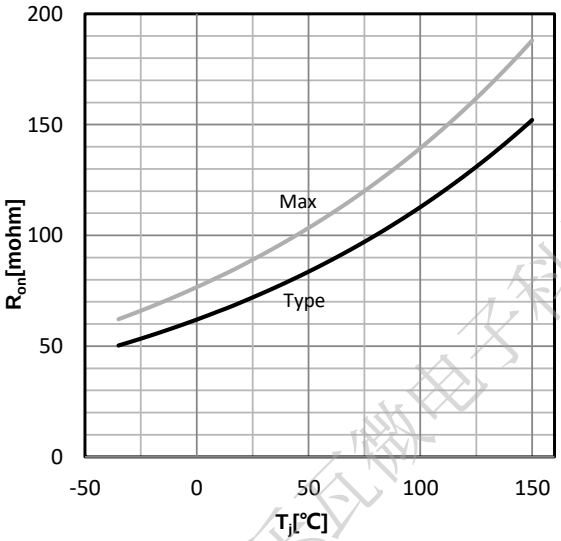
Figure 3: Typ. transfer characteristics	Figure 4: drain-source on resistance
	
$I_D=f(V_{GS})$, $\lvert V_{DS}\rvert>2\lvert I_D\rvert R_{DS}(on)max$;	$R_{DS(on)}=f(T_j)$, I_D=-10A, V_{GS}=-10V;

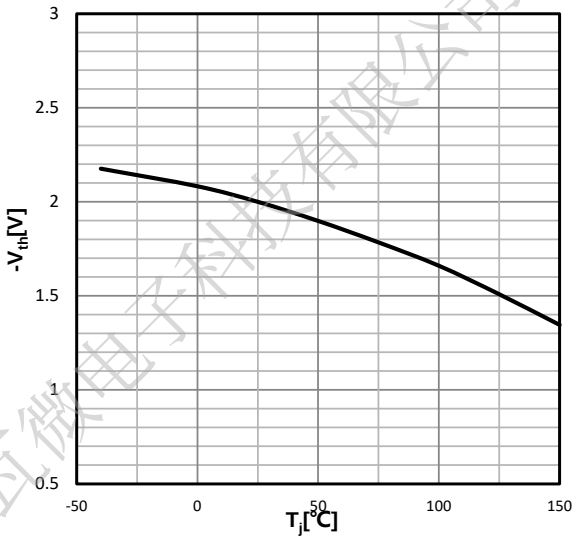
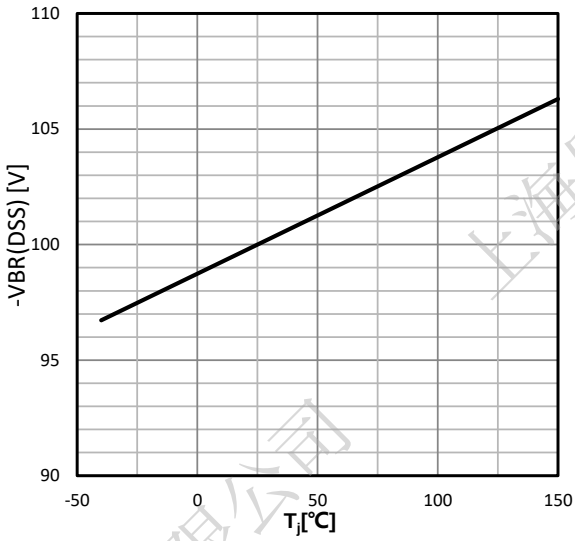
Figure 5: Typ. gate threshold voltage	Figure 6: Drain-source breakdown voltage
	
V_{GS}=f(T_j),I V_{GS}=$V_{DS,}I_D$=-250μA;	$V_{BR(DSS)}$=f(T_j); I_D=-250μA;

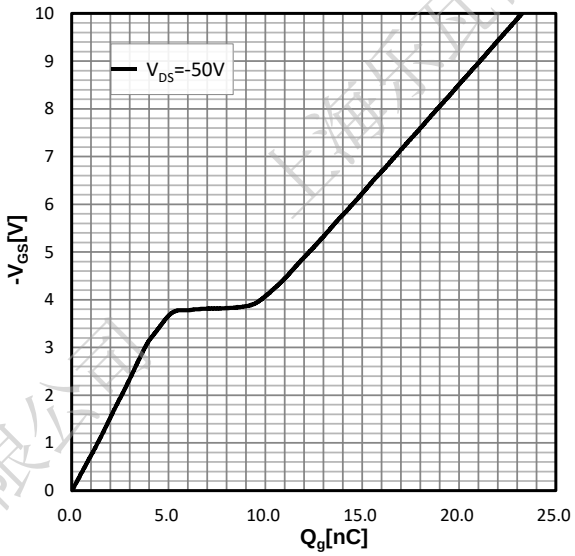
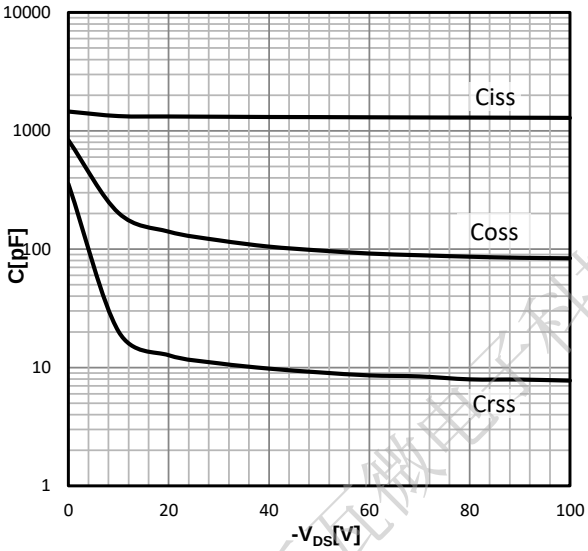
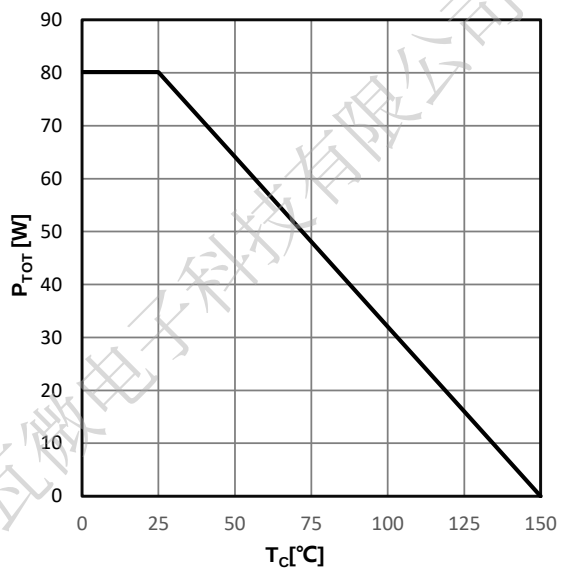
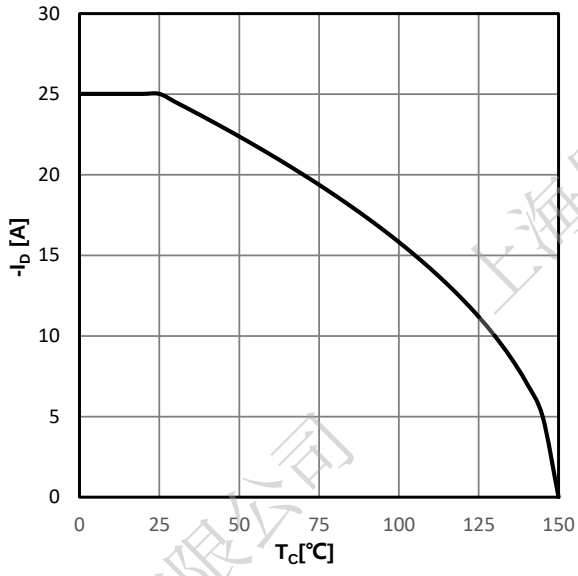
Figure 7: Typ. gate charge	Figure 8: Typ. Capacitances
	
V_{GS}=f(Q_g), I_D=-10A, T_j=25 °C; parameter: V_{DS}	C=f(V_{DS}); V_{GS}=0V; f=1.0 MHz;

Figure 9: Power dissipation



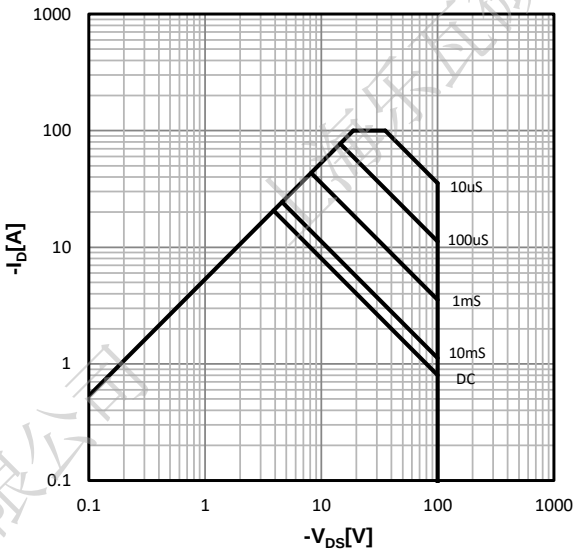
$P_{tot}=f(T_C)$;

Figure 10:Drain current



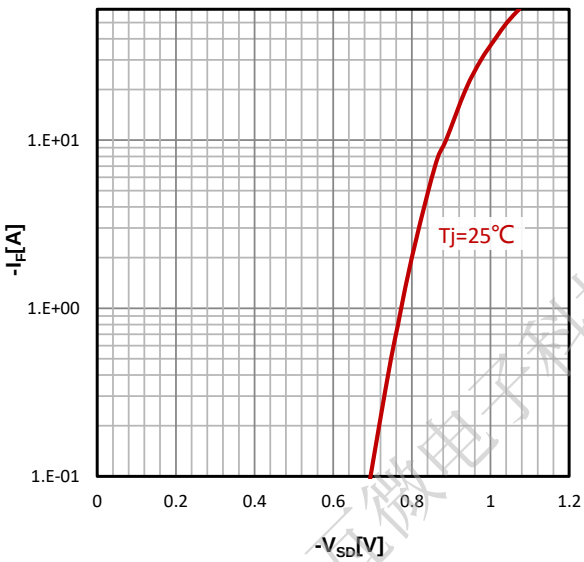
$I_D=f(T_C)$;

Figure 11: Safe operating area



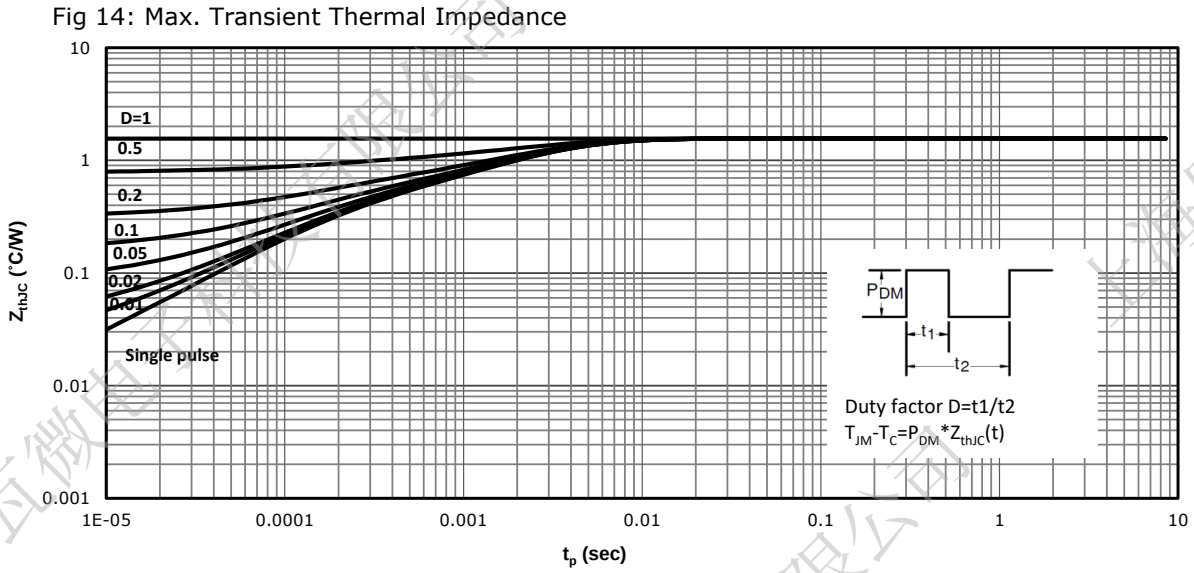
$I_D=f(V_{DS})$; T_C=25 °C; D=0; parameter: tp

Figure 12: Typ. forward characteristics



$I_F=f(V_{SD})$;

Figure 13: Max. Transient Thermal Impedance



$Z_{thJC}=f(t_p)$; parameter: D

Test Circuit & Waveform:

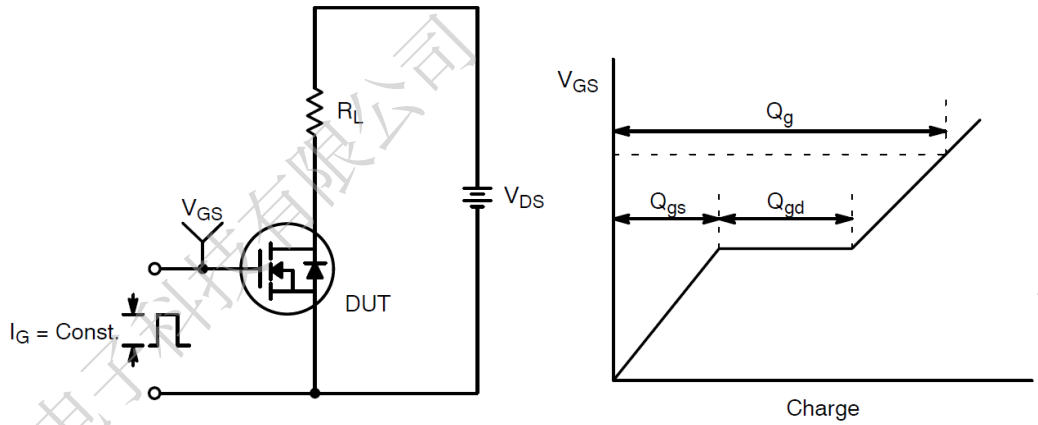


Figure 14: Gate Charge Test Circuit & Waveform

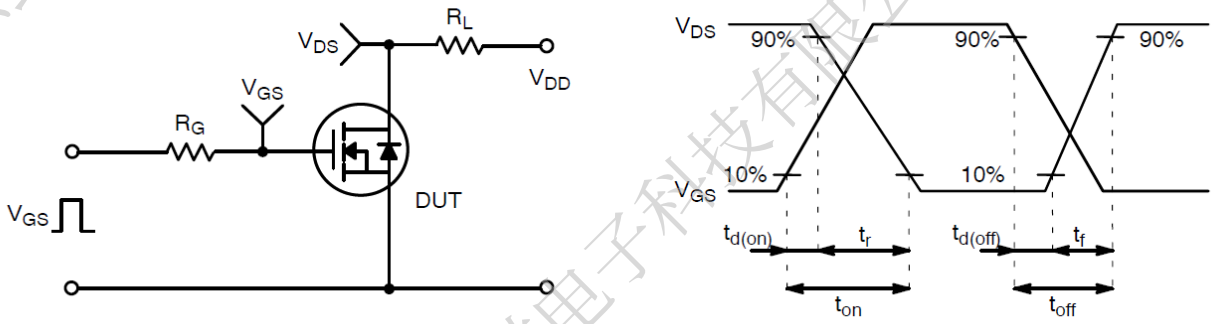


Figure 15: Resistive Switching Test Circuit & Waveforms

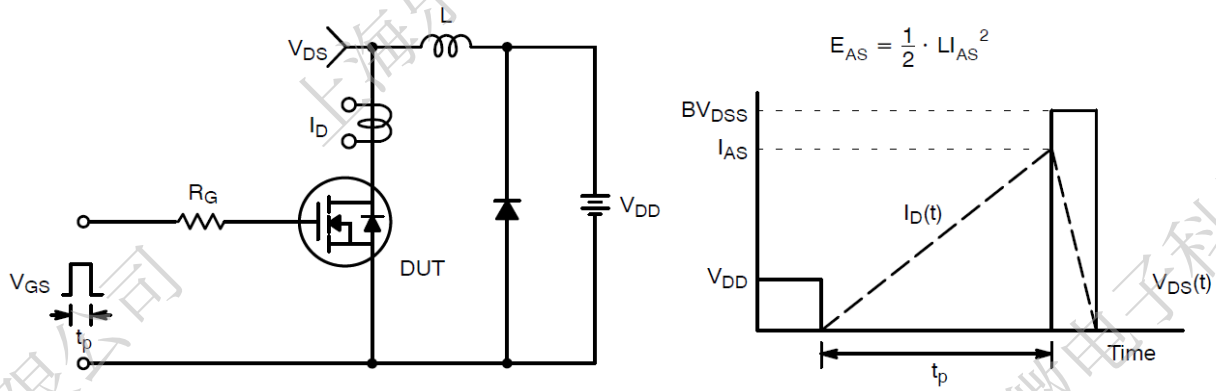
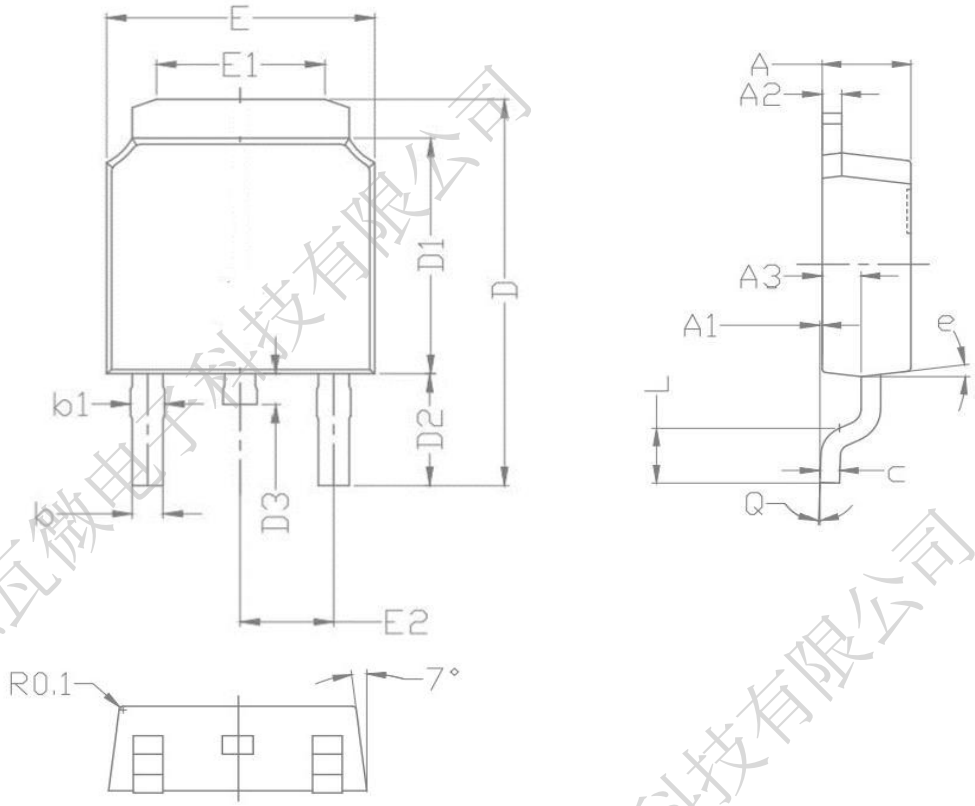


Figure 16: Unclamped Inductive Switching Test Circuit & Waveforms

Package Outline:



COMMON			
PKG	**TO-252-2L**		
Symbol	**MIN**	**NOM**	**MAX**
A	2.200	2.300	2.400
A1	0.000	0.075	0.150
A2	0.500	0.508	0.550
A3	0.960	1.010	1.060
b	0.740	0.760	0.800
b1	0.880	0.900	0.950
C	0.500	0.508	0.550
D	9.800	10.025	10.350
D1	6.050	6.100	6.180
D2	2.850	2.900	2.950
D3	0.600	0.800	1.000
E	6.550	6.600	6.700
E1	4.050	4.130	4.200
E2	2.250	2.286	2.300
L	1.400	1.500	1.600
e	7°		
Q	0°	2°	5°

Revision History:

Revison	Date	Descriptions
Rev 1.0	Feb.2023	Initial Version

Disclaimer:

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