



Reverse Voltage - 20 to 200V
Forward Current - 5.0A

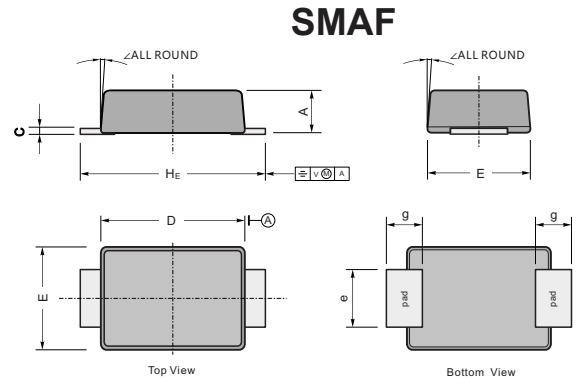


FEATURES

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

MECHANICAL DATA

- Case: SMAF
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 27mg / 0.00095oz



UNIT		A	C	D	E	e	g	He	∠
mm	max	1.2	0.20	3.7	2.7	1.6	1.2	4.9	7°
	min	0.9	0.12	3.3	2.4	1.3	0.8	4.4	
mil	max	47	7.9	146	106	63	47	193	
	min	35	4.7	130	94	51	31	173	

Absolute Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

Parameter	Symbols	SS52AFL	SS54AFL	SS56AFL	SS58AFL	SS510AFL	SS512AFL	SS515AFL	SS520AFL	Units	
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	20	40	60	80	100	120	150	200	V	
Maximum RMS voltage	V_{RMS}	14	28	42	56	70	84	105	140	V	
Maximum DC Blocking Voltage	V_{DC}	20	40	60	80	100	120	150	200	V	
Maximum Average Forward Rectified Current	$I_{F(AV)}$	5.0								A	
Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	150								A	
Max Instantaneous Forward Voltage at 5 A	V_F	0.45	0.55	0.75	0.85					V	
Maximum DC Reverse Current $T_a = 25^\circ\text{C}$ at Rated DC Reverse Voltage $T_a = 100^\circ\text{C}$	I_R	1.0				50				mA	
Typical Junction Capacitance ⁽¹⁾	C_j	500	300								pF
Typical Thermal Resistance ⁽²⁾	$R_{\theta JA}$	60								°C/W	
Operating Junction Temperature Range	T_j	-55 ~ +150								°C	
Storage Temperature Range	T_{stg}	-55 ~ +150								°C	

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C

(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

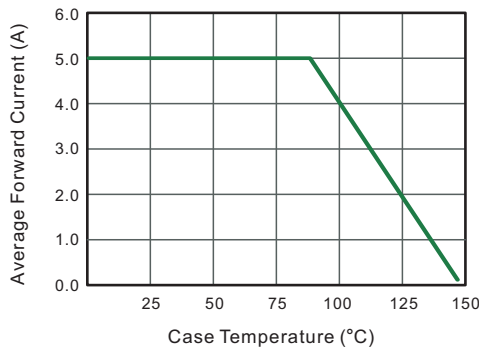


Fig.2 Typical Reverse Characteristics

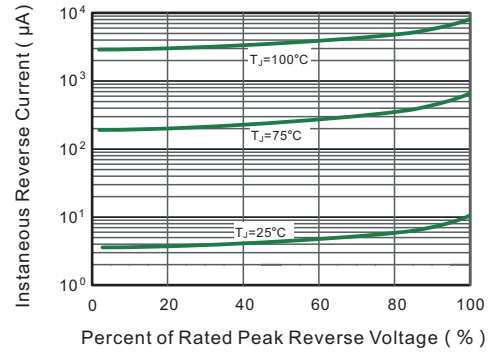


Fig.3 Typical Forward Characteristic

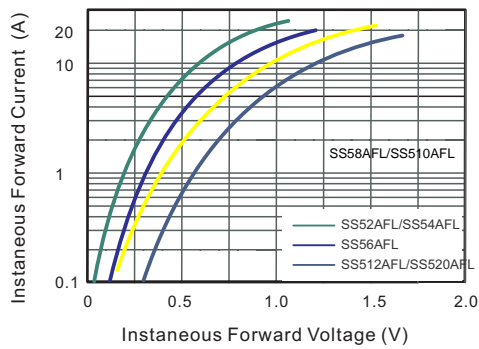


Fig.4 Typical Junction Capacitance

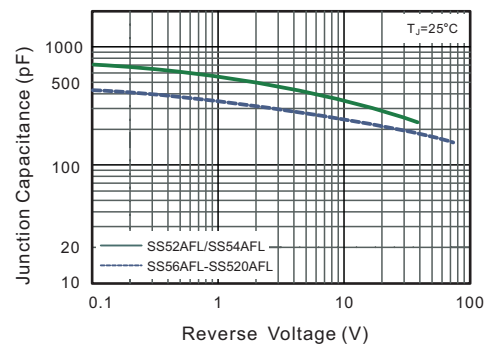


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

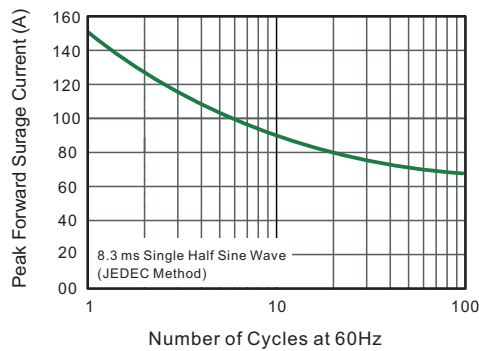
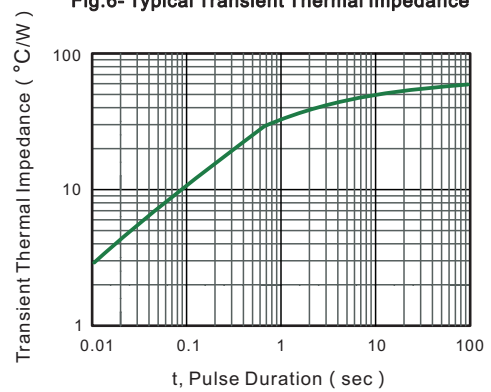
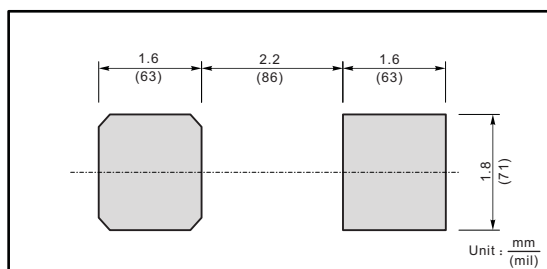


Fig.6- Typical Transient Thermal Impedance



The recommended mounting pad size



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