	Ŧ		2	3	4		5		6			7	8	9	
G					2.0xNpin+7.34±0.3						Recommended P.C.B Layout(Top Side) _G (PCB BOARD TOLERANCE±0.05)				
F	Rated Contac Withsta Insulatio Operati	nd Voltag on Resis on Temp t Materia	nce:20mΩ Max ge:400V AC/DC tance:800MΩ M erature:—40°c l:Brass) lin to +105°c		-2±0.10					15-▲	ø1.2	2.0		F
E	Insulato Standa	or Materio rd:	Au or Sn Over al:Polyester(ULS PA6T Temp: 230°C (260°C	S	—2.0xN	pin+6.34	±0.3—			55+015		—2.0x(N-1)PIN	-2±0.1	E	
D									+0.15	0.15	0.15			H±0.25	D
С						-2-ø1.0	╽╷╽╽╷╽ ╖╷╷╷╷╷╷╷ ॼ─ॼ─ॼ ├ ╼─	IJIJ IJIJIJ ╼─SQ	╢╷╢╷╷ ╘╝╵╘╝╵╺		5.65±0.15		0.25 ₩±0.25		с
В					2*NPIN-4±0.1 2.0xNo.of Positions±0.30							Dimension antitheses listITEMDWPBLAlternate0.506.5016.9018.00			
						OPERATION	DRAW			SCALE		深圳市	可连盛精密连接错	器有限公司	$\left - \right $
A	AO	2012.03.28	NEW DRAV	VING		X.XX ±0.2 X.XXX ±0.1 Angle ±3*	0 СНЕСК 0			SIZE SHEET		PART NO.	BH-000		
	REV 1	DATE	MODIFICATION		CHANGE	DIM TOI				PROJ.	♦母	TITLE:	SMT=6.5 总高16.9	9	
	1		2	3	4		5		6			/	8	9	

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