

# Micropower Op Amp, Comparator and Reference

# **FEATURES**

- Quiescent Current: 5µA Typ
   Outputs Swing Rail-to-Rail
- Low Op Amp Offset Voltage: 700µV Max
- Reference Output Drives 0.01µF Capacitor
- Internal 1.2V ±0.4% Reference Output (LTC1541)
- Low Input Bias Current: 1nA Max
- Reference Output Can Source Up to 2mA
- Internal ±2.25mV Comparator Hysteresis
- Comparator and Op Amp Input Range Includes Ground
- Op Amp Capable of Driving Up to 1000pF Load
- Unity-Gain Stable and 12kHz Bandwidth
- 2.5V to 12.6V Supply Voltage Range
- Pin Compatible Upgrade for MAX951/MAX953
- Available in 3mm x 3mm x 0.8mm DFN Package

# **APPLICATIONS**

- Battery- or Solar-Powered Systems
- Automotive Keyless Entry
- Low Frequency, Local Area Alarms/Detectors
- Infrared Receivers for Remote Controls
- Smoke Detectors and Safety Sensors
- GSM Portable Phones

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### DESCRIPTION

The LTC®1541/LTC1542 combine a micropower amplifier, comparator and bandgap reference (LTC1541) in an 8-pin package. The part operates from a single 2.5V to 12.6V or dual  $\pm 1.25$ V to  $\pm 6.3$ V supply with a typical supply current of 5µA. Both the op amp and comparator feature a common mode input voltage range that extends from the negative supply to within 1.3V of the positive supply. The op amp output stage swings from rail-to-rail. The comparator's inverting input is internally connected to the reference output (LTC1541).

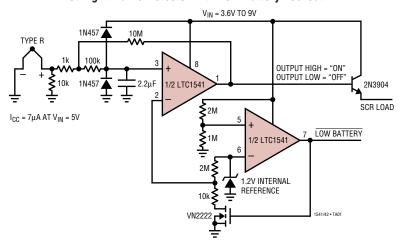
The reference output voltage is  $1.2V \pm 1\%$  over the extended temperature range. The output can drive a bypass capacitor of up to  $0.01\mu F$  without any oscillations. It can also source up to 2mA and sink up to  $20\mu A$ .

The op amp is internally compensated to be unity-gain stable with typical GBW at 12kHz and slew rate of 8V/ms. The comparator has  $\pm 2.25$ mV of internal hysteresis to ensure clean output switching, even with slow moving input signals.

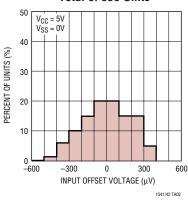
The LTC1541/LTC1542 are available in MSOP and SO-8 packages. For space-limited applications, the LTC1541/LTC1542 are available in a 3mm x 3mm low profile (0.8mm) dual fine-pitch leadless package (DFN).

# TYPICAL APPLICATION

#### Pilot Light Flame Detector with Low-Battery Lockout



#### Op Amp V<sub>OS</sub> Distribution, V<sub>CM</sub> = 2.5V, SO-8 Package Total of 839 Units

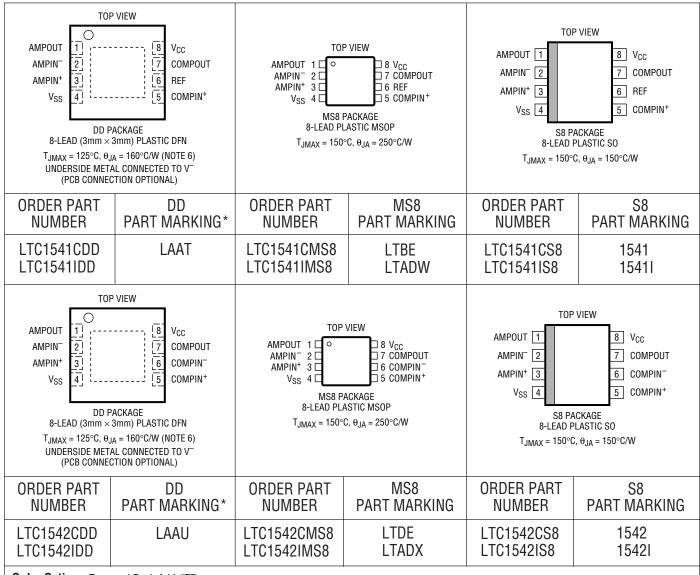




# **ABSOLUTE MAXIMUM RATINGS** (Note 1)

13V
0.3V to 13V
0.3V to 13V
Indefinite
65°C to 150°C
65°C to 125°C

# PACKAGE/ORDER INFORMATION



Order Options Tape and Reel: Add #TR

Lead Free: Add #PBF Lead Free Tape and Reel: Add #TRPBF Lead Free Part Marking: http://www.linear.com/leadfree/

Consult LTC Marketing for parts specified with wider operating temperature ranges.



<sup>\*</sup>Temperature grades are identified by a label on the shipping container.

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SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Power Supp	oly						
V <sub>CC</sub>	Supply Voltage Range		•	2.5		12.6	V
I <sub>CC</sub>	Supply Current (Note 2)				5	10	μΑ
		Commercial Grade	•			16	μΑ
		Industrial Grade	•			17	μΑ
Comparator	1						
$V_{OS}$	Input Offset Voltage (Note 3)	(SO-8)				1	mV
		Commercial Grade (SO-8)	•			1.5	mV
		Industrial Grade (SO-8)	•			2.0	mV
		(MSOP)(DFN) Commercial Grade (MSOP)(DFN)				1.2 2.0	mV mV
		Industrial Grade (MSOP)(DFN)				2.5	mV
$V_{TRIP}$	Trip Point (Note 4)			1.7	2.25	2.8	mV
- 11111	· · · · · · · · · · · · · · · · · · ·		•	1		3.8	mV
I <sub>IN</sub>	Input Leakage Current (Note 5)		•		0.01	1	nA
V <sub>CM</sub>	Input Common Mode Range		•	V <sub>SS</sub>		V <sub>CC</sub> – 1.3V	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = V_{SS}$ to $(V_{CC} - 1.3V)$ (LTC1542)				0.7	mV/V
		Commercial Grade	•			8.0	mV/V
		Industrial Grade	•			0.9	mV/V
PSRR	Power Supply Rejection Ratio	$V_{CC} = 3V \text{ to } 12V$				0.25	mV/V
		Commercial Grade (SO-8)	•			0.30 0.35	mV/V
	Duana action Dalou	Industrial Grade (SO-8)	•		00	0.33	mV/V
t <sub>PD</sub>	Propagation Delay	Overdrive = 10mV Overdrive = 100mV			20 8		μS μS
V <sub>OH</sub>	Output High Voltage	I <sub>OUT</sub> = -2mA	•	V <sub>CC</sub> - 0.2V			V
V <sub>OL</sub>	Output Low Voltage	I <sub>OUT</sub> = 1.8mA	•	100 0:21		V <sub>SS</sub> + 0.3V	
	(LTC1541 Only)	.001				133 . 0.01	
V <sub>REF</sub>	Reference Voltage	No Load (SO-8)		1.195	1.200	1.205	V
* KEF	Troibronos voitago	Commercial Grade (SO-8)	•	1.190	1.200	1.210	V
		Industrial Grade (SO-8)	•	1.188		1.212	V
		No Load (MSOP)		1.190	1.200	1.210	V
		Commercial Grade (MSOP)	•	1.185		1.215	V
		Industrial Grade (MSOP)	•	1.183		1.217	V
		No Load (DFN)		1.185	1.200		V
		Commercial Grade (DFN) Industrial Grade (DFN)		1.180 1.178		1.220 1.222	V
$\Delta V_{REF}$	Load Regulation	I <sub>SOURCE</sub> = 2mA		1.170	2	6	mV
ΔVREF	Ludu negulation	I <sub>SINK</sub> = 10μA	•		0.2	1.5	mV
		COUNTY 1 SPEC	•			5	mV
e <sub>n</sub>	Voltage Noise	0.1Hz to 10Hz			16		μV <sub>P-P</sub>
Op Amp				•			
V <sub>OS</sub>	Input Offset Voltage	V <sub>CM</sub> = 2.5V (SO-8)				0.7	mV
		Commercial Grade (SO-8)	•			1.25	mV
		Industrial Grade (SO-8)	•			1.65	mV
		V <sub>CM</sub> = 2.5V (MSOP) (DFN)				1.00	mV
		Commercial Grade (MSOP) (DFN)	•			1.75	mV
		Industrial Grade (MSOP) (DFN)				2.0	mV



# **ELECTRICAL CHARACTERISTICS** The $\bullet$ denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$ . $V_{CC} = 5V$ , $V_{SS} = 0V$ , unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
I <sub>B</sub>	Input Bias Current	V <sub>CM</sub> = 2.5V	•		0.01	1	nA
A <sub>VOL</sub>	Large-Signal Gain	AMPOUT = 0.5V to 4.5V, No Load AMPOUT = 0.5V to 4.5V, R <sub>LOAD</sub> = 100k Commercial Grade Industrial Grade	•	80 60 38 35	1000 500		V/mV V/mV V/mV V/mV
GBW	Gain Bandwidth	$A_V = 1V/V$			12		kHz
SR	Slew Rate	$A_V = 1V/V$			8		V/ms
$V_{CM}$	Input Common Mode Range		•	V <sub>SS</sub>		$V_{\rm CC}-1.3V$	V
CMRR	Common Mode Rejection Ratio	V <sub>CM</sub> = V <sub>SS</sub> to (V <sub>CC</sub> – 1.3V) Commercial Grade Industrial Grade	•			0.5 0.6 0.6	mV/V mV/V mV/V
PSRR	Power Supply Rejection Ratio	V <sub>CC</sub> = 3V to 12V Commercial Grade Industrial Grade	•			0.19 0.21 0.23	mV/V mV/V mV/V
V <sub>OH</sub>	Output High Voltage	R <sub>LOAD</sub> = 100k to V <sub>SS</sub> Commercial Grade Industrial Grade	•	$\begin{array}{c} V_{CC} - 0.07 \\ V_{CC} - 0.10 \\ V_{CC} - 0.12 \end{array}$			V V V
V <sub>OL</sub>	Output Low Voltage	R <sub>LOAD</sub> = 100k to V <sub>CC</sub> Commercial Grade Industrial Grade	•			V <sub>SS</sub> + 0.05 V <sub>SS</sub> + 0.10 V <sub>SS</sub> + 0.12	V V V
I <sub>SOURCE</sub>	Output Source Current		•	0.9 0.7	1.8		mA mA
I <sub>SINK</sub>	Output Sink Current		•	1.3 0.9	1.8		mA mA
e <sub>n</sub>	Input Noise Voltage	f <sub>0</sub> = 0.1Hz to 10Hz			3		μV <sub>P-P</sub>

The ullet denotes specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^{\circ}C$ .  $V_{CC} = 3V$ ,  $V_{SS} = 0V$ , unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Power Supp	ply		•	1			
V <sub>CC</sub>	Supply Voltage Range		•	2.5		12.6	V
I <sub>CC</sub>	Supply Current (Note 2)				4.5	9.5	μА
		Commercial Grade	•			15	μΑ
		Industrial Grade	•			16	μΑ
Comparato	1						
V <sub>OS</sub>	Input Offset Voltage (Note 3)	(SO-8)				1.0	mV
		Commercial Grade (SO-8)	•			1.5	mV
		Industrial Grade (SO-8)	•			2.0	mV
		(MSOP)(DFN)				1.2	mV
		Commercial Grade (MSOP)(DFN)	•			2.0	mV
		Industrial Grade (MSOP)(DFN)	•			2.5	mV
V <sub>TRIP</sub>	Trip Point (Note 4)			1.80	2.35	2.90	mV
			•	1.00		3.90	mV
I <sub>IN</sub>	Input Leakage Current (Note 5)		•		0.01	1	nA
V <sub>CM</sub>	Input Common Mode Range		•	V <sub>SS</sub>		$V_{CC} - 1.3$	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = V_{SS}$ to $(V_{CC} - 1.3V)$ (LTC1542)				0.8	mV/V
		Commercial Grade	•			0.9	mV/V
		Industrial Grade	•			1.0	mV/V
			'				15412fd



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# **ELECTRICAL CHARACTERISTICS** The ullet denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$ . $V_{CC} = 3V$ , $V_{SS} = 0V$ , unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
PSRR	Power Supply Rejection Ratio	V <sub>CC</sub> = 3V to 12V Commercial Grade Industrial Grade	•			0.25 0.30 0.35	mV/V mV/V mV/V
t <sub>PD</sub>	Propagation Delay	Overdrive = 10mV Overdrive = 100mV			25 12		μs μs
V <sub>OH</sub>	Output High Voltage	I <sub>OUT</sub> = -2mA	•	V <sub>CC</sub> - 0.2			V
$\overline{V_{0L}}$	Output Low Voltage	I <sub>OUT</sub> = 1.8mA	•			V <sub>SS</sub> + 0.3	V
Reference	(LTC1541 Only)		'				
V <sub>REF</sub>	Reference Voltage	No Load (SO-8) Commercial Grade (SO-8) Industrial Grade (SO-8)	•	1.195 1.190 1.188	1.200	1.205 1.210 1.212	V V V
		No Load (MSOP) Commercial Grade (MSOP) Industrial Grade (MSOP)	•	1.190 1.185 1.183	1.200	1.210 1.215 1.217	V V V
		No Load (DFN) Commercial Grade (DFN) Industrial Grade (DFN)	•	1.185 1.180 1.178	1.200	1.215 1.220 1.222	V V
$\Delta V_{REF}$	Load Regulation	$I_{SOURCE} = 1 \text{mA}$ $I_{SINK} = 10 \mu\text{A}$	•		2 0.2	6 1.5 5	mV mV mV
en	Voltage Noise	0.1Hz to 10Hz			16		μV <sub>P-P</sub>
Op Amp							
V <sub>OS</sub>	Input Offset Voltage	V <sub>CM</sub> = 1.5V (SO-8) Commercial Grade (SO-8) Industrial Grade (SO-8)	•			0.7 1.25 1.65	mV mV mV
		V <sub>CM</sub> = 1.5V (MSOP) Commercial Grade (MSOP) (DFN) Industrial Grade (MSOP) (DFN)	•			1.00 1.75 2.0	mV mV mV
I <sub>B</sub>	Input Bias Current	V <sub>CM</sub> = 1.5V	•		0.01	1	nA
A <sub>VOL</sub>	Large-Signal Gain	AMPOUT = 0.5V to 2.5V, No Load AMPOUT = 0.5V to 2.5V, R <sub>LOAD</sub> = 100k Commercial Grade Industrial Grade	•	80.0 45.5 22.0 20.0	1000 500		V/mV V/mV V/mV V/mV
GBW	Gain Bandwidth	$A_V = 1V/V$			12		kHz
SR	Slew Rate	$A_V = 1V/V$			8		V/ms
V <sub>CM</sub>	Input Common Mode Range		•	V <sub>SS</sub>		$V_{CC} - 1.3$	V
CMRR	Common Mode Rejection Ratio	V <sub>CM</sub> = V <sub>SS</sub> to (V <sub>CC</sub> – 1.3V) Commercial Grade Industrial Grade	•			0.7 0.8 0.8	mV/V mV/V mV/V
PSRR	Power Supply Rejection Ratio	V <sub>CC</sub> = 3V to 12V Commercial Grade Industrial Grade	•			0.19 0.21 0.23	mV/V mV/V mV/V
V <sub>OH</sub>	Output High Voltage	R <sub>LOAD</sub> = 100k to V <sub>SS</sub> Commercial Grade Industrial Grade	•	$V_{CC} - 0.07 \\ V_{CC} - 0.10 \\ V_{CC} - 0.12$			V V V
V <sub>OL</sub>	Output Low Voltage	R <sub>LOAD</sub> = 100k to V <sub>CC</sub> Commercial Grade Industrial Grade	•			V <sub>CC</sub> + 0.05 V <sub>CC</sub> + 0.10 V <sub>CC</sub> + 0.12	V V



# **ELECTRICAL CHARACTERISTICS** The • denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$ . $V_{CC} = 3V$ , $V_{SS} = 0V$ , unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
I <sub>SOURCE</sub>	Output Source Current			0.6	0.95		mA
			•	0.4			mA
I <sub>SINK</sub>	Output Sink Current			1.2	1.8		mA
			•	0.8			mA
e <sub>n</sub>	Input Noise Voltage	f <sub>0</sub> = 0.1Hz to 10Hz			3		μV <sub>P-P</sub>

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

**Note 2:** Supply current is tested with COMPIN<sup>+</sup> = (REF – 100mV) for LTC1541 and COMPIN<sup>+</sup> = 0V, COMPIN<sup>-</sup> = 100mV for LTC1542.

**Note 3:** Input offset voltage is defined as the center of the input referred hysteresis,  $V_{CM}$  = REF (LTC1541).  $V_{CM}$  = 1/2  $V_{CC}$  (LTC1542).

**Note 4:** Trip point is defined as the differential input voltage required to make the comparator output change state. The difference between upper and lower trip point is equal to the width of the input referred hysteresis,  $V_{CM} = REF$  (LTC1541).  $V_{CM} = 1/2 \ V_{CC}$  (LTC1542).

**Note 5:** The input leakage current is measured for COMPIN $^+$  at 0V (LTC1541) and COMPIN $^+$  = COMPIN $^-$  = 0V (LTC1542).

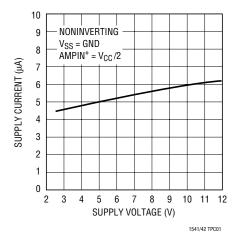
Note 6: The  $\theta_{JA}$  specified for the DD package is with minimal PCB heat spreading metal. Using expanded metal area on all layers of a board reduces this value

**Note 7:** The LTC1541C/LTC1541I and LTC1542C/LTC1542I are guaranteed functional over the operating temperature range of  $-40^{\circ}$ C to 85°C.

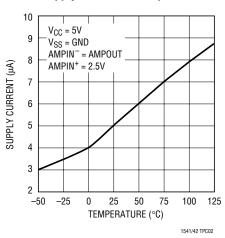
**Note 8:** The LTC1541C and LTC1542C are guaranteed to meet specified performance from 0°C to 70°C. The LTC1541C and LTC1542C are designed, characterized and expected to meet performance from -40°C to 85°C, but are not tested or QA sampled at these temperatures. The LTC1541I and LTC1542I are guaranteed to meet specified performance from -40°C to 85°C.

# TYPICAL PERFORMANCE CHARACTERISTICS

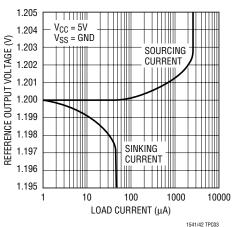
### **Supply Current vs Supply Voltage**



#### **Supply Current vs Temperature**



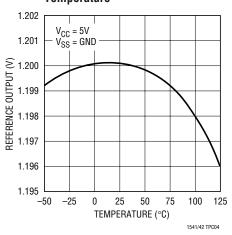
#### Reference Output Voltage vs Load Current



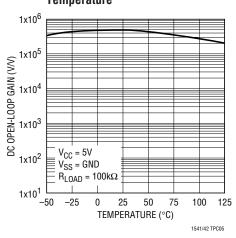
1541/42 TPC0

# TYPICAL PERFORMANCE CHARACTERISTICS

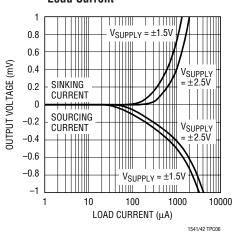
#### Reference Output Voltage vs Temperature



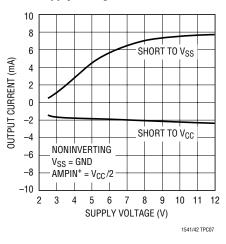
#### Op Amp DC Open-Loop Gain vs Temperature



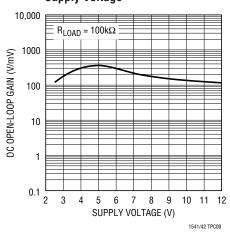
#### Op Amp Output Voltage vs Load Current



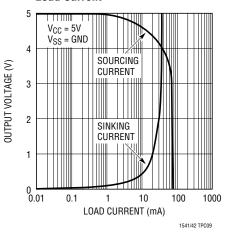
#### Op Amp Short-Circuit Current vs Supply Voltage



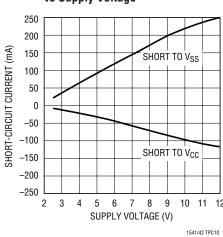
Op Amp DC Open-Loop Gain vs Supply Voltage



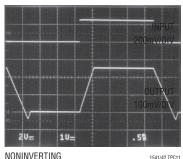
Comparator Output Voltage vs Load Current



# Comparator Short-Circuit Current vs Supply Voltage

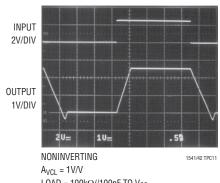


Op Amp Small-Signal Transient Response



NONINVERTING 1541/42 TPC11 A $_{VCL} = 1V/V$  LOAD =  $100k\Omega//100pF$  TO  $V_{SS}$   $V_{SUPPLY} = 5V$ 

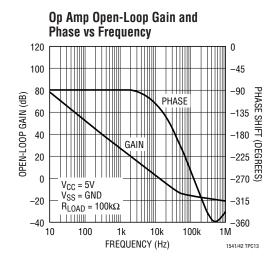
# Op Amp Large-Signal Transient Response



 $A_{VCL}$  = 1V/V LOAD = 100k $\Omega$ //100pF TO V<sub>SS</sub> V<sub>SUPPLY</sub> = 5V



# TYPICAL PERFORMANCE CHARACTERISTICS



# PIN FUNCTIONS

**AMPOUT (Pin 1):** Op Amp Output. The output can swing from rail-to-rail while driving a capacitive load of up to 1000pF. The output can source and sink 0.7mA (min).

**AMPIN**<sup>-</sup> (**Pin 2**): Inverting Input of Op Amp. The input common mode ranges from  $V_{SS}$  to  $(V_{CC}-1.3V)$ . The input current is typically 10pA at 25°C.

**AMPIN**<sup>+</sup> (**Pin 3**): Noninverting Input of Op Amp. The input common mode ranges from  $V_{SS}$  to ( $V_{CC}-1.3V$ ). The input current is typically 10pA at 25°C.

V<sub>SS</sub> (Pin 4): Negative Supply or Ground Connection.

**COMPIN**<sup>+</sup> (**Pin 5**): Noninverting Input of Comparator. The input common mode ranges from  $V_{SS}$  to  $(V_{CC}-1.3V)$ . The input current is typically 10pA at 25°C.

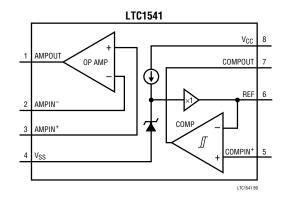
**REF (Pin 6) (LTC1541):** Reference Output.  $V_{REF} = 1.2V \pm 1\%$  over industrial temperature range, and is also connected to inverting comparator input internally. This device can source up to 2mA and sink up to  $20\mu A$ . The output can drive a bypass capacitor of up to  $0.01\mu F$  without oscillation.

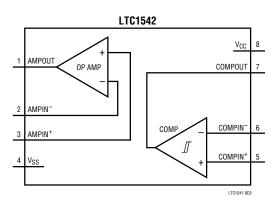
**COMPIN**<sup>-</sup> (Pin 6) (LTC1542): Inverting Input of Comparator. The input common mode ranges from  $V_{SS}$  to  $(V_{CC}-1.3V)$ . The input current is typically 10pA at 25°C.

**COMPOUT (Pin 7):** Comparator Output. The output can source 20mA and sink 5mA.

 $V_{CC}$  (Pin 8): Positive Supply, 2.5V  $\leq$  V<sub>CC</sub>  $\leq$  12.6V. The supply bypass capacitors are not required if the supply impedance is low. For single supply applications, it is a good general practice to bypass V<sub>CC</sub> with a 0.1μF capacitor to ground.

# **BLOCK DIAGRAMS**







# APPLICATIONS INFORMATION

The LTC1541/LTC1542 are a combination of a micropower op amp, comparator and 1.2V  $\pm 1\%$  reference (LTC1541) in an 8-pin package. The LTC1541 has the negative input of the comparator internally connected to the reference output. The supply voltage range is from 2.5V to 12.6V for single supply and  $\pm 1.25V$  to  $\pm 6.3V$  for dual supplies. The supply current is a mere  $5\mu A$  (typical) with a 5V single supply.

### Op Amp

The op amp is internally compensated to be unity-gain stable, with typical GBW at 12kHz and slew rate of 8V/ms. The output can drive a capacitive load of up to 1000pF and swings from rail-to-rail. The input range is from the negative rail to within 1.3V of the positive rail. The input bias current is less than 1nA maximum at the extended temperature range.

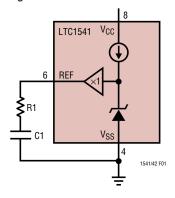


Figure 1. Damping the Reference Output

# **Comparator**

The comparator has a high impedance differential input stage with a common mode input range from the negative rail to within 1.3V of the positive rail. The CMOS output stage can swing from rail-to-rail and source up to 20mA continuously. The output stage has been designed to eliminate the power supply glitches that normally occur when the output changes logic state. In addition, internal hysteresis (±2.25mV) ensures clean output switching even with slow moving input signals. The negative input is internally connected to the reference for the LTC1541.

#### Reference

The internal bandgap reference has an output voltage of  $1.2V \pm 1\%$  over the industrial grade temperature range.

The output can source up to 2mA and sink up to  $20\mu A$  with a 5V supply. The output can drive a bypass capacitor of up to  $0.01\mu F$  without oscillation and by inserting a series resistor, capacitance values up to  $100\mu F$  can be used (Figure 1). Figure 2 shows the resistor value required for different capacitor values to achieve critical damping. Bypassing the reference can help prevent false tripping of the comparator by preventing glitches on  $V_{CC}$  or reference load transients from disturbing the reference output voltage. Figures 3a and 3b show the bypass reference output with a square wave applied to the  $V_{CC}$  pin while resistor R1 damps the reference response. Note that the comparator output doesn't trip.

### **Op Amp Stability**

Unlike other industry standard micropower CMOS op amps, the op amp in the LTC1541/LTC1542 maintain stability in unity-gain configuration while driving heavy capacitive loads of up to 1000pF.

Although this family is primarily designed for low frequency applications, good layout is extremely important. Low power, high impedance circuits may increase the effects of board leakage and stray capacitance. For example, the combination of a 10M resistance (from leakage between traces on a contaminated, poorly designed PC board) and a 1pF stray capacitance provides a pole at approximately 16kHz, which is near the amplifier's bandwidth. Board routing and layout should minimize leakage and stray capacitance. In some cases, stray capacitance may be unavoidable and it may be necessary to add a small capacitor across the feedback resistor to compensate (Figure 4); select the smallest capacitor value that ensures stability.

### **Inputs**

The input common mode range for both the op amp and comparator is from the negative supply to within 1.3V of the positive supply. The inputs can be taken more than 300mV below the negative supply without damaging the device if the current out of the pin is limited to less than 1mA. Unlike bipolar input op amps and comparators, the outputs of the CMOS LTC1541/LTC1542 will not reverse phase when the inputs are taken above the common mode input range.



# APPLICATIONS INFORMATION

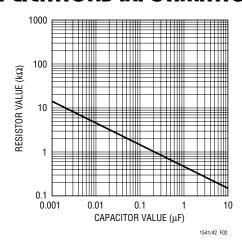


Figure 2. Damping Resistance vs Bypass Capacitor Value

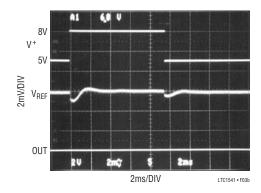


Figure 3b. Reference and Comparator Output Transient Response

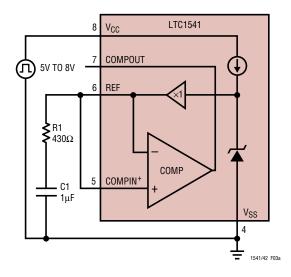


Figure 3a. Reference Transient Response Test Circuit

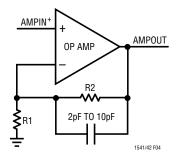
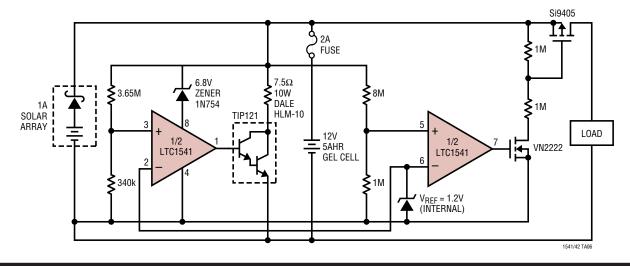


Figure 4. Compensation for Feedback Node Capacitance

# TYPICAL APPLICATIONS

#### Shunt Battery Charger with Low-Battery Load Disconnect

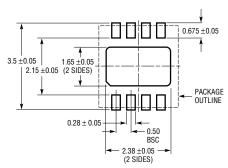


T LINEAR

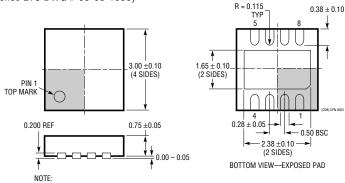
# PACKAGE DESCRIPTION

#### **DD Package** 8-Lead Plastic DFN (3mm × 3mm)

(Reference LTC DWG # 05-08-1698)



RECOMMENDED SOLDER PAD PITCH AND DIMENSIONS



- 1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE MO-229 VARIATION OF (WEED-1)
- 2. ALL DIMENSIONS ARE IN MILLIMETERS
  3. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE 4. EXPOSED PAD SHALL BE SOLDER PLATED

 $3.00 \pm 0.102$ 

 $(.118 \pm .004)$ 

# MS8 Package 8-Lead Plastic MSOP

(Reference LTC DWG # 05-08-1660)

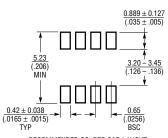
0 254

(.010)

GAUGE PLANE

0.18 (.007) DETAIL "A"

DETAIL "A"



RECOMMENDED SOLDER PAD LAYOUT

- NUTE:

  1. DIMENSIONS IN MILLIMETER/(INCH)

  2. DRAWING NOT TO SCALE

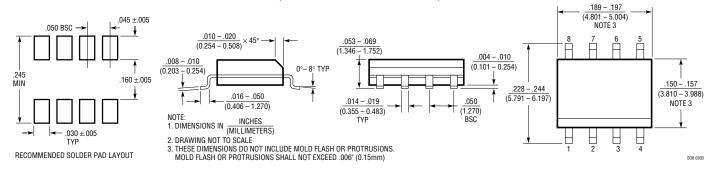
  3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
  MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006') PER SIDE

  4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
- INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006°) PER SIDE 5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004°) MAX

#### (NOTE 3) 7 6 5 | | | | | | (.0205)REF $0.53 \pm 0.152$ $3.00 \pm 0.102$ $(.021 \pm .006)$ $\frac{4.90 \pm 0.152}{(.193 \pm .006)}$ $(.118 \pm .004)$ (NOTE 4) 1.10 0.86 (.043) MAX (.034)REF SEATING 0.22 - 0.38 $0.127 \pm 0.076$ (.009 - .015)0.65 TYP (.0256) BSC

### S8 Package 8-Lead Plastic Small Outline (Narrow .150 Inch)

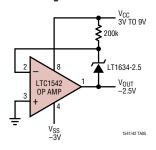
(Reference LTC DWG # 05-08-1610)



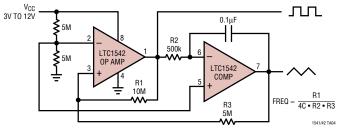


# TYPICAL APPLICATIONS

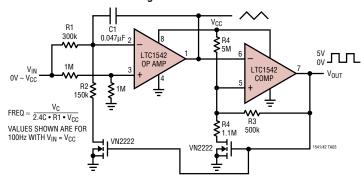
#### **Negative Reference**



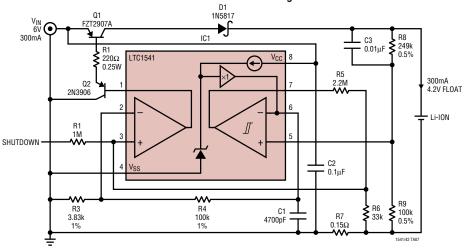
# **Single Supply Function Generator**



#### **Voltage Control Oscillator**



#### Linear Lithium-Ion Charger



# **RELATED PARTS**

PART NUMBER	DESCRIPTION	COMMENTS
LT®1078/LT1079	Dual/Quad Micropower, Single Supply Precision Op Amps	70μV, V <sub>OS</sub> Max and 0.4μV/°C Drift, 200kHz GBW, 0.07V/μs
		Slew Rate, Input/Output Common Mode Includes Ground
LT1178/LT1179	Dual/Quad 17μA Max, Single Supply Precision Op Amps	70μV, V <sub>OS</sub> Max and 2.5μV/°C Drift Max, 85kHz GBW, 0.04V/μs
		Slew Rate, Input/Output Common Mode Includes Ground
LT1490/LT1491	Dual/Quad Micropower Rail-to-Rail Input and Output Op Amps	Single Supply Input Range: –0.4V to 44V, Micropower 50µA
		Amplifier, Rail-to-Rail Input and Output, 200kHz GBW
LT1635	Micropower Rail-to-Rail Op Amp and Reference	130µA of Supply Current, 1.3mV V <sub>OS</sub> Max
LT2078/LT2079	Dual/Quad Micropower, Single Supply Precision Op Amps	SO-8 and 14-Lead Standard Pinout, 70µV V <sub>OS</sub> Max, 200kHz GBW
LT2178/LT2179	Dual/Quad 17µA Max, Single Supply Precision Op Amps	SO-8 and 14-Lead Standard Pinout, 70µV V <sub>OS</sub> Max, 85kHz GBW
		15412f

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LM339EDR2G LT6700HS6-2#TRM NTE919 NTE922 TS883IQ2T LT6700HVCS6-3#TRMPBF LT6700HVHS6-3#TRMPBF
MAX978EEE+T MAX975ESA+T MAX9602EUG+T MAX997EUA+T MIC841NYC5-T5 LM393WDT MCP6564T-E/STVAO
MAX983ESA+T NCX2200GMAZ LTC1540CMS8#PBF MC10E1651FNG