



**HumPRC™ Series 868MHz
RF Transceiver Module
Data Guide**

Wireless made simple®



Warning: Some customers may want Linx radio frequency (“RF”) products to control machinery or devices remotely, including machinery or devices that can cause death, bodily injuries, and/or property damage if improperly or inadvertently triggered, particularly in industrial settings or other applications implicating life-safety concerns (“Life and Property Safety Situations”).

NO OEM LINX REMOTE CONTROL OR FUNCTION MODULE SHOULD EVER BE USED IN LIFE AND PROPERTY SAFETY SITUATIONS. No OEM Linx Remote Control or Function Module should be modified for Life and Property Safety Situations. Such modification cannot provide sufficient safety and will void the product’s regulatory certification and warranty.

Customers may use our (non-Function) Modules, Antenna and Connectors as part of other systems in Life Safety Situations, but only with necessary and industry appropriate redundancies and in compliance with applicable safety standards, including without limitation, ANSI and NFPA standards. It is solely the responsibility of any Linx customer who uses one or more of these products to incorporate appropriate redundancies and safety standards for the Life and Property Safety Situation application.

Do not use this or any Linx product to trigger an action directly from the data line or RSSI lines without a protocol or encoder/decoder to validate the data. Without validation, any signal from another unrelated transmitter in the environment received by the module could inadvertently trigger the action.

All RF products are susceptible to RF interference that can prevent communication. RF products without frequency agility or hopping implemented are more subject to interference. This module does have a frequency hopping protocol built in, but the developer should still be aware of the risk of interference.

Do not use any Linx product over the limits in this data guide. Excessive voltage or extended operation at the maximum voltage could cause product failure. Exceeding the reflow temperature profile could cause product failure which is not immediately evident.

Do not make any physical or electrical modifications to any Linx product. This will void the warranty and regulatory and UL certifications and may cause product failure which is not immediately evident.

33	Using the PB Line
34	Using the Low Power Features
36	The Command Data Interface
37	Reading from Registers
38	Writing to Registers
39	Command Length Optimization
40	Example Code for Encoding Read/Write Commands
42	The Command Data Interface Command Set
80	Typical Applications
84	HumPRC™ Series Long-Range Handheld Transmitter
86	Power Supply Requirements
86	Antenna Considerations
87	Interference Considerations
88	Pad Layout
89	Microstrip Details
90	Board Layout Guidelines
91	Helpful Application Notes from Linx
92	Production Guidelines
92	Hand Assembly
92	Automated Assembly
94	General Antenna Rules
96	Common Antenna Styles
98	Regulatory Considerations
101	OEM Labeling Requirements
102	Notes

Ordering Information

Ordering Information	
Part Number	Description
HUM-868-PRC	868MHz HumPRC™ Series Remote Control Transceiver, Castellation Interface, External Antenna Connection
HUM-868-PRC-CAS	868MHz HumPRC™ Series Remote Control Transceiver, Castellation Interface, External Antenna Connection
HUM-868-PRC-UFL	868MHz HumPRC™ Series Remote Control Transceiver, Castellation Interface, U.FL / MHF Compatible Connector
EVM-868-PRC-CAS	868MHz HumPRC™ Series Carrier Board, Through-Hole Pin Interface, SMA Connector
EVM-868-PRC-UFL	868MHz HumPRC™ Series Carrier Board, Through-Hole Pin Interface, U.FL / MHF Compatible Connector
MDEV-868-PRC	868MHz HumPRC™ Series Master Development System

Figure 2: Ordering Information

Absolute Maximum Ratings

Absolute Maximum Ratings				
Supply Voltage V_{cc}	-0.3	to	+3.9	VDC
Any Input or Output Pin	-0.3	to	$V_{cc} + 0.3$	VDC
RF Input			0	dBm
Operating Temperature	-40	to	+85	°C
Storage Temperature	-40	to	+85	°C

Exceeding any of the limits of this section may lead to permanent damage to the device. Furthermore, extended operation at these maximum ratings may reduce the life of this device.

Figure 3: Absolute Maximum Ratings



Warning: This product incorporates numerous static-sensitive components. Always wear an ESD wrist strap and observe proper ESD handling procedures when working with this device. Failure to observe this precaution may result in module damage or failure.

HumPRC™ Series Transceiver Specifications

Parameter	Symbol	Min.	Typ.	Max.	Units	Notes
Timing						
Module Turn-On Time						
Via V_{CC}		71		190	ms	4
Via <u>POWER_DOWN</u>			35		ms	4
Via Standby			4.12		ms	4
Serial Command Response						
Volatile R/W			0.4	5	ms	8
NV Update			2.4	50	ms	8
Factory Reset		199		484	ms	14
Channel Dwell Time				400	ms	
Interface Section						
UART Data rate		9,600		115,200	bps	
Input						
Logic Low	V_{IL}			$0.3 \cdot V_{CC}$	VDC	
Logic High	V_{IH}	$0.7 \cdot V_{CC}$			VDC	
Output						
Logic Low, MODE_IND, ACK_OUT	V_{OLM}			$0.3 \cdot V_{CC}$	VDC	1,9
Logic High, MODE_IND, ACK_OUT	V_{OHM}	$0.7 \cdot V_{CC}$			VDC	1,9
Logic Low	V_{OL}			$0.3 \cdot V_{CC}$		1,10
Logic High	V_{OH}	$0.7 \cdot V_{CC}$				1,10
Flash (Non-Volatile) Memory Specifications						
Flash Refresh Cycles		2,000			cycles	12
1. Measured at $3.3V V_{CC}$						11. End of <u>CMD_DATA_OUT</u> stop bit to change in <u>CRESP</u>
2. Measured at $25^{\circ}C$						12. Number of non-volatile memory refresh cycles. The number of write operations per refresh cycle varies from 8 to 150.
3. Input power < $-60dBm$						13. With CSMA disabled
4. Characterized but not tested						14. Start of factory reset command to end of last ACK response
5. PER = 1%						15. The maximum output power is limited on the higher channels in order to comply with ETSI regulations.
6. Into a 50-ohm load						
7. No RF interference						
8. From end of command to start of response						
9. 60mA source/sink						
10. 6mA source/sink						

Figure 4: Electrical Specifications

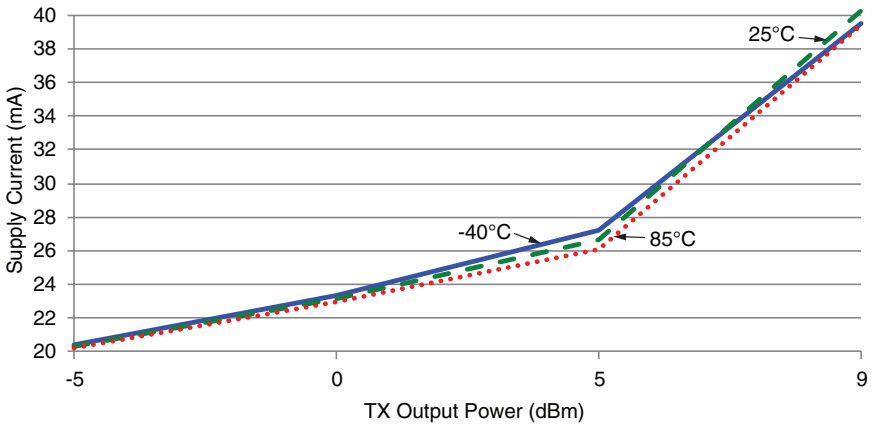


Figure 8: HumPRC™ Series Transceiver Average TX Current vs. Transmitter Output Power at 3.3V

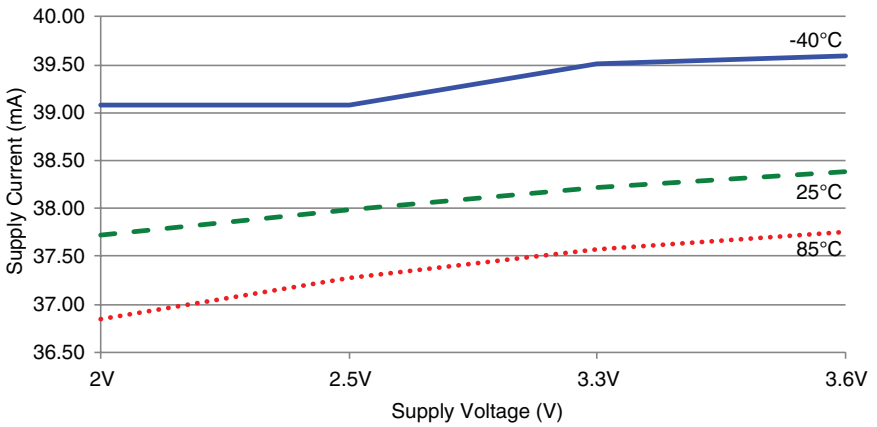


Figure 7: HumPRC™ Series Transceiver TX Current vs. Supply Voltage at Max Power

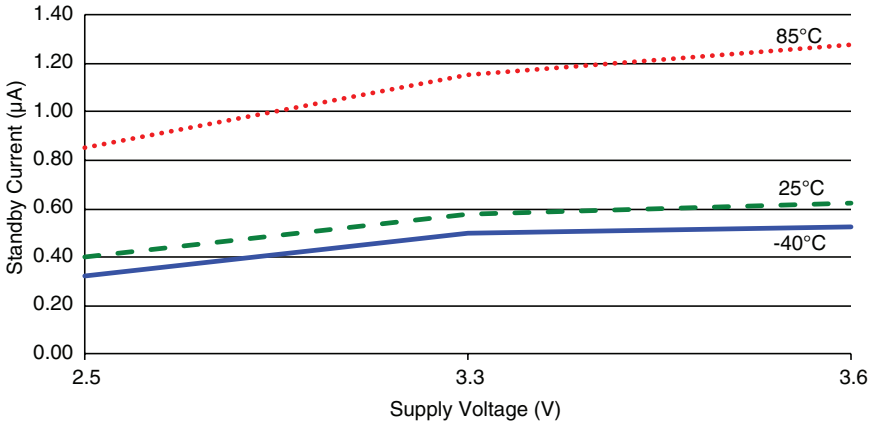


Figure 11: HumPRC™ Series Transceiver Standby Current Consumption vs. Supply Voltage

Pin Assignments

There are three version of the module. The standard version is the smallest. The other versions have mostly the same pin assignments, but the antenna is routed to either a castellation (-CAS) or a U.FL connector (-UFL), depending on the part number ordered.

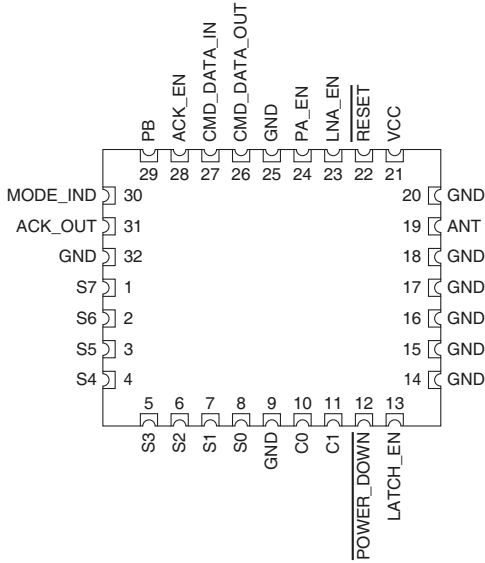


Figure 14: HumPRC™ Series Transceiver Standard Version Pin Assignments (Top View)

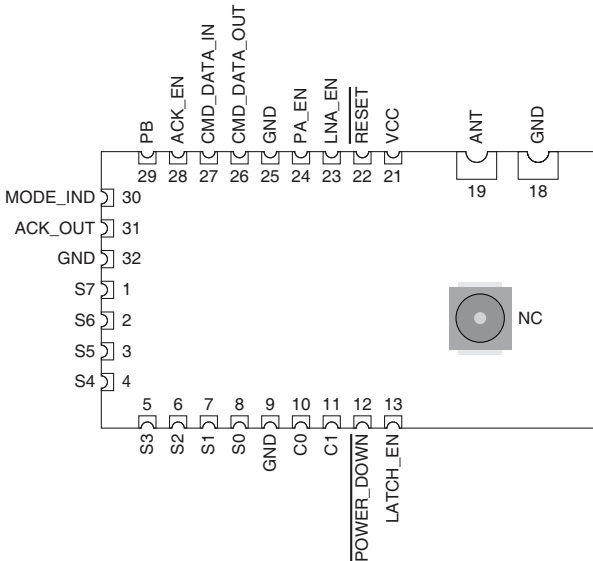


Figure 13: HumPRC™ Series Transceiver Encapsulated Version Pin Assignments - Castellated Connection (Top View)

Pin Descriptions

Pin Number	Name	I/O	Description
13	LATCH_EN	I	If this line is high, then the status line outputs are latched (a received command to activate a status line toggles the output state). If this line is low, then the output lines are momentary (active for as long as a valid signal is received). Do not leave floating.
19	ANTENNA	—	50-ohm RF Antenna Port
21	VCC	—	Supply Voltage
22	$\overline{\text{RESET}}^2$	I	This line resets the module when pulled low. It should be pulled high for normal operation. Leave unconnected to minimize leakage current.
23	LNA_EN	O	Low Noise Amplifier Enable. This line is driven high when receiving. It is intended to activate an optional external LNA.
24	PA_EN	O	Power Amplifier Enable. This line is driven high when transmitting. It is intended to activate an optional external power amplifier.
26	CMD_DATA_OUT	O	Command Data Out. Output line for the serial interface commands
27	CMD_DATA_IN	I	Command Data In. Input line for the serial interface commands. If serial control is not used, this line should be tied to supply to minimize current consumption.
28	ACK_EN	I	Pull this line high to enable the module to send an acknowledgement message after a valid control message has been received. Do not leave floating.
29	PB ¹	I	A high on this line initiates the Join Process, which causes two units to accept each other's transmissions. It is also used with a special sequence to reset the module to factory default configuration.
30	MODE_IND	O	This line indicates module activity. It can source enough current to drive a small LED, causing it to flash. The duration of the flashes indicates the module's current state.
31	ACK_OUT	O	This line goes high when the module receives an acknowledgement message from another module after sending a control message.

1. These lines have an internal 20k Ω pull-down resistor

2. These lines have an internal 10k Ω pull-up resistor

Figure 16: HumPRC™ Series Transceiver Pin Descriptions

Theory of Operation

The HumPRC™ Series transceiver is a low-cost, high-performance synthesized FSK transceiver. Figure 19 shows the module's block diagram.

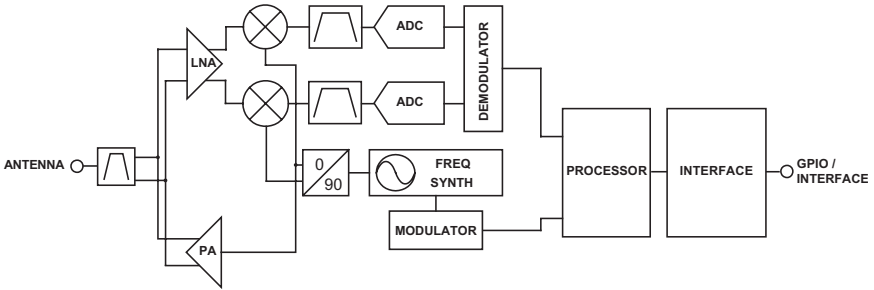


Figure 19: HumPRC™ Series Transceiver RF Section Block Diagram

The HumPRC™ Series transceiver operates in the 863 to 870MHz frequency band. The transmitter output power is programmable, though the maximum power is automatically limited on the higher channels to comply with ETSI regulations. The range varies depending on the antenna implementation and the local RF environment. When operating near the maximum range, there can be higher packet loss when transmitting on the higher channels due to output power limiting, although it is anticipated that most customers won't notice any difference since the transceiver utilizes 70 channels for transmissions.

The RF carrier is generated directly by a frequency synthesizer that includes an on-chip VCO. The received RF signal is amplified by a low noise amplifier (LNA) and down-converted to I/Q quadrature signals. The I/Q signals are digitized by ADCs.

A low-power onboard communications processor performs the radio control and management functions including Automatic Gain Control (AGC), filtering, demodulation and packet synchronization. A control processor performs the higher level functions and controls the serial and hardware interfaces.

A crystal oscillator generates the reference frequency for the synthesizer and clocks for the ADCs and the processor.

Transceiver Operation

The transceiver has two roles: Initiating Unit (IU) that transmits control messages and Responding Unit (RU) that receives control messages. If all of the status lines are set as inputs, then the module is set as an IU only. The module stays in a low power sleep mode until a status line goes high, starting the Transmit Operation.

If all of the status lines are set as outputs, then the module is set as an RU only. It stays in Receive Operation looking for a valid transmission from a paired IU.

A module with both input and output status lines can operate as an IU and an RU. The module idles in Receive Operation until either a valid transmission is received or a status line input goes high, initiating the Transmit operation.

When an input goes high, the transceiver captures the logic state of each of the status lines. The line states are placed into a packet and transmitted using the configured addressing mode, hop sequence and encryption key (if enabled).

An associated RU receives the packet and sets its status line outputs according to the received packet. It then stays synchronized with the IU and updates the states of its outputs with every packet. Its outputs can be connected to external circuitry that activates when the lines go high.

The RU can also send an acknowledgement back to the IU. If the ACK_EN line is high when a valid control packet is received, the RU sends back an acknowledgement. When the IU receives the acknowledgement, it raises its ACK_OUT line. The ACK_EN line can be connected to ground to disable acknowledgements, connected to the power supply to acknowledge on receipt of the valid command or controlled by external circuitry to acknowledge when an action has taken place.

The ACK_EN can be connected to an LED as an indication to the user or used by the system in other ways, such as updating a display or being used to deactivate an automated system.

System Operation

Transmitters and receivers are paired using the built-in Join Process (see the [Join Process](#) for details). One device is configured as an Administrator and creates the network address and encryption key. When Nodes join, the Administrator sends them the encryption key, network address and their unique address within the network. The addressing method used by the HumPRC™ Series modules can support up to hundreds of nodes, depending on the use model (duration of activations and how often they are sent).

It is up to the designer to determine which device makes the most sense as the Administrator in the final system, but there are some common configurations. In a system with one transmitter and one receiver, it does not matter which is the Administrator. In a system where one transmitter is going to activate several receivers, the transmitter is normally the Administrator (Figure 20 a). In a system with one receiver and multiple transmitters, the receiver should be the Administrator (Figure 20 b).

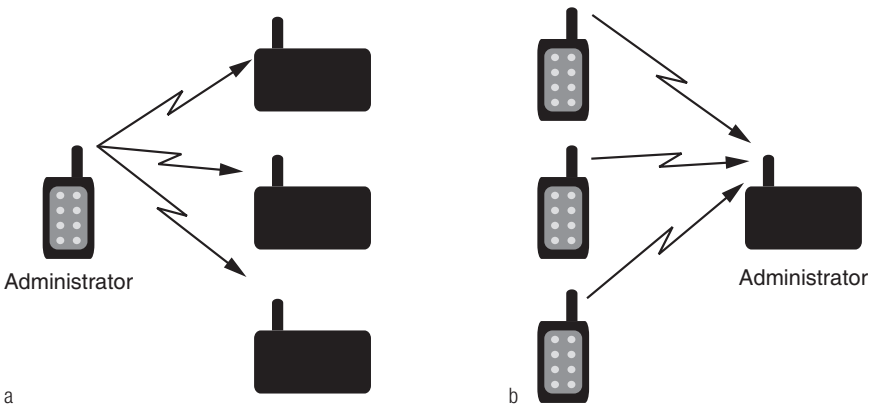


Figure 20: HumPRC™ Series Transceiver Transmitter to Receiver Ratios

A system with multiple transmitters and receivers can use any of the devices as an Administrator (Figure 21 a) or may use a separate device that is only used to join new devices to the network (Figure 21 b). Once all system nodes have received the key and their address, the Administrator node operates the same as any other node.

By default, the Administrator and all Nodes broadcast to the entire network. All transmitters can activate all receivers in the network. An external microcontroller can be used to change the [UDESTIDO](#) register to activate a specific Node in the network. This is a more advanced operation and requires the microcontroller and custom firmware.

Polite Spectrum Access

Europe's ETSI standards have very specific requirements for operating in the 868MHz band, which are contained in EN 300 220-1. One of the items is called Polite Spectrum Access (PSA) and is a method of performing a Clear Channel Assessment before transmitting to mitigate interference with other systems.

The HUM-868-PRO employs Polite Spectrum Access. This allows the module to exceed the 0.01% duty cycle limitation that otherwise applies to modules operating in the 863-870MHz band. This is advantageous for systems that need to transmit large data streams or have an unpredictable usage pattern.

Systems that employ Polite Spectrum Access, while exempt from the normal duty cycle restrictions, must meet channel occupancy time requirements in ETSI EN 300 220-1 v3.1.1 section 5.21.3. The HUM-868 PRO module has a built-in mechanism that ensures compliance to this requirement by internally limiting each channel's cumulative on time. This limits the transmission time on any given channel to 33.3 seconds per hour. The module uses 70 channels so it can transmit for 2,333s of every hour or about 64% assuming no delay for transmissions by other units.

It is preferable to spread the transmit time out evenly to avoid using all of the time at the start of the hour and then having 21 minutes' delay at the end of the hour. To accomplish this, the module divides the time into blocks of 180s called bandsread intervals. The module can transmit on each channel for 1.66s of every 180s (33.3s of every 3600s ÷ 20).

The PSA options are configured with the [ENC SMA](#) register. Setting the register to 0x00 disables PSA and the transmission is immediate. This can be used in applications with an inherent duty cycle <0.1%.

Setting the register to 0x01 enables LBT but without the transmitter on time duty cycle restriction.

Setting the register to 0x02 enables the full LBT with transmitter on time restriction. This complies with the ETSI regulations for LBT + AFA.

Leaving the register in its default value of 0x02 enables Polite Spectrum Access. To maintain validity of the HUM-868-PRO module Declaration of Conformity (DoC) this setting (0x02) must be used and must not be user adjustable.

Addressing Modes

The module has very flexible addressing methods selected with the **ADDMODE** register. It can be changed during operation. The transmitting module addresses packets according to the addressing mode configuration. The receiving module processes all addressing types regardless of the **ADDMODE** configuration. If the received message matches the addressing criteria, it is output on the UART. Otherwise it is discarded. The **ADDMODE** configuration also enables assured delivery.

There are three addressing modes: DSN, User and Extended User. Each mode offers different communications methods, but all use source and destination addressing. The source address is for the transmitting unit, the destination address is the intended receiver. Each mode uses different registers for both the source and destination addresses.

Extended User Addressing mode uses the four user destination address bytes (**UDESTID**[3-0]) as a destination address. The module's local address is contained in the four user source ID registers (**USRCID**[3-0]).

In normal operation, each module has a user ID mask (**UMASK**[3-0]) that splits the 32 address bits into up to three fields to provide a network address and address fields for sub-networks, supporting both individual addressing and broadcast addressing within the user's network.

The HumPRC™ Series is normally configured using the Join Process, which sets the addressing mode to Extended User mode. The other modes would normally only be used if the HumPRC™ Series is being implemented in a mixed system that also uses the HumPRO™ Series modules.

Please see the HumPRO™ Series data guide for a description of the other addressing modes. A detailed explanation and examples for each addressing mode are given in Reference Guide RG-00105.

AES Encryption

HumPRC™ Series modules offer AES encryption. Encryption algorithms are complex mathematical calculations that use a large number called a key to scramble data before transmission. This is done so that unauthorized persons who may intercept the signal cannot access the data. To decrypt the data, the receiver must use the same key that was used to encrypt it. It performs the same calculations as the transmitter and if the key is the same, the data is recovered.

The HumPRC™ Series module has the option to use AES encryption, arguably the most common encryption algorithm on the market. This is implemented in a secure mode of operation to ensure the secrecy of the transmitted data. It uses a 128-bit key to encrypt the transmitted data. The source and destination addresses are sent in the clear.

There are two ways to enable encryption and set the key: sending serial commands and using the Join Process.

Writing an encryption key to the module with the CDI

The module has no network key when shipped from the factory. An encryption key can be written to the module using the CDI. The [CMD](#) register is used to write or clear a key. The key cannot be read.

The same key must be written to all modules that are to be used together. If they do not have the same key, then they will not communicate in encrypted mode.

The JOIN Process

The Join Process can be used to generate and distribute the encryption key and addresses through a series of button presses. The key is stored in an Administrator device and the process uses a factory key to distribute the key to node devices in a secure manner. See the Join Process section for more information on this feature.

A module becomes a node by joining with an administrator. This is done by pressing and releasing the PB button on both units. The modules automatically search for each other using a special protocol. When they find each other, the administrator sends the node the encryption key, UMASK and its network address. The UDESTID is set to the address of the administrator. The values are encrypted using a special factory-defined key. Once the Join Process is complete, the MODE_IND blinks on both units and they now operate together. This is shown in Figure 22 A.

If UMASK is pre-set when Generate Key is initiated, then the Join Process uses that mask and sets the address accordingly. This can allow more nodes in the network. This is shown in Figure 22 B. Likewise, the network key can be written to the module with the CDI interface. If the PGKEY bit in the **SECOPT** register is also set to 0, the Generate Key process will generate a network address without changing the preset key. Or the administrator can be completely configured through the CDI and the Join Process used to associate nodes in the field. This gives the system designer many options for configuration.

The **SECOPT** register is used to configure options related to the Join Process. This allows the OEM to set desired values at the factory and allow final network configuration in the field. This includes disabling the ability to change the address, change the key, share the key or perform a factory reset through the PB line. The built-in security prohibits changing a node to an administrator without changing the key.

Please see Reference Guide RG-00107, The HumPRO™ Series Join Process for more details and examples of the Join Process.

Operation with the HumPRO™ Series

The commands from the HumPRC™ Series module can be received by a HumPRO™ Series transceiver and vice versa. The modules should be joined using the normal Join Process. The IU sends a REMOTE_ACTIVATE packet and accepts a REMOTE_CONFIRM reply.

A microcontroller connected to the HumPRO™ Series can be programmed to take action based on the STATUS byte in a REMOTE_ACTIVATE packet that is received from a HumPRC™ Series module. It can also read out the packet header and know the address of the sending module and respond with a REMOTE_CONFIRM packet to activate the ACK_OUT line on the HumPRC™ module.

Likewise, the microcontroller can be programmed to send a REMOTE_ACTIVATE packet to a HumPRC™ Series module. This opens up many options for creative mixed-mode design.

Remote Activation

The REMOTE_ACTIVATE packet consists of six bytes:

0x03 0x00 0x00 0x00 0x10 <STATUS>

The first byte is 0x03 with the next three bytes 0x00. Byte five is 0x10 which indicates a Remote Activation. Byte six is the STATUS byte, which is a bit map of the button states. Bit 0 corresponds to button S0 and so forth. Each bit is 1 if the corresponding line is high.

Remote Confirm

The REMOTE_CONFIRM packet has the following format:

0x03 0x00 0x00 0x00 0x11 <DURATION> <ALIVE>

The first two bytes are 0x00 0x11 and indicate that the packet is a remote confirm packet.

The DURATION byte indicates the amount of time that the ACK_OUT line should be held high. This value is multiplied by 10ms. If the value is 0, the output is immediately taken low. The default value is 0x02 for 20ms. This value overrides the effect of a previously received REMOTE_CONFIRM packet.

Acknowledgement

A responding module is able to send an acknowledgement to the transmitting module. This allows the initiating module to know that the responding side received the command.

When the Responding Unit receives a valid REMOTE_ACTIVATE packet, it immediately checks the state of the ACK_EN line. If it is high the module sends a REMOTE_CONFIRM packet.

When the Initiating Unit receives a REMOTE_CONFIRM packet, it pulls the ACK_OUT line high for an amount of time specified by the REMOTE_CONFIRM packet (20ms by default).

Connecting the ACK_EN line to V_{CC} causes the RU to transmit REMOTE_CONFIRM packets as soon as it receives a valid REMOTE_ACTIVATE packet. Alternately this line can be controlled by an external circuit that raises the line when a specific action has taken place. This confirms to the IU that the action took place rather than just acknowledging receipt of the signal.

Note: Only one RU should be enabled to transmit an acknowledgement response for a given IU since multiple acknowledgements will interfere with each other.

External Amplifier Control

The HumPRC™ Series transceiver has two output lines that are designed to control external amplifiers. The PA_EN line goes high when the module activates the transmitter. This can be used to activate an external power amplifier to boost the signal strength of the transmitter. The LNA_EN line goes high when the module activates the receiver. This can be used to activate an external low noise amplifier to boost the receiver sensitivity. These external amplifiers can significantly increase the range of the system at the expense of higher current consumption and system cost.

The states of the PA_EN and LNA_EN lines can be read in the [LSTATUS](#) register. This offers a quick way to determine the current state of the radio.

Using the MODE_IND Line

The MODE_IND line is designed to be connected to an LED to provide visual indication of the module's status and current actions. The pattern of blinks indicates the particular feedback from the module. Figure 24 shows the different blink patterns and their meanings.

HumPRC™ Series Transceiver MODE_IND Line Timing	
Display [on/off time in seconds]	Module Status
Join Operation	
Two quick blinks	Administrator Join. The administrator is looking for a node to join with.
One quick blink	Node Join. The node is looking for an administrator to join with.
Quick blink	Key Transfer Active. Key transfer is taking place (administrator and node).
Slow Blink	Key Transfer Complete. The module has completed a key transfer (administrator and node).
Temporary On	On when the PB line is high
Two quick blinks, one time	Join Canceled.
Slow blink, repeat 3 times	Failure. For Share Key or Get Key, there are multiple units attempting to pair, protocol error, or timeout without response
Slow blink and two quick blinks	Long Hold Acknowledgement. The long hold period for Generate Key or Reset Sequence was recognized (PB is asserted)
Key Test Results	
One quick blink Three times	No Key. There is no network key or network address.
Two quick blinks Three times	Key Set, node. The network key and network address are set on a node.
Three quick blinks Three times	Key Set, administrator. The network key and network address are set on an administrator.
Normal operation	
Off	No activity
Temporarily on	Transmitting or receiving packet

Figure 24: HumPRC™ Series MODE_IND Line Timing

Using the Low Power Features

The module supports a sleep state to save current in battery-powered applications. During the sleep state, no module activity occurs and no packets can be received but current consumption is less than 1µA typical.

There are two ways of putting the module to sleep. First, pulling the Power Down (`POWER_DOWN`) line low puts the module to sleep. Taking the line high wakes the module. Second, all of the following should be true:

1. There is no transmission in progress
2. All status lines are low and either
 - `IDLE = 1` (default) and all status lines are configured as inputs, or
 - `IDLE = 2` (allows sleeping when incoming control message may be missed)
3. The internal KeepAlive timer has expired.

The internal KeepAlive timer is set by the following events:

1. On wakeup from a transition on the `CMD_DATA_IN` line, KeepAlive is set to 2s. This allows time for an external unit to change `IDLE` to 0 to keep the unit awake.
2. On each transmission, KeepAlive is set to 760ms if the remaining KeepAlive time is less. $[\max(760\text{ms}, \text{KeepAlive})]$
3. On reception of a `REMOTE_CONFIRM` packet, KeepAlive is set to received `ALIVE` value multiplied by 0.1s if the remaining KeepAlive time is less. The KeepAlive can be extended indefinitely by periodic reception of `REMOTE_CONFIRM` messages.
 $\max(\text{REMOTE_CONFIRM.keepAlive} * 100\text{ms}, \text{KeepAlive})$

The Command Data Interface

The HumPRC™ Series transceiver has a serial Command Data Interface (CDI) that is used to configure and control the transceiver through software commands. This interface consists of a standard UART with a serial command set. The CMD_DATA_IN and CMD_DATA_OUT lines are the interface to the module's UART. The UART is configured for 1 start bit, 1 stop bit, 8 data bits, no parity and a serial data rate set by register [UARTBAUD](#) (default 9,600bps).

Configuration settings are stored in two types of memory inside the module. Volatile memory is quick to access, but it is lost when power is removed from the module. Non-volatile memory has a limited number of write cycles, but is retained when power is removed. When a configuration parameter has both a non-volatile and volatile register, the volatile register controls the operation unless otherwise stated. The non-volatile register holds the default value that is loaded into the volatile register on power-up.

Configuration settings are read from non-volatile memory on power up and saved in volatile memory. The volatile and non-volatile registers have different address locations, but the same read and write commands. The two locations can be changed independently.

The general serial command format for the module is:

[FF] [Length] [Command]

The Length byte is the number of bytes in the Command field. The Command field contains the register address that is to be accessed and, in the case of a write command, the value to be written. Neither Length nor Command can contain a 0xFF byte.

Byte values of 128 (0x80) or greater can be sent as a two-byte escape sequence of the format:

0xFE, [value - 0x80]

For example, the value 0x83 becomes 0xFE, 0x03. The Length count includes the added escape bytes.

A response is returned for all valid commands. The first response byte is CMD_ACK (0x06) or CMD_NACK (0x15). Additional bytes may follow, as determined by the specific command.

Writing to Registers

To allow any byte value to be written, values of 128 (0x80) or greater can be encoded into a two-byte escape sequence of the format 0xFE, [value - 0x80]. This includes register addresses as well as values to be written to the registers. The result is that there are four possible packet structures because of the possible escape sequences. These are shown in Figure 29.

HumPRC™ Series Write to Configuration Register Command					
Register and Value less than 128 (0x80)					
Header	Size	Address	Value		
0xFF	0x02	REG	V		
Register less than 128 (0x80) and a Value greater than or equal to 128 (0x80)					
Header	Size	Address	Escape	Value	
0xFF	0x03	REG	0xFE	V-0x80	
Register greater than or equal to 128 (0x80) and a Value less than 128 (0x80)					
Header	Size	Escape	Address	Value	
0xFF	0x03	0xFE	REG-0x80	V	
Register and Value greater than or equal to 128 (0x80)					
Header	Size	Escape	Address	Escape	Value
0xFF	0x04	0xFE	REG-0x80	0xFE	V-0x80

Figure 29: HumPRC™ Series Write to Configuration Register Command

Generally, there are three steps to creating the command.

1. Determine the register address and the value to be written.
2. Encode the address and value as either the number (N) or the encoded number (0xFE, N-0x80) as appropriate.
3. Add the header (0xFF) and the size.

The module responds with an ACK (0x06). If the ACK is not received, the command should be resent. The module responds with a NACK (0x15) if a write is attempted to a read-only or invalid register.

As an example, to write 01 to register 0x83, send

FF 03 FE 03 01

Note: The non-volatile memory has a life expectancy with a limited number of refresh cycles. Please see the electrical specifications.

Example Code for Encoding Read/Write Commands

This software example is provided as a courtesy in “as is” condition. Linx Technologies makes no guarantee, representation, or warranty, whether express, implied, or statutory, regarding the suitability of the software for use in a specific application. The company shall not, in any circumstances, be liable for special, incidental, or consequential damages, for any reason whatsoever.

File EncodeProCmd.c

```
/* Sample C code for encoding Hum-fff-PRO commands
**
** Copyright 2015 Linx Technologies
** 159 Ort Lane
** Merlin, OR, US 97532
** www.linxtechnologies.com
**
** License:
** Permission is granted to use and modify this code, without royalty, for
** any purpose, provided the copyright statement and license are included.
*/

#include "EncodeProCmd.h"

/* Function: HumProCommand
** Description: This function encodes a command byte sequence.
** If len = 1, a read command is generated.
** If len > 1, a write command is generated.
** rcmd[0] = register number
** rcmd[1..(n-1)] = bytes to write
*/
unsigned char HumProCommand(unsigned char *ecmd, const unsigned char *rcmd, unsigned char n) /* number of encoded bytes, n+2 to 2*n+2 */
{
    unsigned char dx; /* destination index */
    unsigned char sx; /* source index */
    unsigned char v; /* value to be encoded */
    dx = 2;
    sx = 0;
    while (n--) {
        v = rcmd[sx++];
        if (v >= 0xf0) {
            ecmd[dx++] = 0xfe;
            v &= 0x7f;
        }
        ecmd[dx++] = v;
    }
    ecmd[0] = 0xff;
    ecmd[1] = dx - 2;
}
```

The Command Data Interface Command Set

The following sections describe the registers.

HumPRC™ Series Configuration Registers

Name	NV Addr	Vol Addr	R/W	Default Value	Description
CRCERRS		0x40	R/W	0x00	CRC Error Count
HOPTABLE	0x00	0x4B	R/W	0xFF	Channel Hop Table
TXPWR	0x02	0x4D	R/W	0x03	Transmit Power
UARTBAUD	0x03	0x4E	R/W	0x01	UART data rate
ADDMODE	0x04	0x4F	R/W	0x0F	Addressing mode
DATATO	0x05	0x50	R/W	0x10	Data timeout
MAXTXRETRY	0x07	0x52	R/W	0x02	Maximum Transmit Retries
ENCRC	0x08	0x53	R/W	0x01	Enable CRC checking
BCTRIG	0x09	0x54	R/W	0x40	Byte Count trigger
ENC SMA	0x0B	0x56	R/W	0x02	Enable Polite Spectrum Access
IDLE	0x0D	0x58	R/W	0x01	Idle Mode
WAKEACK	0x0E	0x59	R/W	0x01	UART Acknowledge on Wake
UDESTID3	0x0F	0x5A	R/W	0xFF	Destination Address for User Packet Type, extended
UDESTID2	0x10	0x5B	R/W	0xFF	Destination Address for User Packet Type, extended
UDESTID1	0x11	0x5C	R/W	0xFF	Destination Address for User Packet Type
UDESTID0	0x12	0x5D	R/W	0xFF	Destination Address for User Packet Type
USRCID3	0x13	0x5E	R/W	0xFF	Source Address for User Packet Type, extended
USRCID2	0x14	0x5F	R/W	0xFF	Source Address for User Packet Type, extended
USRCID1	0x15	0x60	R/W	0xFF	Source Address for User Packet Type
USRCID0	0x16	0x61	R/W	0xFF	Source Address for User Packet Type
UMASK3	0x17	0x62	R/W	0xFF	Address Mask for User Packet Type, extended
UMASK2	0x18	0x63	R/W	0xFF	Address Mask for User Packet Type, extended
UMASK1	0x19	0x64	R/W	0xFF	Address Mask for User Packet Type
UMASK0	0x1A	0x65	R/W	0xFF	Address Mask for User Packet Type
DESTDSN3	0x1D	0x68	R/W	0xFF	Destination Device Serial Number
DESTDSN2	0x1E	0x69	R/W	0xFF	Destination Device Serial Number
DESTDSN1	0x1F	0x6A	R/W	0xFF	Destination Device Serial Number
DESTDSN0	0x20	0x6B	R/W	0xFF	Destination Device Serial Number

CRCERRS - CRC Error Count

Volatile Address = 0x40

The value in the CRCERRS register is incremented each time a packet with a valid header is received that fails the CRC check on the payload. This check applies only to unencrypted packets. Overflows are ignored. Writing 0x00 to this register initializes the count. Figure 31 shows the command and response.

HumPRC™ Series CRC Error Count							
Read Command				Read Response			
Header	Size	Escape	Address	ACK	Address	Value	
0xFF	0x02	0xFE	0x40	0x06	0x40	V	
Write Command							
Header	Size	Address	Value				
0xFF	0x02	0x40	V				

Figure 31: HumPRC™ Series CRC Error Count Command and Response

HOPTABLE - Channel Hop Table

Volatile Address = 0x4B; Non-Volatile Address = 0x00

The module supports 6 different hop sequences with minimal correlation. The sequence is set by the value in the HOPTABLE register. Changing the hop sequence changes the band utilization, much the same way that a channel does for a non-hopping transmitter. The hop table selection must match between the transmitter and receiver. Valid values are 0-5. The default value of 0xFF must be changed before communication can occur. This is normally done by the Join process. Figure 32 shows the command and response.

HumPRC™ Series Channel Hop Table							
Read Command				Read Response			
Header	Size	Escape	Address	ACK	Address	Value	
0xFF	0x02	0xFE	0x4B 0x00	0x06	0x4B 0x00	V	
Write Command							
Header	Size	Address	Value				
0xFF	0x02	0x4B 0x00	V				

Figure 32: HumPRC™ Series Channel Hop Table Command and Response

HumPRC™ Series Hop Sequences by Channel Number

Channel Index	0	1	2	3	4	5
1	52	50	40	67	13	42
2	42	67	25	38	49	20
3	45	24	54	14	60	4
4	50	49	1	4	28	9
5	18	42	43	49	35	26
6	11	61	60	20	42	54
7	67	28	66	57	67	51
8	28	51	16	65	32	40
9	62	10	29	34	5	49
10	48	21	58	39	16	37
11	51	16	30	12	69	36
12	29	29	64	40	7	63
13	33	54	68	60	9	25
14	31	59	56	53	52	7
15	24	3	36	31	66	55
16	22	17	47	32	24	31
17	15	64	38	47	17	62
18	5	40	19	30	4	46
19	8	60	51	0	39	39
20	68	18	33	17	0	2
21	32	37	46	9	3	43
22	12	63	55	43	26	41
23	43	48	61	33	30	10
24	34	46	12	58	29	35
25	69	57	17	59	51	60
26	13	47	21	46	65	38
27	57	5	9	54	68	30
28	0	34	4	7	36	68
29	10	22	2	26	58	21
30	2	9	14	22	22	61
31	39	4	52	1	12	5
32	59	0	0	61	34	1
33	46	45	3	10	53	59
34	49	23	67	35	6	22
35	65	53	65	21	2	19
36	7	30	24	62	33	8
37	66	7	28	36	47	53
38	21	2	42	16	45	67
39	3	31	41	66	23	14
40	4	15	53	69	61	12
41	23	66	69	6	21	16
42	25	11	35	3	20	11
43	26	36	45	56	41	0
44	19	27	7	37	64	44
45	6	14	34	15	62	69
46	54	44	15	41	10	17
47	53	55	49	51	31	48
48	9	19	26	50	48	57
49	16	39	62	44	56	24

TXPWR - Transmitter Output Power

Volatile Address = 0x4D; Non-Volatile Address = 0x02

The value in the TXPWR register sets the module's output power. Figure 35 shows the command and response and Figure 36 available power settings and typical power outputs for the module. The default setting is 0x03.

HumPRC™ Series Transmitter Output Power Mode						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x4D 0x02	0x06	0x4D 0x02	PWR
Write Command						
Header	Size	Address	Value			
0xFF	0x02	0x4D 0x02	PWR			

Figure 35: HumPRC™ Series Transmitter Output Power Mode Command and Response

HumPRC™ Series Transmitter Output Power Mode Register Settings	
PWR	Typical Output Power (dBm)
0x00	-5
0x01	0
0x02	+5
0x03	+9*

* The maximum output power is limited on the upper channels to comply with ETSI regulations.

Figure 36: HumPRC™ Series Transmitter Output Power Mode Settings

ADDMODE - Addressing Mode

Volatile Address = 0x4F; Non-Volatile Address = 0x04

This register is controlled automatically by the HumPRC™ application and the Join Process, so should not be changed by external commands.

DATATO - Transmit Wait Timeout

Volatile Address = 0x50; Non-Volatile Address = 0x05

This register selects options for transferring packet data in the HumPRO™ Series. These options are controlled automatically by the HumPRC™ application and do not have any effect on its operation.

MAXTXRETRY - Maximum Transmit Retries

Volatile Address = 0x52; Non-Volatile Address = 0x07

The value in the MAXTXRETRY register sets the number of transmission retries performed if an acknowledgement is not received. If an acknowledgement is not received after the last retry, exception EX_NORFACK is raised. Figure 39 shows examples of the command.

HumPRC™ Series Maximum Transmit Retries						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x52 0x07	0x06	0x52 0x07	V
Write Command						
Header	Size	Address	Value			
0xFF	0x02	0x52 0x07	V			

Figure 39: HumPRC™ Series Maximum Transmit Retries Command and Response

The time between retries depends on the current baud rate. Figure 40 shows the time between retries based on baud rate. The elapsed transmit and acknowledgment time is $(\text{retries} + 1) \times (\text{PacketTransmitTime} + \text{Timeout})$.

HumPRC™ Series Acknowledgement Timeout Times	
Baud Rate	Timeout Time
9,600	50ms
19,200	50ms
38,400	30ms
57,600	30ms
115,200	30ms

Figure 40: HumPRC™ Series Acknowledgement Timeout Times

ENCSMA - Polite Spectrum Access Enable

Volatile Address = 0x56; Non-Volatile Address = 0x0B

Carrier-Sense Multiple Access (CSMA), also called Listen Before Talk (LBT) or Polite Spectrum Access (PSA), is a best-effort transmission protocol that listens to the channel before transmitting a message. See the [Polite Spectrum Access](#) section for details. Figure 43 shows examples of the commands and Figure 44 shows the available values.

HumPRC™ Series CSMA Enable						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x56 0x0B	0x06	0x56 0x0B	V
Write Command						
Header	Size	Address	Value			
0xFF	0x02	0x56 0x0B	V			

Figure 43: HumPRC™ Series CSMA Enable Command and Response

HumPRC™ Series CSMA Enable Register Settings	
V	Mode
0x00	Disable CSMA
0x01	Enable CSMA with no duty cycle restrictions
0x02	Enable CSMA with duty cycle restrictions for full ETSI compliance

Figure 44: HumPRC™ Series CSMA Enable Register Settings

Setting this register to 0x00 disables PSA, causing an immediate transmission when a data packet is ready. The application must limit the duty cycle to <0.1%, since this mode does not conform to ETSI PSA.

Setting this register to 0x01 enables LBT without the duty cycle constraints. For ETSI conformance, the external device is responsible for ensuring that the transmitter duty cycle is <10%. The module evenly divides transmission time among un-busy channels.

Setting the register to 0x02 enables PSA and a limitation of 33.3s per channel per hour to meet ETSI standards. This is achieved for each channel by limiting the amount of transmit or response time within a 180s bandspread interval. This register must be 0x02 to meet the module's Declaration of Conformity (DoC).

WAKEACK - ACK on Wake

Volatile Address = 0x59; Non-Volatile Address = 0x0E

When UART Acknowledge on Wake is enabled, the module sends an ACK (0x06) character out of the CMD_DATA_OUT line after the module resets or wakes from sleep. This indicates that the module is ready to accept data and commands. A value of 0x01 enables this feature; 0x00 disables it. The default value is 0x01. Figure 47 shows examples of the commands and Figure 48 shows the available values.

HumPRC™ Series ACK on Wake						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x59 0x0E	0x06	0x59 0x0E	V
Write Command						
Header	Size	Address	Value			
0xFF	0x02	0x59 0x0E	V			

Figure 47: HumPRC™ Series ACK on Wake Command and Response

HumPRC™ Series ACK on Wake Register Settings	
V	Mode
0x00	Disable ACK
0x01	Enable ACK

Figure 48: HumPRC™ Series ACK on Wake Register Settings

USRCID - User Source Address

Volatile Address = 0x5E-0x61; Non-Volatile Address = 0x13-0x16

These registers contain the address of the module when User Addressing mode or Extended User Addressing mode are enabled. User Addressing mode uses bytes 0 and 1 to determine the source address for both transmitted messages and matching received messages. Extended User Addressing mode uses all four bytes. When the **COMPAT** register is 0x02 in User Address mode, bytes 3 and 2 must be 0. Each register byte is read and written separately. Figure 50 shows the User Source ID registers.

HumPRC™ Series User Source Address Registers			
Name	Volatile Address	Non-Volatile Address	Description
USRCID3	0x5E	0x13	MSB of the extended source address
USRCID2	0x5F	0x14	Byte 2 of the extended source address
USRCID1	0x60	0x15	Byte 1 of the extended source address MSB of the short source address
USRCID0	0x61	0x16	LSB of the extended source address and short source address

Figure 50: HumPRC™ Series User Source Address Registers

DESTDSN - Destination Serial Number

Volatile Address = 0x68-0x6B; Non-Volatile Address = 0x1D-0x20

These registers contain the serial number of the destination module when DSN Addressing Mode is enabled. Each register byte is read and written separately.

Figure 52 shows the Destination DSN registers.

HumPRC™ Series Destination DSN Registers			
Name	Volatile Address	Non-Volatile Address	Description
DESTDSN3	0x68	0x1D	MSB of the destination DSN
DESTDSN2	0x69	0x1E	Byte 2 of the destination DSN
DESTDSN1	0x6A	0x1F	Byte 1 of the destination DSN
DESTDSN0	0x6B	0x20	LSB of the destination DSN

Figure 52: HumPRC™ Series Destination DSN Registers

CMDHOLD - $\overline{\text{CMD}}$ Halts Traffic

Volatile Address = 0x6E; Non-Volatile Address = 0x23

This register selects options for transferring packet data in the HumPRO™ Series. These options are controlled automatically by the HumPRC™ application and do not have any effect on its operation.

COMPAT - Compatibility Mode

Volatile Address = 0x70; Non-Volatile Address = 0x25

This register selects options for transferring packet data in the HumPRO™ Series. These options are controlled automatically by the HumPRC™ application and do not have any effect on its operation.

AUTOADDR - Auto Addressing

Volatile Address = 0x71; Non-Volatile Address = 0x26

This register is controlled automatically by the HumPRC™ application, so should not be changed by external commands.

MYDSN - Local Device Serial Number

Non-Volatile Address = 0x34-0x37

These registers contain the factory-programmed read-only Device Serial Number. This address is unique for each module and is included in all packet types as a unique origination address.

Figure 57 shows the Device Serial Number registers.

HumPRC™ Series DSN Registers		
Name	Non-Volatile Address	Description
MYDSN3	0x34	MSB of the serial number
MYDSN2	0x35	Byte 2 of the serial number
MYDSN1	0x36	Byte 1 of the serial number
MYDSN0	0x37	LSB of the serial number

Figure 57: HumPRC™ Series DSN Registers

CUSTID - Customer ID

Non-Volatile Address = 0x39-0x3A

These registers are not used by the HumPRC™ application and do not have any effect on its operation. Figure 58 shows the Customer ID registers.

HumPRC™ Series Customer ID Registers		
Name	Non-Volatile Address	Description
CUSTID1	0x39	MSB of the customer ID
CUSTID0	0x3A	LSB of the customer ID

Figure 58: HumPRC™ Series Transceiver Customer ID Registers

RELEASE - Release Number

Non-Volatile Address = 0x78

This register contains a number designating the firmware series and hardware platform. Figure 60 shows examples of the commands and Figure 61 lists current releases to date.

HumPRC™ Series Release Number						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x78	0x06	0x78	V

Figure 60: HumPRC™ Series Transceiver Release Number Command and Response

HumPRC™ Series Release Number Register Settings	
V	Release Number
0x24	HUM-868-PRC
0x25	HUM-900-PRC

Figure 61: HumPRC™ Series Transceiver Release Number Register Settings

A more detailed firmware version is available for versions 0x20 and above in the FWVER register.

PRSSI - Last Good Packet RSSI

Volatile Address = 0x7B

This register holds the received signal strength in dBm of the last successfully received packet. A successful packet reception is one that causes payload data to be output on the UART interface. The value in this register is overwritten each time a new packet is successfully processed. The register value is an 8-bit signed integer representing the RSSI in dBm. It is accurate to ± 3 dB.

HumPRC™ Series Last Good Packet RSSI						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x7B	0x06	0x7B	V

Figure 63: HumPRC™ Series Transceiver Last Good Packet RSSI Command and Response

ARSSI - Ambient RSSI

Volatile Address = 0x7C

This register returns the ambient receive signal strength on the current channel in dBm. The signal strength is measured as soon as the command is received. The register value is an 8-bit signed integer representing the RSSI in dBm. It is accurate to ± 3 dB at the high RF data rate, and +3 to -20 dB at the low RF data rate. The channel being read may be any of the channels in the selected hopping sequence.

HumPRC™ Series Ambient RSSI						
Read Command				Read Response		
Header	Size	Escape	Address	ACK	Address	Value
0xFF	0x02	0xFE	0x7C	0x06	0x7C	V

Figure 64: HumPRC™ Series Transceiver Ambient RSSI Command and Response

NVCYCLE - Non-Volatile Refresh Cycles

Non-Volatile Address = 0xC4-0xC5

These read-only non-volatile registers contain the number of lifetime refresh cycles performed for the non-volatile memory. The minimum lifetime refreshes is 2,000 refresh cycles. Beyond this the refreshes may not be complete and the module's operation can become unpredictable.

HumPRC™ Series Non-Volatile Refresh Cycles Registers

Name	Non-Volatile Address	Description
NVCYCLE1	0xC4	MSB of the number of refresh cycles
NVCYCLE0	0xC5	LSB of the number of refresh cycles

Figure 66: HumPRC™ Series Non-Volatile Refresh Cycles Registers

Between 8 and 150 non-volatile write operations can be made before a refresh cycle is necessary. Writing the registers from lowest to highest address maximizes the number of write operations per refresh cycle.

It is recommended to write the desired default values to non-volatile memory and use the volatile registers for values that change frequently.

These registers show the total number of refresh cycles that have occurred. This gives an indication of the remaining life expectancy of the memory. Figure 66 shows the Non-Volatile Refresh Cycles registers.

CMD - Command Register

Volatile Address = 0xC7

This volatile write-only register is used to issue special commands.

HumPRC™ Series Command Register				
Write Command				
Header	Size	Escape	Address	Value
0xFF	Size	0xFE	0x47	V

Figure 69: HumPRC™ Series Transceiver Command Register Command and Response

Value V is chosen from among the options in Figure 70.

HumPRC™ Series CMD Values	
CMD Value	Operation
0x10	JOINCTL – Join Process Control
0x11	WRKEY – Write Key
0x12	CLRKEY – Clear Key
0x13	RLDKEY – Reload Key
0x20 0xAA 0xBB	NVRESET – Reset non-volatile registers to factory default

Figure 70: HumPRC™ Series Command Register Values

The **Join Process Control** command allows the software to initiate or stop the secure [Join Process](#). It has the following subcommands.

HumPRC™ Series JOINCTL Subcommand Values	
Subcommand Value	Operation
0	Halt Join Process
1	Generate a random network key and address. This sets the module as the network administrator (SECOPT:KEYRCV=0)
2	Perform the Join Process with another module

Figure 71: HumPRC™ Series JOINCTL Subcommand Values

These operations are equivalent to the push-button initiated operation. If the Join Process is started by the serial command (CMD:JOINCTL[2]), push-button operation is ignored until the Join Process finishes.

Register write operations are inhibited when the Join Process is active except that a Halt Join command is never inhibited. A Halt Join operation completes before the ACK is sent.

The **Reload Key** command copies the key in non-volatile memory (NKN) to the volatile location (NKV). This allows a sophisticated system to change the keys during operation and quickly revert back to the default key.

The **Non-volatile Reset** command (FF 07 FE 47 20 FE 2A FE 3B) sets all non-volatile registers to their default values. When the configuration is reset, the following message, shown in quotes, is sent out the UART at the current baud rate, then the module is reset, similar to a power cycle:

“\r\nConfiguration Reset\r\n”.

This reset can also be done by toggling the PB line as described in the [Restore Factory Defaults](#) section.

SECSTAT - Security Status

Volatile Address = 0xC9

This volatile read-only register provides status of the security features.

HumPRC™ Series Security Status							
Read Command					Read Response		
Header	Size	Escape	Escape	Address	ACK	Address	Value
0xFF	0x03	0xFE	0xFE	0xC9	0x06	0xC9	V

Figure 74: HumPRC™ Series Transceiver Security Status Command and Response

The command returns a single byte. Figure 75 shows the meanings of the bits in the returned value byte.

HumPRC™ Series Security Status Value	
Bit	Status
0	Reserved
1	0 = No volatile key is set 1 = A volatile key is set
2	0 = No non-volatile key is set 1 = A non-volatile key is set
3	Reserved
4	Reserved
5	Reserved
6	Reserved
7	Reserved

Figure 75: HumPRC™ Series Security Status Values

EEXFLAG - Extended Exception Flags

Volatile Address = 0xCD - 0xCF

These volatile registers contain flags for various events. Similar to the EXCEPT register, they provide a separate bit for each exception.

HumPRC™ Series Extended Exception Flags Registers		
Name	Volatile Address	Description
EEXFLAG2	0xCD	Byte 2 of the extended exception flags
EEXFLAG1	0xCE	Byte 1 of the extended exception flags
EEXFLAG0	0xCF	LSB of the extended exception flags

Figure 78: HumPRC™ Series Transceiver Extended Exception Code Registers

When an exception occurs, the associated bit is set in this register. If the corresponding bit in the EEXMASK is set and EXMASK is zero, the EX status line is set. Reading an EEXFLAG register does not clear the register.

Writing to an EEXFLAG register causes the register to be set to the BIT_AND(current_value, new_value). This provides a way of clearing bits that have been serviced without clearing a bit that has been set since the flag register was read. This prevents a loss of notification of an exception.

Register bits can only be cleared, not set, from the write command though some flags are also cleared internally. Unless otherwise noted, exceptions are cleared by writing a zero to the corresponding register bit.

Flag **EX_TXDONE** is set when a data packet has been transmitted. If the packet was sent with acknowledgement enabled, this flag indicates that the acknowledgment has also been received.

Flag **EX_RXWAIT** is 1 when there are buffered incoming data bytes which have not been sent to the UART. It is cleared by reading or discarding all data bytes.

Flag **EX_UNENCRYPT** is 1 when a received packet is not encrypted. This can only occur when SECOPT:EN_UNC=1.

Flag **EX_SEQDEC** is 1 when a received encrypted packet has a smaller sequence number than the previously received packet. Possible causes are an attempt to replay a previous message by an attacker, receiving a message from a different transmitter or restarting the transmitter.

EEXMASK - Extended Exception Mask

Volatile Address = 0xD0-0xD2; Non-Volatile Address = 0x80-0x82

These registers contain a mask for the events in EEXFLAG, using the same offset and bit number.

HumPRC™ Series Extended Exception Mask Registers			
Name	Volatile Address	Non-Volatile Address	Description
EEXMASK2	0xD0	0x80	Byte 2 of the extended exception mask
EEXMASK1	0xD1	0x81	Byte 1 of the extended exception mask
EEXMASK0	0xD2	0x82	LSB of the extended exception mask

Figure 80: HumPRC™ Series Transceiver Extended Exception Mask Registers

To use this value, register EXMASK must be zero. If EXMASK is non-zero, this register has no effect on the EX line.

When an exception bit is set in EEXFLAG, the corresponding EEXMASK bit is set, and EXMASK is zero, the EX status line is set, otherwise the EX line is reset. Mask bits for unassigned flags should be zero for future compatibility.

PKTOPT - Packet Options

Volatile Address = 0xD3; Non-Volatile Address = 0x83

This register selects options for transferring packet data in the HumPRO™ Series. These options are controlled automatically by the HumPRC™ application and do not have any effect on its operation.

SECOPT - Security Options

Volatile Address = 0xD4; Non-Volatile Address = 0x84

This register selects options for security features.

HumPRC™ Series Security Options									
Read Command					Read Response				
Header	Size	Escape	Escape	Address	ACK	Address	Value		
0xFF	0x03	0xFE	0xFE	0x54 0x04	0x06	0xD4 0x84	V		
Write Command									
Header	Size	Escape	Address	Value					
0xFF	0x03	0xFE	0x54 0x04	V					

Figure 82: HumPRC™ Series Transceiver Packet Options Command and Response

Each bit in the register sets an option as shown in Figure 83. Unlike other registers, the non-volatile register (0x84) affects all Join operations. The EN_UNENC bit in the volatile register affects data packet reception.

HumPRC™ Series Transceiver Security Option Codes		
Bit	Name	Description
0	PB_RESET	Permit factory reset from PB input sequence
1	PSHARE	Permit key sharing
2	PGKEY	Permit clearing key and changing key
3	CHGADDR	Permit changing an address
4	KEYRCV	1: Receive key and address during Join Process (node) 0: Send key and address during Join Process (admin)
5	EN_UNENC	Enable receiving unencrypted packets
6	Reserved	Reserved (must be 1)
7	EN_CHANGE	Enable changes to security options

Figure 83: HumPRC™ Series Transceiver Security Option Codes

When PB_RESET is 1 the Factory Reset function is enabled from the PB input. This allows a user to reset the module configurations back to the factory defaults with 4 short presses and a 3 second hold of a button connected to the PB input.

When PSHARE is 1 the Share Network Key function is enabled during the Join Process. This allows an administrator to share the encryption key it created. When 0, a Join Process sends the network address, but no key.

Typical Applications

The following steps describe how to use the HumPRC™ Series module with hardware only.

1. Set the C0 and C1 lines opposite on both sides.
2. Press and hold the PB button for 30s on the unit chosen as Administrator. When MODE_IND flashes, release PB. The unit is set as the Administrator.
3. Press the PB button on both sides. The MODE_IND LED begins flashing slowly to indicate that the module is searching for another module.
4. Once the pairing is complete, the MODE_IND LED flashes quickly to indicate that the pairing was successful.
5. The modules are now paired and ready for normal use.
6. Pressing a status line button on one module (the IU) activates the corresponding status line output on the second module (the RU).
7. Taking the ACK_EN line high on the RU causes the module to send an acknowledgement to the IU. The ACK_OUT line on the IU goes high to indicate that the acknowledgement has been received. Tying the line to V_{cc} causes the module to send an acknowledgement as soon as a command message is received.

This is suitable for basic remote control or command systems. No programming is necessary for basic hardware operation.

Basic application circuits for one-way remote control are shown in Figure 84. Circuits for bi-directional remote control are shown in Figure 85.

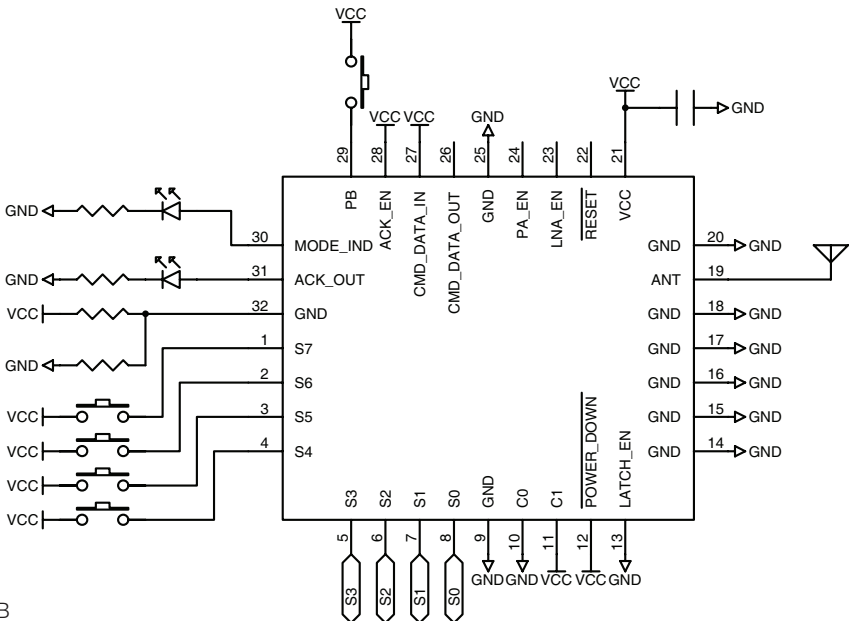
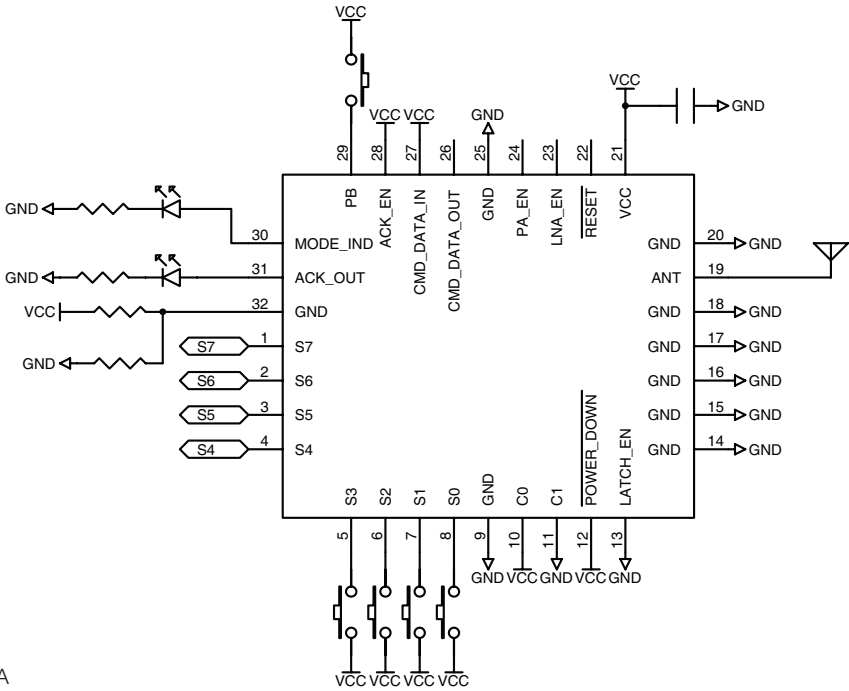


Figure 85: HumPRC™ Series Transceiver Basic Application Circuits for Bi-directional Remote Control

HumPRC™ Series Long-Range Handheld Transmitter

The HumPRC™ Series Long-Range Handheld Transmitter is ideal for general-purpose remote control and command applications. It incorporates the HumPRC™ Series remote control transceiver, antenna and a coin-cell battery into a plastic enclosure. A membrane switch array is used to activate the unit. An LED embedded into the membrane switch indicates acknowledgement from the remote device. It has a transmission range of up to 1,300m (0.8 mile) depending on the receiver antenna and operating environment.



The transmitter is available in 868MHz and 900MHz for multi-region operation. The 868MHz version has been tested to European ETSI requirements and received its CE mark. The 900MHz version has been certified by the United States FCC and Industry Canada. This reduces development costs and time to market.



The membrane switch array can be customized to have specific artwork, logos, colors, number of buttons (up to eight) and button positions. A one-time NRE is required to create the custom switch, but minimum order quantities can be as low as 200

pieces. Contact Linx for more information.

Ordering Information

Part Number	Description
OTX-***-HH-LR8-PRC	HumPRC™ Long-Range Handheld Transmitter

*** = 868, 900MHz

Figure 87: HumPRC™ Series Long-Range Handheld Transmitter Ordering Information

Power Supply Requirements

The module does not have an internal voltage regulator, therefore it requires a clean, well-regulated power source. The power supply noise should be less than 20mV. Power supply noise can significantly affect the module's performance, so providing a clean power supply for the module should be a high priority during design.

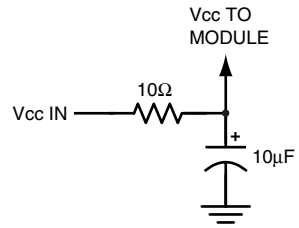


Figure 88: Supply Filter

A 10Ω resistor in series with the supply followed by a 10μF tantalum capacitor from V_{cc} to ground helps in cases where the quality of supply power is poor (Figure 88). This filter should be placed close to the module's supply lines. These values may need to be adjusted depending on the noise present on the supply line.

Antenna Considerations

The choice of antennas is a critical and often overlooked design consideration. The range, performance and legality of an RF link are critically dependent upon the antenna. While adequate antenna performance can often be obtained by trial and error methods, antenna design and matching is a complex task. Professionally designed antennas such as those from Linx (Figure 89) help ensure maximum performance and FCC and other regulatory compliance.



Figure 89: Linx Antennas

Linx transmitter modules typically have an output power that is higher than the legal limits. This allows the designer to use an inefficient antenna such as a loop trace or helical to meet size, cost or cosmetic requirements and still achieve full legal output power for maximum range. If an efficient antenna is used, then some attenuation of the output power will likely be needed.

It is usually best to utilize a basic quarter-wave whip until your prototype product is operating satisfactorily. Other antennas can then be evaluated based on the cost, size and cosmetic requirements of the product. Additional details are in Application Note AN-00500.

Pad Layout

The pad layout diagrams below are designed to facilitate both hand and automated assembly. Figure 90 shows the footprint for the smaller version and Figure 91 shows the footprint for the encapsulated version.

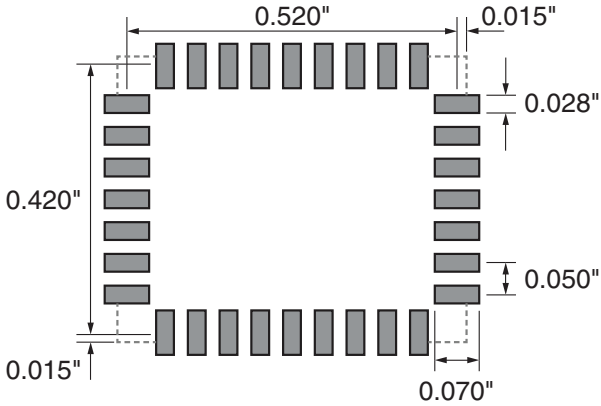


Figure 90: HUM-***-PRC Recommended PCB Layout

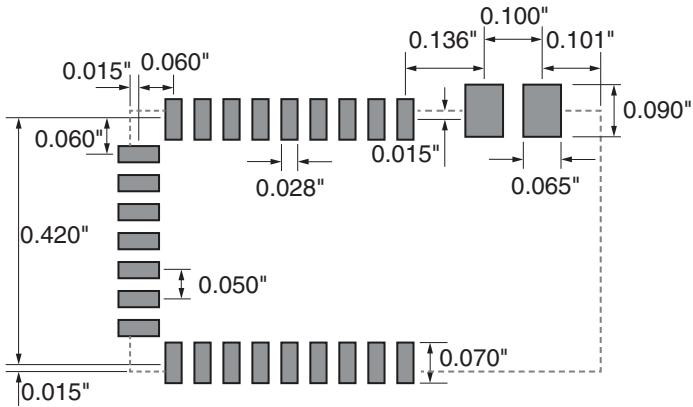


Figure 91: HUM-***-PRC-UFL/CAS Recommended PCB Layout

Board Layout Guidelines

The module's design makes integration straightforward; however, it is still critical to exercise care in PCB layout. Failure to observe good layout techniques can result in a significant degradation of the module's performance. A primary layout goal is to maintain a characteristic 50-ohm impedance throughout the path from the antenna to the module. Grounding, filtering, decoupling, routing and PCB stack-up are also important considerations for any RF design. The following section provides some basic design guidelines.

During prototyping, the module should be soldered to a properly laid-out circuit board. The use of prototyping or “perf” boards results in poor performance and is strongly discouraged. Likewise, the use of sockets can have a negative impact on the performance of the module and is discouraged.

The module should, as much as reasonably possible, be isolated from other components on your PCB, especially high-frequency circuitry such as crystal oscillators, switching power supplies, and high-speed bus lines.

When possible, separate RF and digital circuits into different PCB regions.

Make sure internal wiring is routed away from the module and antenna and is secured to prevent displacement.

Do not route PCB traces directly under the module. There should not be any copper or traces under the module on the same layer as the module, just bare PCB. The underside of the module has traces and vias that could short or couple to traces on the product's circuit board.

The Pad Layout section shows a typical PCB footprint for the module. A ground plane (as large and uninterrupted as possible) should be placed on a lower layer of your PC board opposite the module. This plane is essential for creating a low impedance return for ground and consistent stripline performance.

Use care in routing the RF trace between the module and the antenna or connector. Keep the trace as short as possible. Do not pass it under the module or any other component. Do not route the antenna trace on multiple PCB layers as vias add inductance. Vias are acceptable for tying together ground layers and component grounds and should be used in multiples.

Production Guidelines

The module is housed in a hybrid SMD package that supports hand and automated assembly techniques. Since the modules contain discrete components internally, the assembly procedures are critical to ensuring the reliable function of the modules. The following procedures should be reviewed with and practiced by all assembly personnel.

Hand Assembly

Pads located on the bottom of the module are the primary mounting surface (Figure 95). Since these pads are inaccessible during mounting, castellations that run up the side of the module have been provided to facilitate solder wicking to the module's underside. This allows for very

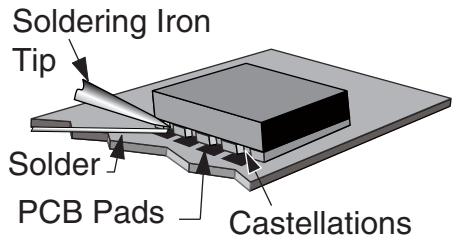


Figure 95: Soldering Technique

quick hand soldering for prototyping and small volume production. If the recommended pad guidelines have been followed, the pads will protrude slightly past the edge of the module. Use a fine soldering tip to heat the board pad and the castellation, then introduce solder to the pad at the module's edge. The solder will wick underneath the module, providing reliable attachment. Tack one module corner first and then work around the device, taking care not to exceed the times in Figure 96.

Warning: Pay attention to the absolute maximum solder times.

Absolute Maximum Solder Times

Hand Solder Temperature: +427°C for 10 seconds for lead-free alloys

Reflow Oven: +255°C max (see Figure 97)

Figure 96: Absolute Maximum Solder Times

Automated Assembly

For high-volume assembly, the modules are generally auto-placed. The modules have been designed to maintain compatibility with reflow processing techniques; however, due to their hybrid nature, certain aspects of the assembly process are far more critical than for other component types. Following are brief discussions of the three primary areas where caution must be observed.

General Antenna Rules

The following general rules should help in maximizing antenna performance.

1. Proximity to objects such as a user's hand, body or metal objects will cause an antenna to detune. For this reason, the antenna shaft and tip should be positioned as far away from such objects as possible.
2. Optimum performance is obtained from a $\frac{1}{4}$ - or $\frac{1}{2}$ -wave straight whip mounted at a right angle to the ground plane (Figure 98). In many cases, this isn't desirable for practical or ergonomic reasons, thus, an alternative antenna style such as a helical, loop or patch may be utilized and the corresponding sacrifice in performance accepted.

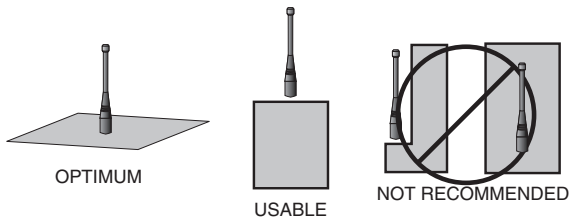


Figure 98: Ground Plane Orientation

3. If an internal antenna is to be used, keep it away from other metal components, particularly large items like transformers, batteries, PCB tracks and ground planes. In many cases, the space around the antenna is as important as the antenna itself. Objects in close proximity to the antenna can cause direct detuning, while those farther away will alter the antenna's symmetry.
4. In many antenna designs, particularly $\frac{1}{4}$ -wave whips, the ground plane acts as a counterpoise, forming, in essence, a $\frac{1}{2}$ -wave dipole (Figure 99). For this reason, adequate ground plane area is essential. The ground plane can be a metal case or ground-fill areas on a circuit board. Ideally, it should have a surface area less than or equal to the overall length of the $\frac{1}{4}$ -wave radiating element. This is often not practical due to size and configuration constraints. In these instances, a designer must make the best use of the area available to create as much ground

VERTICAL $\lambda/4$ GROUNDED ANTENNA (MARCONI)

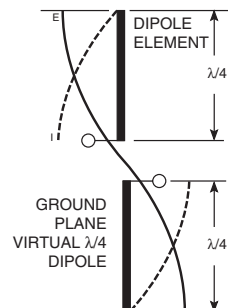


Figure 99: Dipole Antenna

Common Antenna Styles

There are hundreds of antenna styles and variations that can be employed with Linx RF modules. Following is a brief discussion of the styles most commonly utilized. Additional antenna information can be found in Linx Application Notes AN-00100, AN-00140, AN-00500 and AN-00501. Linx antennas and connectors offer outstanding performance at a low price.

Whip Style

A whip style antenna (Figure 101) provides outstanding overall performance and stability. A low-cost whip can be easily fabricated from a wire or rod, but most designers opt for the consistent performance and cosmetic appeal of a professionally-made model. To meet this need, Linx offers a wide variety of straight and reduced height whip style antennas in permanent and connectorized mounting styles.



Figure 101: Whip Style Antennas

The wavelength of the operational frequency determines an antenna's overall length. Since a full wavelength is often quite long, a partial 1/2- or 1/4-wave antenna is normally employed. Its size and natural radiation resistance make it well matched to Linx modules. The proper length for a straight 1/4-wave can be easily determined using the formula in Figure 102. It is also possible to reduce the overall height of the antenna by using a helical winding. This reduces the antenna's bandwidth but is a great way to minimize the antenna's physical size for compact applications. This also means that the physical appearance is not always an indicator of the antenna's frequency.

$$L = \frac{234}{F_{\text{MHz}}}$$

Figure 102:

L = length in feet of quarter-wave length
F = operating frequency in megahertz

Specialty Styles

Linx offers a wide variety of specialized antenna styles (Figure 103). Many of these styles utilize helical elements to reduce the overall antenna size while maintaining reasonable performance. A helical antenna's bandwidth is often quite narrow and the antenna can detune in proximity to other objects, so care must be exercised in layout and placement.



Figure 103: Specialty Style Antennas

Regulatory Considerations

Note: Linx RF modules are designed as component devices that require external components to function. The purchaser understands that additional approvals may be required prior to the sale or operation of the device, and agrees to utilize the component in keeping with all laws governing its use in the country of operation.

When working with RF, a clear distinction must be made between what is technically possible and what is legally acceptable in the country where operation is intended. Many manufacturers have avoided incorporating RF into their products as a result of uncertainty and even fear of the approval and certification process. Here at Linx, our desire is not only to expedite the design process, but also to assist you in achieving a clear idea of what is involved in obtaining the necessary approvals to legally market a completed product.

The HUM-868-PRO module is an RF transceiver operating in the h1.3 band (ERC 70-03) using AFA +LBT (Also known as Polite Spectrum Access). It falls under Equipment Class I (EN 301 489-3 v1.6.1) and Receiver Category 2 (EN 300 220-1 v3.1.1)

It is important to note that the HUM-868-PRO is not declared as an FHSS system. Rather, it is declared as an RF transceiver using Adaptive Frequency Agility (AFA). The AFA system uses periodic channel adaptivity with 70 available channels.

The HUM-868-PRO-CAS module has been tested and conforms to EN 300 220-1 V3.1.1, EN 300 220-2 v3.1.1, EN 301 489-1 V1.9.2, and EN 301 489-3 v1.6.1.

To maintain validity of the HUM-868-PRO-CAS module Declaration of Conformity (DoC):

1. The module shall not be modified.
2. The system shall use an antenna of a similar type with a gain equal to or less than the antenna that was used during the module testing. Contact Linx Technologies for implementation guidelines.
3. The following module settings must not be changed from their default values and must not be user adjustable:

Regulatory Standards Tested

- Operating Frequency - EN 300 220-1 v3.1.1 (2017-02) Section 5.1.2
- Unwanted Emissions in the Spurious Domain - EN 300 220-1 v3.1.1 (2017-02) Section 5.9.2
- Effective Radiated Power - EN 300 220-1 v3.1.1 (2017-02) Section 5.2.2
- Occupied Bandwidth - EN 300 220-1 v3.1.1 (2017-02) Section 5.6.2
- TX out of band Emissions - EN 300 220-1 v3.1.1 (2017-02) Section 5.8.2
- Transient Power - EN 300 220-1 v3.1.1 (2017-02) Section 5.10.2
- TX Behavior under Low Voltage Conditions - EN 300 220-1 v3.1.1 (2017-02) Section 5.12.2
- RX Sensitivity - EN 300 220-1 v3.1.1 (2017-02) Section 5.14.2
- Blocking - EN 300 220-1 v3.1.1 (2017-02) Section 5.14.2
- Clear Channel Assessment Method - EN 300 220-1 v3.1.1 (2017-02) Section 5.21.2
- Polite Spectrum Access Timing Parameters - EN 300 220-1 v3.1.1 (2017-02) Section 5.21.3
- Adaptive Frequency Agility - EN 300 220-1 v3.1.1 (2017-02) Section 5.21.4
- Indirect ESD, ± 2.0 kV and ± 4.0 kV (HCP and VCP) - EN 301 489-3 v1.6.1
- Radiated RF EM Field Test - EN301 489-3 v1.6.1

Notes



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