

## L3D10065F

### 650V Silicon Carbide Schottky Diode

#### Features

- 650 Volt Schottky Rectifier
- Shorter recovery time
- Highspeed switching possible
- HighFrequency Operation
- TemperatureIndependent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on VF

#### Applications

- HVAC
- Switch Mode Power Supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter Stages
- AC/DC converters

**V<sub>RRM</sub>** = 650 V

**I<sub>F</sub>(T<sub>c</sub>=150°C) = 10 A**

**Q<sub>c</sub> = 28 nC**

#### Benefits

- Higher safety margin against overvoltage
- Improved efficiency all load conditions
- Increased efficiency compared to Silicon Diode alternatives
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway
- Essentially No Switching Losses

#### Package



Type : TO-220F-2L

1、Cathode 2、Anode



#### Maximum Ratings (T<sub>c</sub>= 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage	650	V		
V <sub>RSM</sub>	Surge Peak Reverse Voltage	650	V		
V <sub>DC</sub>	DC Blocking Voltage	650	V		
I <sub>F</sub>	Continuous Forward Current	10	A	T <sub>c</sub> =150°C	Fig. 7
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current	80	A	T <sub>c</sub> =25°C, t <sub>p</sub> =10 ms, Half Sine Wave,	
I <sub>FSM</sub>	Non-Repetitive Peak Forward Surge Current	105	A	T <sub>c</sub> =25°C, t <sub>p</sub> =10ms, Half Sine Wave	
I <sub>F,Max</sub>	Non-Repetitive Peak Forward Surge Current	840	A	T <sub>c</sub> =25°C, t <sub>p</sub> = 10 μs, Pulse	
P <sub>tot</sub>	Power Dissipation	138 60	W	T <sub>c</sub> =25°C T <sub>c</sub> =110°C	Fig. 6
T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature	-55 to +175	°C		

## Electrical Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_F$	Forward Voltage	1.45	1.70	V	$I_F = 10 \text{ A } T_J=25^\circ\text{C}$	Fig. 1
		1.75	2.00		$I_F = 10 \text{ A } T_J=175^\circ\text{C}$	
$I_R$	Reverse Current	2	20	$\mu\text{A}$	$V_R = 650 \text{ V } T_J=25^\circ\text{C}$	Fig. 2
		40	200		$V_R = 650 \text{ V } T_J=175^\circ\text{C}$	
$Q_C$	Total Capacitive Charge	28		nC	$V_R = 400 \text{ V}, I_F = 10 \text{ A}, T_J = 25^\circ\text{C}$	Fig. 4
					$Q_C = \int_0^{V_R} C(V) dV$	
$C$	Total Capacitance	550		pF	$V_R = 0 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	Fig. 3
		53			$V_R = 200 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	
		48			$V_R = 400 \text{ V}, T_J = 25^\circ\text{C}, f = 1 \text{ MHz}$	
$E_C$	Capacitance Stored Energy	7.0		$\mu\text{J}$	$V_R = 400 \text{ V}$	Fig. 5

## Thermal Characteristics

Symbol	Parameter	Typ.	Unit	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	2.18	$^\circ\text{C/W}$	Fig. 8

## Ordering Information

Part Number	Marking	Package	Base qty	Delivery Mode
L3D10065F	L3D10065F	TO-220F-2L	50	Tube

## Typical Performance

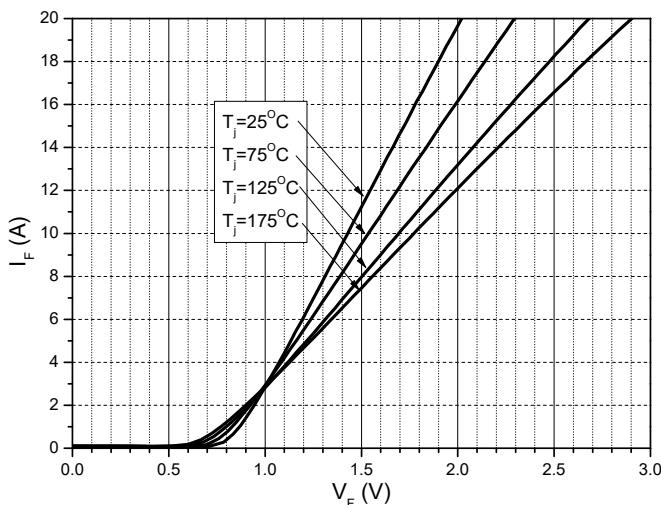


Figure 1. Forward Characteristics

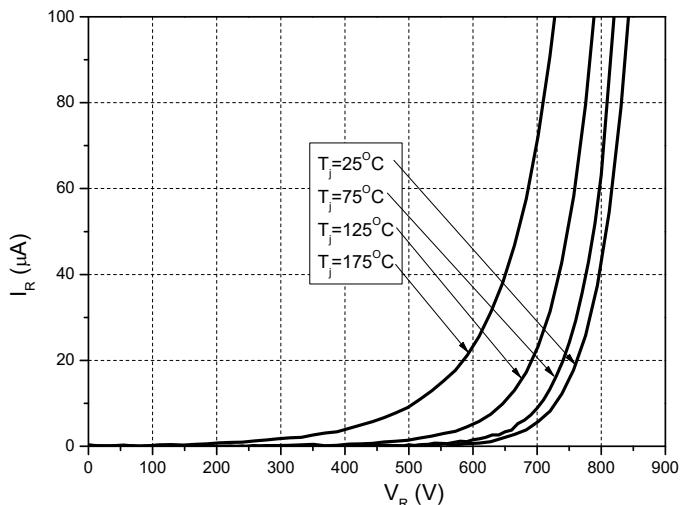


Figure 2. Reverse Characteristics

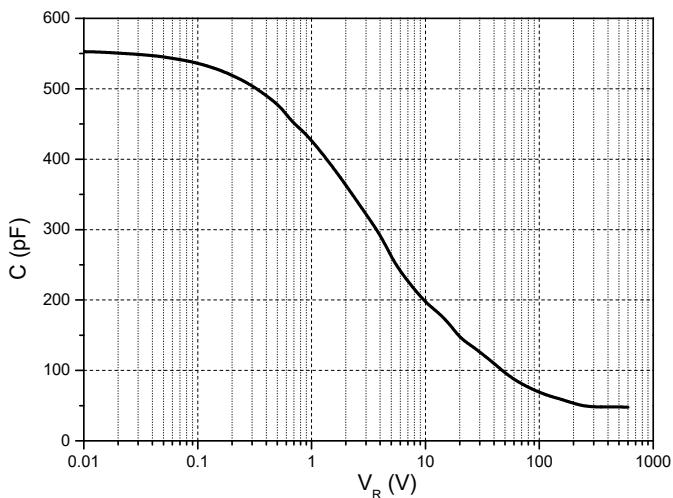


Figure 3. Capacitance vs. Reverse Voltage

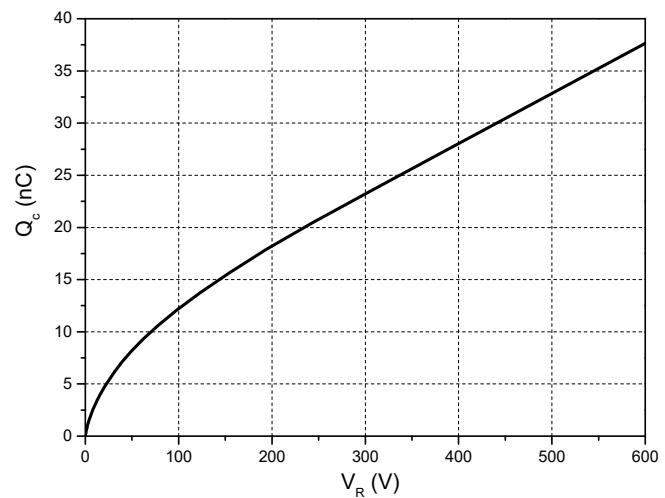


Figure 4. Total Capacitance Charge vs. Reverse Voltage

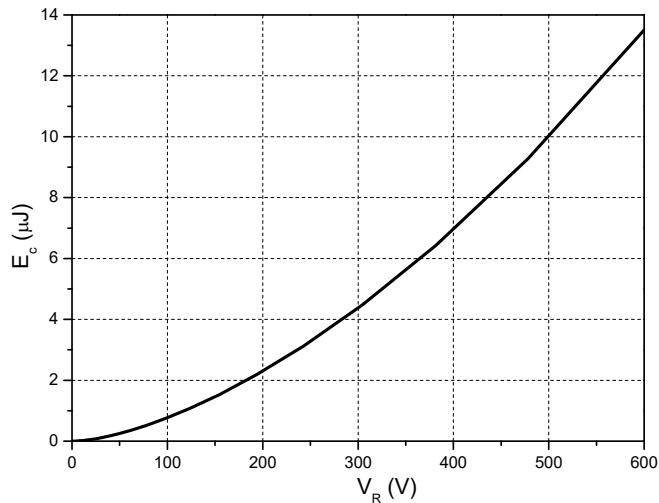


Figure 5. Capacitance Stored Energy

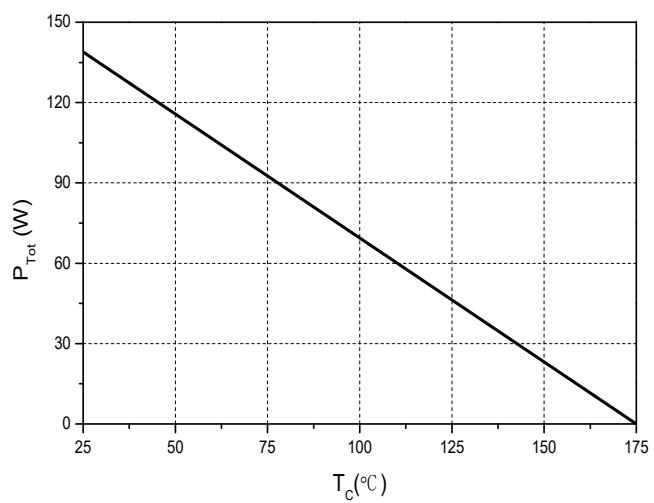


Figure 6. Power Derating

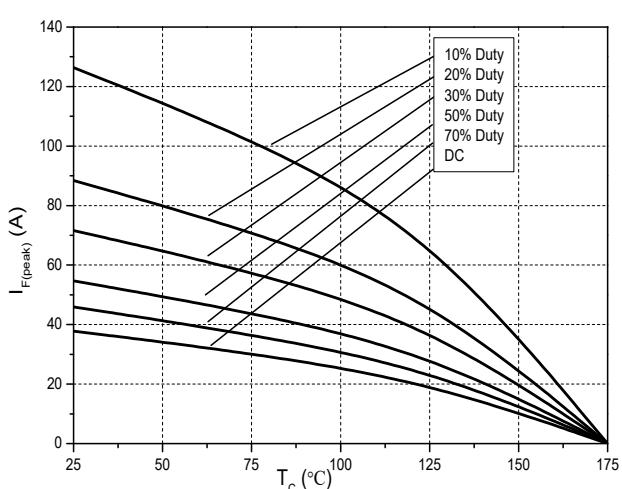


Figure 7. Current Derating

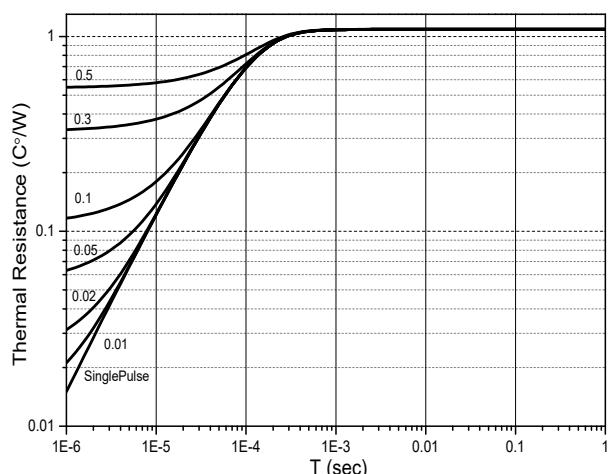
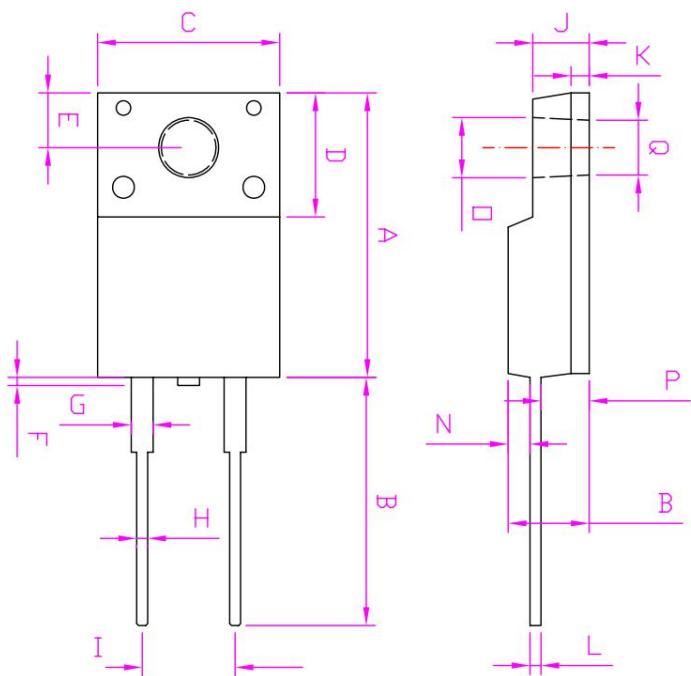


Figure 8. Transient Thermal Impedance

## Package Dimensions:TO-220F-2L



REF.DIM	DATA BOOK mm		
	NOR	MIN	MAX
<b>A</b>	15.6	14.8	16.1
<b>B</b>	13	12.65	13.8
<b>C</b>	10	9.85	10.36
<b>D</b>	6.5	4.6	6.8
<b>E</b>	3.0	2.55	3.5
<b>F</b>			1
<b>G</b>	1.2	1	1.45
<b>H</b>	0.6	0.3	0.9
<b>I</b>	5.1	4.8	5.4
<b>J</b>	3.1	2.34	3.3
<b>K</b>	1.0	0.55	1.3
<b>L</b>	0.6	0.36	0.8
<b>M</b>	4.45	4.2	4.9
<b>N</b>	1.2	1.1	1.8
<b>O</b>	3.3	2.9	3.5
<b>P</b>	2.6	2.5	3.15
<b>Q</b>	3	2.9	3.5

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