



**Spec No.: DS70-2009-0001**Effective Date: 05/12/2009

Revision: -

**LITE-ON DCC** 

**RELEASE** 

BNS-OD-FC001/A4

**Property of Lite-on Only** 

## 6N138, 6N139

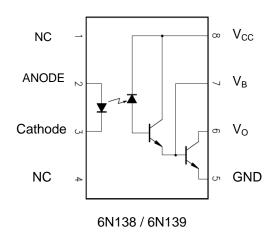
### Single Channel, High Speed Optocouplers



### **Description**

These high gain series couplers use a light emitter diode and an integrated high gain photo detector to provide extremely high current transfer ratio between input and output. Separate pins for the photodiode and output stage result in TTL compatible saturation voltage and high speed operation. Where desired the Vcc and Vo terminals may be tied together to achieve conventional photo darlington operation. A base access terminal allows a gain bandwidth adjustment to be made.

### **Functional Diagram**



Truth Table (Positive Logic)

LED	OUT
ON	L
OFF	Н

A 0.1µF bypass Capacitor must be connected between Pin8 and Pin5



#### **Features**

- High current transfer ratio 2000% typical.
- Low input current requirements 0.5mA
- High output current 60mA
- CTR guarantee 0~70°C.
- Instantaneous common mode rejection 10KV/ μ sec
- TTL compatible output 0.1V V<sub>OL</sub> typical
- UL, CSA approved.

#### **APPLICATIONS**

- Digital logic ground isolation
- Low input current line receiver
- Telephone ring detector
- EIA-RS-232C line receiver
- Current loop receiver
- High common mode noise line receiver

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## **Property of Lite-on Only**

## **Ordering Information**

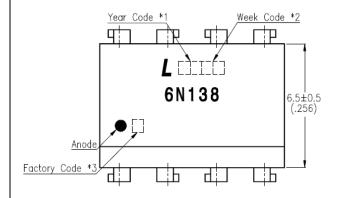
		Minimur	n CMR		
Part	Option	dV/dt (V/μs)	V <sub>CM</sub> (V)	CTR	Remarks
					Single Channel, DIP-8
6N138	M			400	Single Channel, Wide Lead Spacing
	S	1,000	10		Single Channel, SMD-8
		1,000	10		Single Channel, DIP-8
6N139	M			300	Single Channel, Wide Lead Spacing
	S				Single Channel, SMD-8

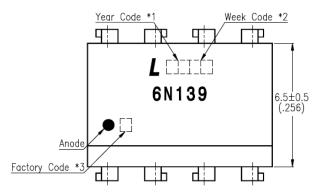
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#### **OUTLINE DIMENSIONS**

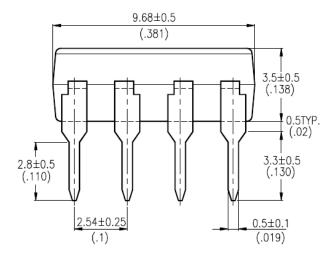
8-pin DIP Package (6N138 / 6N139)

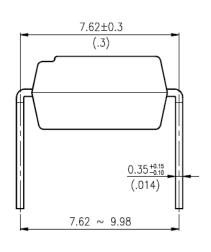




- \*1. Year date code.
- \*2. 2-digit work week.
- \*3. Factory identification mark (Z: Taiwan, Y: Thailand).

Dimensions are in Millimeters and (Inches).



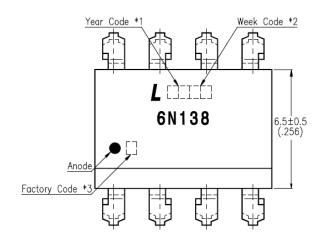


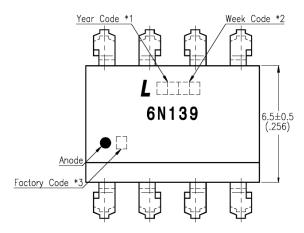
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**Property of Lite-on Only** 

### **OUTLINE DIMENSIONS**

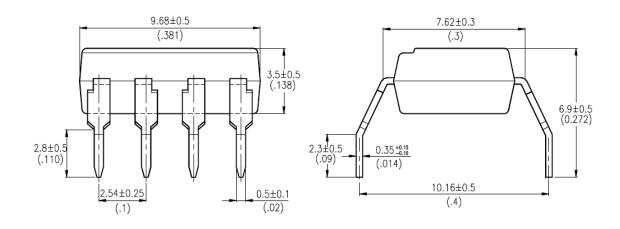
8-pin DIP Wide Lead Spacing Package (6N138M / 6N139M)





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- \*2. 2-digit work week.
- \*3. Factory identification mark (Z: Taiwan, Y: Thailand).

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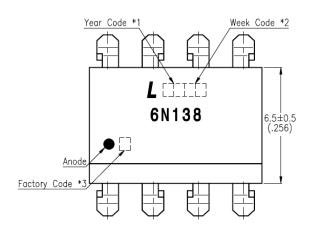
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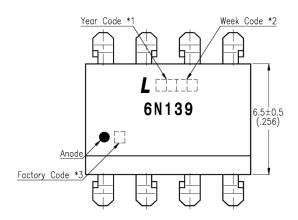
BNS-OD-C131/A4

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#### **OUTLINE DIMENSIONS**

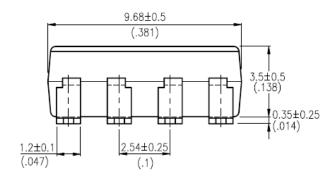
8-pin DIP Surface Mount Package (6N138S / 6N139S)

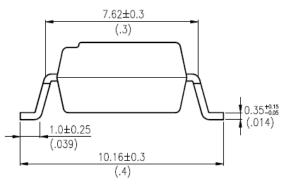




- \*1. Year date code.
- \*2. 2-digit work week.
- \*3. Factory identification mark (Z: Taiwan, Y: Thailand).

Dimensions are in Millimeters and (Inches).



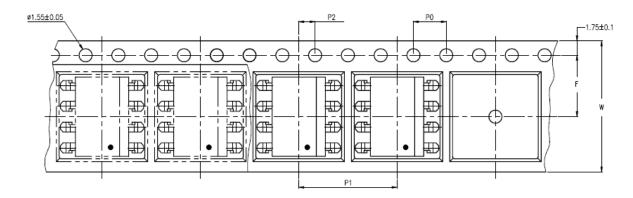


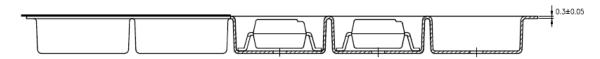
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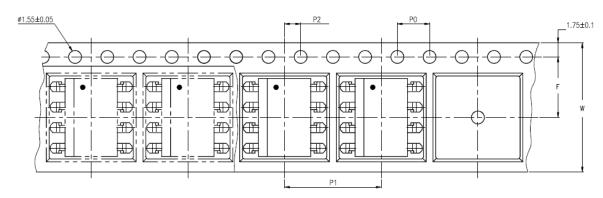
### **TAPING DIMENSIONS**

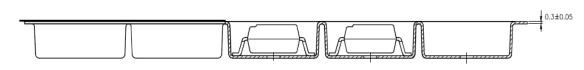
### 6N138S/6N139S-TA





#### 6N138S/6N139S-TA1



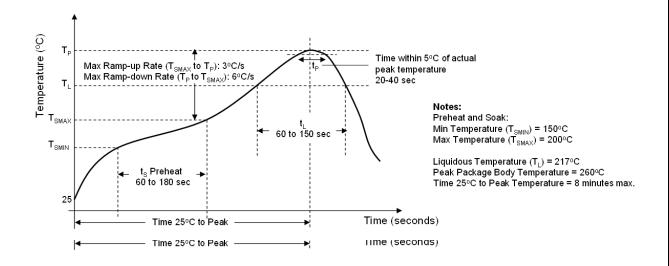


Description	Symbol	Dimensions in millimeters (inches)
Tape wide	W	$16 \pm 0.3$ ( .63 )
Pitch of sprocket holes	P0	4 ± 0.1 ( .15 )
Distance of compartment	F P2	7.5 ± 0.1 ( .295 ) 2 ± 0.1 ( .079 )
Distance of compartment to compartment	P1	12 ± 0.1 ( .472 )

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## **Property of Lite-on Only**

### **Recommended Lead Free Reflow Profile**



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## **Property of Lite-on Only**

## **Absolute Maximum Ratings\*1**

Parameter	Symbol	Device	Min	ТҮР	Max	Units		
Storage Temperature	T <sub>ST</sub>		-55		125	°C		
Operating Temperature	T <sub>A</sub>		-20		85	°C		
Isolation Voltage	V <sub>ISO</sub>	6N138 6N139			5000			$V_{RMS}$
Supply Voltage	V <sub>cc</sub>				15	V		
Lead Solder Temperature * 2	T <sub>SOL</sub>				260	°C		
Input								
Average Forward Input Current	I <sub>F</sub>				20	mA		
Reverse Input Voltage	$V_R$	6N138 6N139			5	V		
Input Power Dissipation	Pı				35	mW		
Output								
Average Output Current	Io	6N138 6N139			50	mA		
Supply Voltage, Output Voltage	Vcc, Vo	6N138	-0.5		7	V		
Supply voltage, Output voltage	vcc, v <sub>0</sub>	6N139	-0.5		18	v		
Output Collector Power Dissipation	Po	6N138 6N139			100	mW		

<sup>1.</sup>Ambient temperature =  $25^{\circ}$ C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

2. 260°C for 10 seconds. Refer to Lead Free Reflow Profile.

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## **Property of Lite-on Only**

## **Electrical Specifications**

Parameters	Test Condition	Symbol	Device	Min	Тур	Max	Units
Input							
Input Forward Voltage	$I_F = 1.6 \text{mA}, T_A = 25^{\circ}\text{C}$	V <sub>F</sub>			1.1	1.7	V
Input Forward Voltage Temperature Coefficient	IF=1.6mA	ΔV <sub>F</sub> /ΔTa	6N138		-1.9		mV/℃
Input Reverse Voltage	$I_R = 10 \mu A T_A = 25 ^{\circ} C$	BV <sub>R</sub>	6N139	5	-	-	>
Input Capacitance	V <sub>F</sub> =0; f=1MH <sub>Z</sub>	C <sub>IN</sub>		-	60	-	pF
Detector							
	I <sub>F</sub> =1.6mA;Vo=0.4V; Vcc=4.5V		6N138	300	1600	2600	%
Current transfer ratio	I <sub>F</sub> =0.5mA;Vo=0.4V; Vcc=4.5V	CTR	CN14.00	400	2000	5000	
	I <sub>F</sub> =1.6mA;Vcc=0.4V; Vcc=4.5V		6N139	500	1600	2600	
	I <sub>F</sub> =1.6mA;Vcc=4.5V; I <sub>o</sub> =4.8mA		6N138	-	0.1	0.4	
Logic low output voltage	$I_{F}=0.5\text{mA;Vcc}=4.5\text{V;} \\ I_{o}=2\text{mA} \\ I_{F}=1.6\text{mA;Vcc}=4.5\text{V;} \\ I_{o}=8\text{mA} \\ I_{F}=5\text{mA;Vcc}=4.5\text{V;} \\ I_{o}=15\text{mA}$	V <sub>OL</sub>	6N139	-	0.1	0.4	V
	I <sub>F</sub> =12mA;Vcc=4.5V; I <sub>o</sub> =24mA			-	0.2		
Lavia biah autaut ausaut	$I_F$ =0mA, Vo=Vcc=7V $T_A$ =25 $^{\circ}$ C		6N138	-	0.05	250	^
Logic high output current	I <sub>F</sub> =0mA, Vo=Vcc=18V T <sub>A</sub> =25°C	- I <sub>OH</sub>	6N139	-	0.1	100	$\mu$ A
Logic low supply current	I <sub>F</sub> =1.6mA, V <sub>o</sub> =open (Vcc=18V)	I <sub>ccL</sub>	6N138 6N139	-	0.4	1.5	mA
Logic high supply current	$I_F$ =0mA, $V_o$ =open; $T_A$ =25°C (Vcc=18V)	I <sub>ccH</sub>	6N138 6N139	-	0.01	10	uA

<sup>\*</sup>All Typical at T<sub>A</sub> =25°C

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## **Property of Lite-on Only**

### **SWITCHING SPECIFICATIONS (AC)**

 $T_A$ =0~70°C, Vcc=5V, unless otherwise specified.

Parameter	Test Condition	Symbol	Device	Min	Тур	Max	Units
	$I_F=1.6$ mA; $R_L=2.2$ k $\Omega$		6N138	-	1.6	10	
Propagation Delay Time to Low Output Level	$I_F$ =0.5mA; $R_L$ =4.7K $\Omega$	t <sub>PHL</sub>	6N139	ı	5	25	μs
	$I_F=12mA; R_L=270\Omega$		ONTO	ı	0.1	1	
	$I_F=1.6$ mA; $R_L=2.2$ k $\Omega$		6N138	-	10	35	
Propagation Delay Time to High Output Level	$I_F$ =0.5mA; $R_L$ =4.7K $\Omega$	t <sub>PLH</sub>	6N139	-	18	60	$\mu$ s
	$I_F=12\text{mA}; R_L=270\Omega$			011139	-	2	7
Logic High Common Mode	I <sub>F</sub> =0mA;  V <sub>CM</sub>  =10V <sub>p-p</sub>	ICM I	6N138	1	10	_	KV/µs
Transient Immunity	$R_L=2.2K\Omega$	CM <sub>H</sub>	6N139	1	10	-	KV/µs
Logic Low Common Mode Transient Immunity	I <sub>F</sub> =1.6mA;	CM <sub>L</sub>	6N138 6N139	1	10		KV/µs
	$ V_{CM} =10V_{p-p} R_L=2.2K$ $\Omega$					-	KV/µs

<sup>\*</sup>All Typical at  $T_A = 25$ °C

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### **Property of Lite-on Only**

### **Isolation Characteristics**

Parameter	Test Condition	Symbol	Min	Тур	Max	Units
Input-Output Insulation Leakage Current	45% RH, t = 5s, V <sub>I-O</sub> = 3kV DC, T <sub>A</sub> = 25°C	I <sub>I-O</sub>			1.0	μΑ
Withstand Insulation Test Voltage	RH $\leq$ 50%, t = 1min, T <sub>A</sub> = 25°C	V <sub>ISO</sub>	5000			$V_{RMS}$
Input-Output Resistance	V <sub>I-O</sub> = 500V DC	R <sub>I-O</sub>		10 <sup>12</sup>		Ω

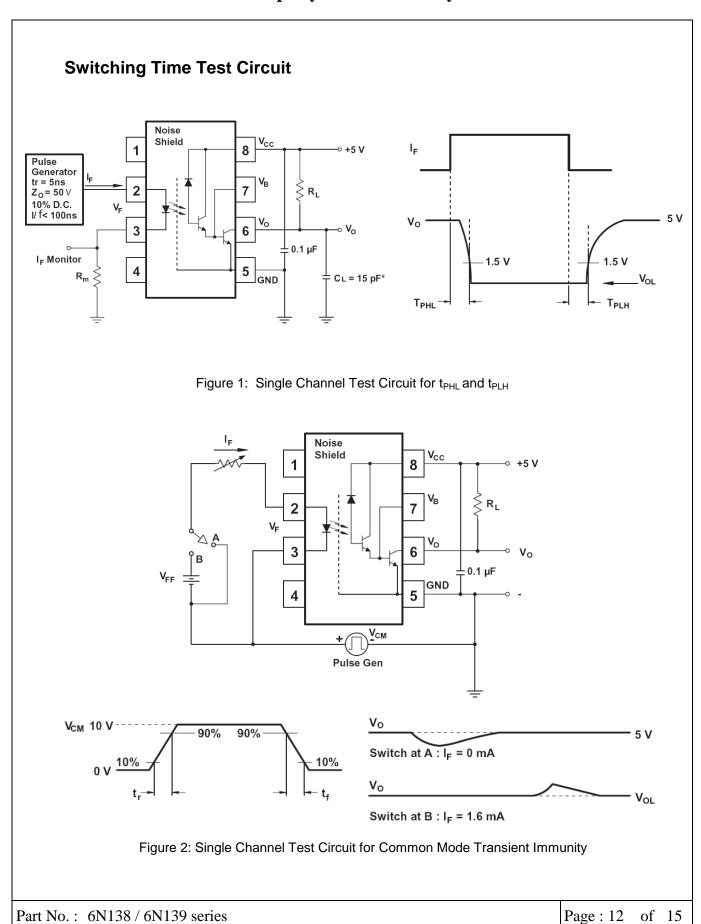
<sup>\*</sup>All Typical at T<sub>A</sub> =25°C

#### Notes,

- 1. AC For 1 Minute, R.H. = 40 ~ 60%. Isolation voltage shall be measured using the following method.
- (1) Short between anode and cathode on the primary side and between collector and emitter on the secondary side
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
- 2. For 10 Seconds
- 3. Current Transfer Ratio (CTR) is defined as the ration of output collector current, Io, to the forward LED input current, IF, times 100%.
- 4. Pin 7 open.
- 5. Instantaneous common mode rejection voltage "output (1)" represents a common mode voltage variation that can hold the output above (1) level (Vo>2.0V). Instantaneous common mode rejection voltage "output (0)" represents a common mode voltage variation that can hold the output above (0) level (Vo<0.8V).
- 6. Device considered a two terminal device. Pins 1, 2, 3 and 4 shorted together and Pins 5, 6, 7 and 8 shorted together.

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### **Property of Lite-on Only**



## **Property of Lite-on Only**

#### **Characteristics Curves**

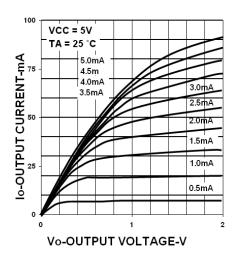


Figure 3: DC transfer characteristics

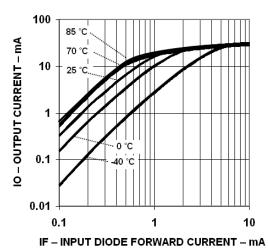


Figure 5: output current vs. input diode forward current

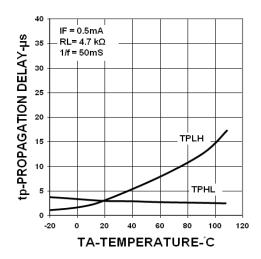


Figure 7: 6N139 propagation delay vs. temperature

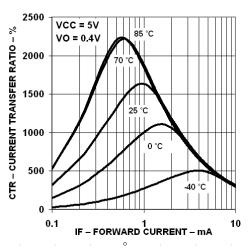


Figure 4: current transfer ratio vs. forward current

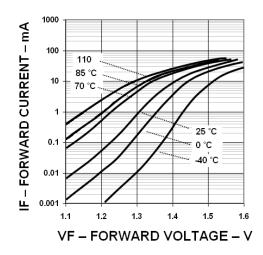


Figure 6: Input diode forward current vs. forward voltage

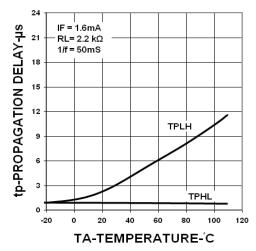


Figure 8: 6N138 propagation delay vs. temperature

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## **Property of Lite-on Only**

#### **Characteristics Curves**

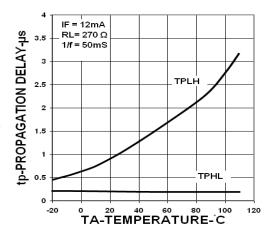


Figure 9: 6N139 propagation delay vs. temperature

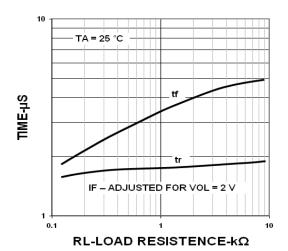


Figure 11: Non-saturated rise and fall time vs. load resistance

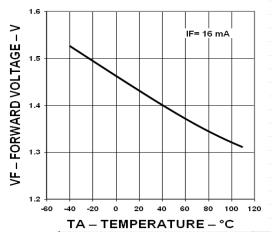


Figure 10: Forward voltage vs. temperature

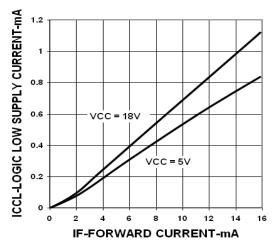


Figure 12: Logic low supply current vs. forward current

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