



Data Sheet

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Lite-on Technology Corp.

Optoelectronics SBG

http://www.liteon.com/opto



1. DESCRIPTION

The LTV-0501 consists of a high efficient AlGaAs Light Emitting Diode and a high speed optical detector. This design provides excellent AC and DC isolation between the input and output sides of the Optocoupler. Connection for the bias of the photodiode improves the speed that of a conventional phototransistor coupler by reducing the base-collector capacitances. A guaranteed common mode transient immunity is up to 1KV/µsec.

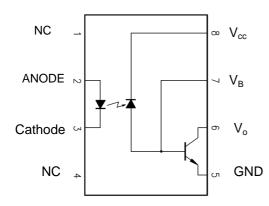
1.1 Features

- SO8 package
- High speed 1MBd typical
- Available in Dual-in-line, Wide lead spacing, Surface mounting package.
- Storable output.

1.2 Applications

- High Voltage Isolation
- Isolation in line receivers
- Feedback element in switching mode power supplier
- Power transistor isolation in motor drives
- Interface between Microprocessor system, computer and their periphe
- Replace pulse transformers.
- Replace slower optocoupler isolators.

1.3 Functional Diagram



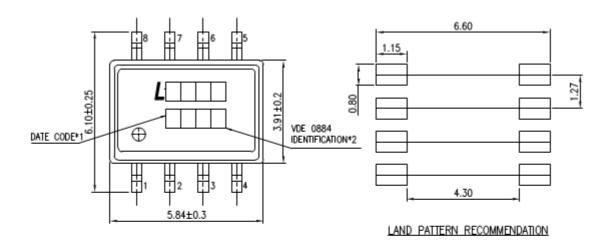
Truth Table (Positive Logic)

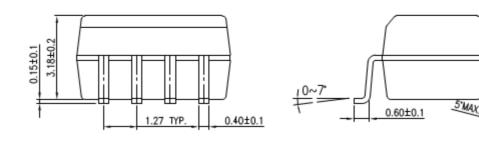
LED	OUT
ON	L
OFF	Н

A 0.1µF bypass Capacitor must be connected between Pin8 and Pin5



2. PACKAGE DIMENSIONS





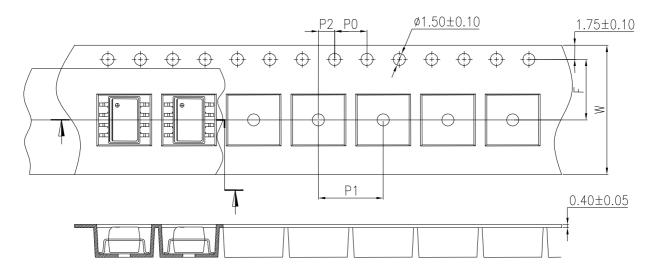
Part No: LTV-0501

Notes:

- 1. Date code
- 2. "V" to represent VDE0884
- 3. Date code
- 4. Dimensions are all in Millimeters.



3. TAPING DIMENSIONS



Description	Symbol	Dimension in mm (inch)
Tape wide	W	16±0.3 (0.63)
Pitch of sprocket holes	P ₀	4±0.1 (0.15)
Distance of compartment	F	7.5±0.1 (0.295)
Distance of compartment	P ₂	2±0.1 (0.079)
Distance of compartment to compartment	P ₁	8±0.1 (0.47)

Quantities Per Reel

Package Type	LTV-0501
Quantities (pcs)	2000



4. RATING AND CHARACTERISTICS

4.1 Absolute Maximum Ratings at Ta=25℃ *1

	Parameter	Symbol	Rating	Unit
	Average Forward Input Current	l _F	25	mA
Input	Reverse Input Voltage	V_R	5	V
	Power Dissipation	Pı	45	mW
	Output Collector Current	I _O	8	mA
Output	Output Collector Voltage	Vo	-0.5~15	V
	Output Collector Power Dissipation	P _o	100	mW
	Isolation Voltage	$V_{\rm iso}$	3750	V_{rms}
	Supply Voltage	Vcc	-0.5~15	V
	Operating Temperature	T_{opr}	-55 ~ +100	$^{\circ}\!\mathbb{C}$
	Storage Temperature	T_{stg}	-55 ~ +125	$^{\circ}$
	Lead Solder Temperature *2	T_{sol}	260	$^{\circ}$

- Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can
 cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely
 affect reliability.
- 2. 260°C for 10 seconds. Refer to Lead Free Reflow Profile.



4.2 ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25℃

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conition	
Input							
Input Forward Voltage	V_{F}	_	_	1.8	V	I _F =16mA, T _A =25°C	
Input Reverse Voltage	BV_R	5.0	_	_	V	I _R = 10μA	
Detector							
				FO		I _F =16mA;Vcc=4.5V;	
Current transfer ratio	CTR	19	24	50	. %	T _A =25°C;Vo=0.4V	
Current transfer fatto	OTK	15	25		70	I _F =16mA;Vcc=4.5V;	
		15	25	_		T _A =25°C;Vo=0.5V	
		_	0.1	0.4		I _F =16mA;Vcc=4.5V;	
Logic low output voltage output	V _{OL}				V	I₀=3.0mA; T _A =25°C	
voltage			0.1	0.5		I _F =16mA;Vcc=4.5V;	
		_	0.1	0.5		I_0 =2.4mA; T_A =25 $^{\circ}$ C	
	I _{OH}	_	0.003	0.5	μ Α	I _F =0mA, Vo=Vcc=5.5V	
						T _A =25°C	
Logic high output current		_	0.01	1		I _F =0mA, Vo=Vcc=15V	
						T _A =25°C	
		_	_	50		Vo = Vcc = 15 V	
Logic low supply current	I _{ccL}	_	50	200	μ Α	I _F =16mA,V _o =open	
				200		(Vcc=15V)	
Logic high supply current	I _{ccH}	_	0.02	1	., Δ	I _F =0mA,V _o =open ;	
			0.02	'	μ A	T _A =25°C (Vcc=15V)	

^{*}All Typical at T_A=25° C



5. SWITCHING SPECIFICATION

T_A=0~70°C, Vcc=5V, unless otherwise specified.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conition	
Propagation Delay Time to Low	4		0.2	0.8	<i>u</i> s	T _A =25℃	
Output Level	t _{PHL}	_	0.2	0.6	μS	(R _L =1.9K Ω , I _F =16mA)	
Propagation Delay Time to High	+		0.6	0.8	<i>u</i> s	T _A =25°C	
Output Level	t _{PLH}	_	0.6	0.6	μ S	(R _L =1.9K Ω , I _F =16mA	
Logic High Common Mode	CM _H		1		K\//uc	I _F =0mA;V _{CM} =10Vp-p;	
Transient Immunity	CIVIH	_	ı	_	KV/µs	$C_L = 15 \text{ pF}; T_A=25C$	
Logic Low Common Mode	CM _L	ICM I		1		KV/µs	I _F =0mA;V _{CM} =10Vp-p;
Transient Immunity			'		πν/μο	$C_L = 15 \text{ pF}; T_A = 25C$	

^{*}All Typical at T_A =25°C



6. ISOLATION CHARACTERISTIC

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conition
Input-Output Insulation Leakage	l			1.0		45% RH, t = 5s,
Current	I _{I-O}	_	_	1.0	μA	$V_{I-O} = 3kV DC, T_A = 25^{\circ}C$
Withstand Insulation Test	V_{ISO}	3750			V_{RMS}	RH ≤ 50%, t = 1min,
Voltage	VISO	3750		_	VRMS	$T_A = 25^{\circ}C$
Input-Output Resistance	R _{I-O}	_	10 ¹²	_	Ω	V _{I-O} = 500V DC

^{*}All Typical at T_A =25°C

Notes

- 1. A 0.1 μF or bigger bypass capacitor for V_{CC} is needed as shown in Fig.1
- 2. Current Transfer Ratio is defined as the ratio of output collector current Io , to the forward LED input current IF, times 100.
- 3. The 1.9K Ω load represents 1TTL unit load of 1.6mA and the 5.6K Ω pull-up resistor.
- 4. The 4.1K Ω load represents 1LSTTL unit load of 0.36mA and the 6.1K Ω pull-up resistor.



7. CHARACTERISTICS CURVES

Figure 1: DC and pulsed transfer characteristics

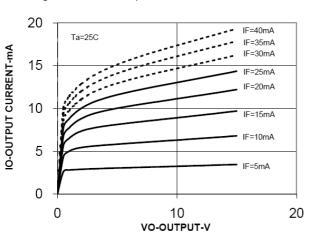


Figure 2: Input current vs. forward voltage

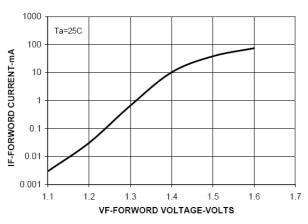


Figure 3: Logic high output current vs. temperature

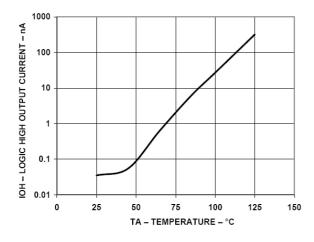


Figure 4: Current transfer ratio vs. input current

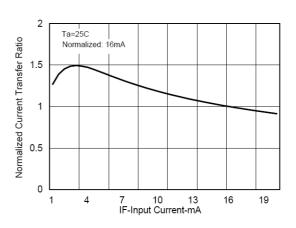


Figure 5: Current transfer ratio vs. temperature

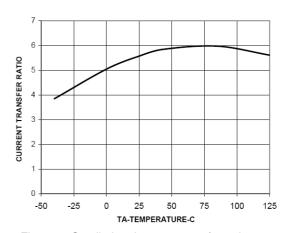


Figure 6: Small-signal current transfer ratio vs.

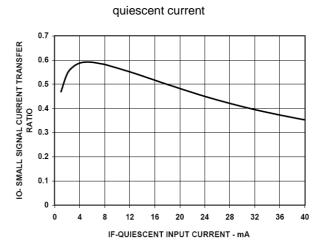




Figure 7: Propagation delay time vs. temperature

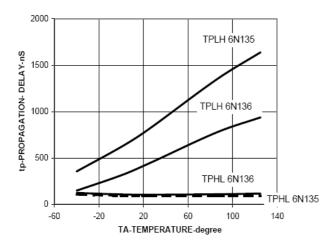
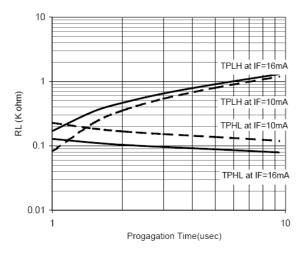


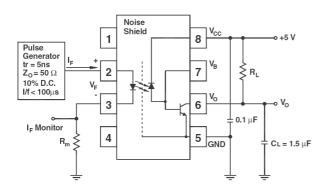
Figure 8: Propagation delay time vs. load resistance



BNC-OD-C131/A4 Created Date: 9/18/2009 Revison: 12/5/2011



8. Switching Time Test Circuit



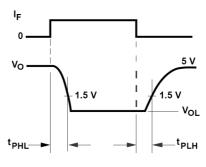


Figure 1: Single Channel Test Circuit for t_{PHL} and t_{PLH}

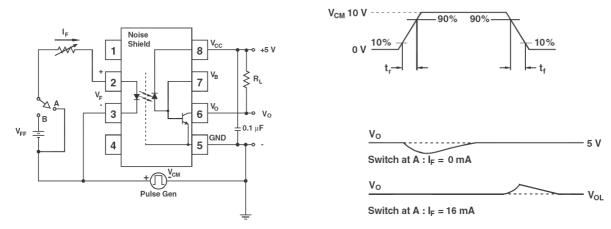


Figure 2: Single Channel Test Circuit for Common Mode Transient Immunity

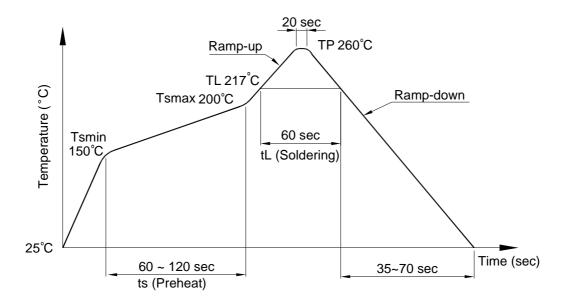


9. TEMPERATURE PROFILE OF SOLDERING

9.1 IR Reflow soldering (JEDEC-STD-020C compliant)

One time soldering reflow is recommended within the condition of temperature and time profile shown below. Do not solder more than three times.

Profile item	Conditions			
Preheat				
- Temperature Min (T _{Smin})	150°C			
- Temperature Max (T _{Smax})	200°C			
- Time (min to max) (ts)	90±30 sec			
Soldering zone				
- Temperature (T _L)	217°C			
- Time (t∟)	60 sec			
Peak Temperature (T _P)	260°C			
Ramp-up rate	3°C / sec max.			
Ramp-down rate	3~6°C / sec			





9.2 Wave soldering (JEDEC22A111 compliant)

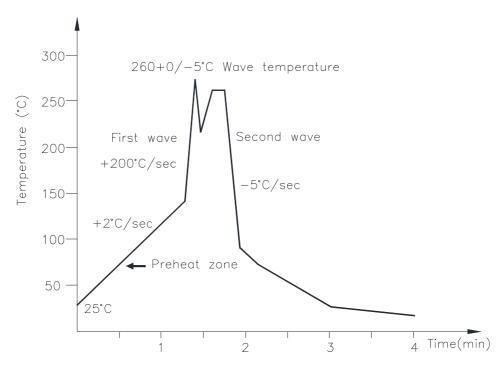
One time soldering is recommended within the condition of temperature.

Temperature: 260+0/-5°C

Time: 10 sec.

Preheat temperature:25 to 140°C

Preheat time: 30 to 80 sec.



9.3 Hand soldering by soldering iron

Allow single lead soldering in every single process. One time soldering is recommended.

Temperature: 380+0/-5°C

Time: 3 sec max.

10. Notes:

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