

2N6504 Series



Description

Designed primarily for half-wave AC control applications, such as motor controls, heating controls and power supply crowbar circuits.

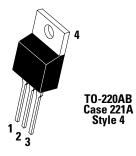
Features

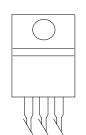
- Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Blocking Voltage to 800
 Volts

P0

- 300 A Surge Current Capability
- Pb–Free Package is Available

Pin Out





Functional Diagram





Maximum Ratings (T = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Peak Repetitive Off-State Voltage (Note 1) 2N6505 Gate Open, Sine Wave 50 to 60 Hz, T _J = 25 to 125°C) 2N6507 2N6508 2N6509		V _{drm} , V _{rrm}	50 100 400 600 800	V
On-State RMS Current (180° Conduction Angles; $T_c = 85$ °C)	I _{T (RMS)}	25	A	
Average On-State Current (180° Conduction Angles; $T_c = 85^{\circ}$ C)		I _{T (AV)}	16	A
Peak Non-repetitive Surge Current (1/2 Cycle, Sine Wave 60 Hz, $T_{J} = 100^{\circ}$ C)		I _{TSM}	250	А
Forward Peak Gate Power (Pulse Width \leq 1.0 µs, T _c = 85°C)		P _{GM}	20	W
Forward Average Gate Power (t = 8.3 ms, $T_c = 85^{\circ}C$)		P _{G(AV)}	0.5	W
Forward Peak Gate Current (Pulse Width \leq 1.0 $\mu s, T_c$ = 85°C)		I _{GM}	2.0	A
Operating Junction Temperature Range		TJ	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the

Recommended Operating Conditions may affect device reliability. 1. V_{BMM} and V_{BMM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Thermal Characteristics			
Rating	Symbol	Value	Unit
*Thermal Resistance, Junction to Case	R _{ejc}	1.5	°C/W
*Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

* Indicates JEDEC Registered Data.

Electrical Characteristics - OFF ($T_c = 25^{\circ}C$ unless otherwise noted)

				-		
Characteristic		Symbol	Min	Тур	Мах	Unit
†Peak Repetitive Blocking Current	T_ = 25°C	I _{DRM} ,	-	-	1.0	μΑ
$(V_{AK} = V_{DRM} = V_{RRM}; \text{ Gate Open})$	T_ = 125°C	I _{RRM}	-	-	2.0	mA

Electrical Characteristics - **ON** ($T_c = 25^{\circ}$ C unless otherwise noted; Electricals apply in both directions)

Characteristic			Min	Тур	Мах	Unit
* Forward On-State Voltage (Note 2) (ITM = 50 A)		V _{TM}	_	-	1.8	V
* Gate Trigger Current (Continuous dc)	T _c = 25°C		-	9.0	30	
* Gate Trigger Current (Continuous dc) $T_c = 25^{\circ}C$ ($V_{AK} = Rated V_{DRM} \text{ or } V_{RRM}$, Gate Open) $T_c = -40^{\circ}C$		GT	-	-	75	mA
* Gate Trigger Voltage (Continuous dc) (V_{AK} = 12 Vdc, R_L = 100 Ω , T_c = -40 °C)			-	1.0	1.5	V
Gate Non-Trigger Voltage (V_{AK} = 12 Vdc, R_{L} = 100 Ω , T_{J} = 125°C)		V _{gd}	0.2	-	-	V
*Holding Current ($V_p = 12$ Vdc, $T_c = 25^{\circ}C$			-	18	40	
Initiating Current = 200 mA, Gate Open)			_	-	80	mA
* Turn-On Time (I $_{\rm TM}$ = 25 A, I $_{\rm GT}$ = 50 mAdc)			_	2.0	-	μs
Turn Off Time () ($rated voltage)$ ($I_{TM} = 2$	5 A, I _B = 25 A)		-	35	-	
Turn-Off Time (V_{RM} = rated voltage) (I_{TM} = 25 A, I_R = 25 A, T_J = 125°C)		L _q	-	55	-	μs

*Indicates JEDEC Registered Data

2. Pulse Test: Pulse Width \leq 300 µsec, Duty Cycle \leq 2%.



Dynamic Characteristics

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Characteristic	Symbol	Min	Тур	Max	Unit
Critical Rate of Rise of Off-State Voltage (Gate Open, Rated VDRM, Exponential Waveform)	dv/dt(c)	-	50	-	V/µs

Voltage Current Characteristic of SCR

Symbol	Parameter		
V _{DRM}	Peak Repetitive Forward Off State Voltage		
I _{DRM}	Peak Forward Blocking Current		
V _{RRM}	Peak Repetitive Reverse Off State Voltage		
I _{RRM}	Peak Reverse Blocking Current		
V _{TM}	Maximum On State Voltage		
I _H	Holding Current		

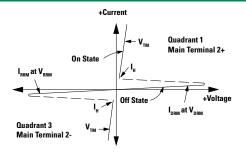


Figure 1. AverageCurrent Derating

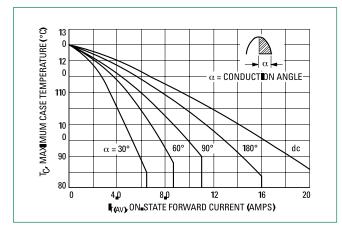


Figure 2. Maximum On-State Power Dissipation

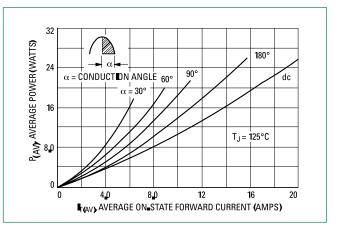




Figure 3. Typical On–State Characteristics

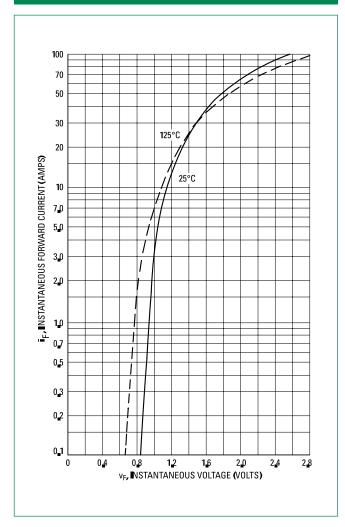


Figure 4. Maximum Non-Repetitive Surge Current

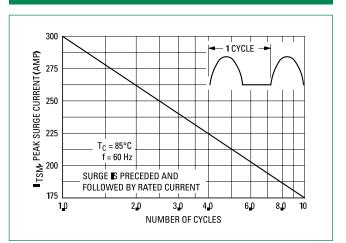
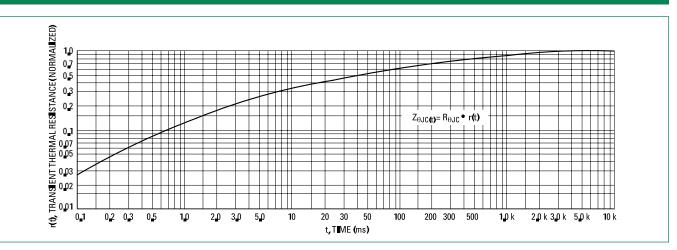


Figure 5. Thermal Response





Typical Trigger Characteristics

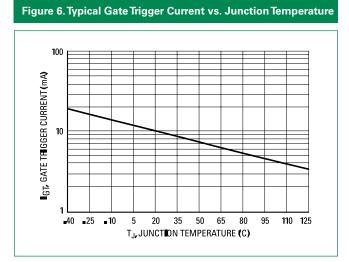


Figure 8. Typical Holding Current vs. Junction Temperature

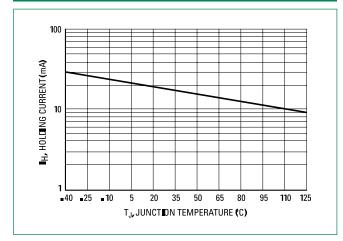
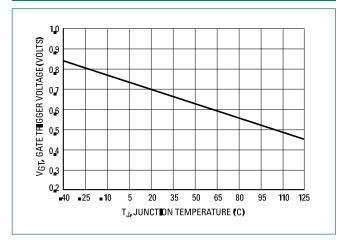


Figure 7. Typical Gate Trigger Voltage vs. Junction Temperature

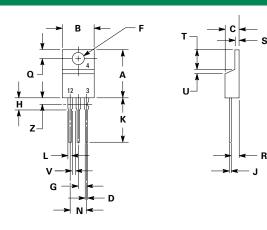


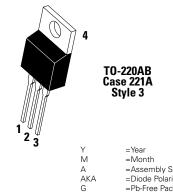


Part Marking System

Ordering Information

Dimensions







=Assembly Site =Diode Polarity =Pb-Free Package

Dim	Inches		Millin	neters
Dim	Min	Max	Min	Max
Α	0.590	0.620	14.99	15.75
В	0.380	0.420	9.65	10.67
С	0.178	0.188	4.52	4.78
D	0.025	0.035	0.64	0.89
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.41	2.67
н	0.110	0.130	2.79	3.30
J	0.018	0.024	0.46	0.61
К	0.540	0.575	13.72	14.61
L	0.060	0.075	1.52	1.91
N	0.195	0.205	4.95	5.21
٥	0.105	0.115	2.67	2.92
R	0.085	0.095	2.16	2.41
S	0.045	0.060	1.14	1.52
т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
v	0.045		1.15	
Z		0.080		2.04

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

Pin Assignment			
1	Cathode		
2	Anode		
3	Gate		
4	Anode		

Device	Package	Shipping		
2N6504	TO-220AB			
2N6504G	TO-220AB (Pb-Free)	500 Units / Box		
2N6505	TO-220AB	500 Offics / Box		
2N6505G	TO-220AB (Pb-Free)			
2N6505T	TO-220AB			
2N6505TG	TO-220AB (Pb-Free)	500 Units / Box		
2N6507	TO-220AB			
2N6507G	TO-220AB (Pb-Free)	500 Units / Box		
2N6507T	TO-220AB			
2N6507TG	TO-220AB (Pb-Free)	500 Units / Box		
2N6508	TO-220AB			
2N6508G	TO-220AB (Pb-Free)	500 Units / Box		
2N6508TG	TO-220AB	500 Units / Box		
2N6509	TO-220AB (Pb-Free)	500 Units / Box		
2N6509G	TO-220AB			
2N6509T	TO-220AB (Pb-Free)	500 Units / Box		
2N6509TG	TO-220AB			

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