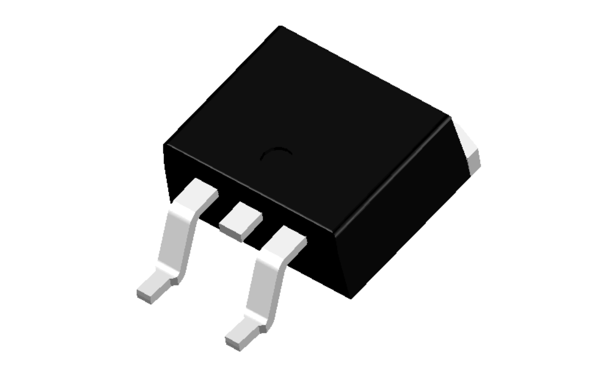
IXYS A Littelfuse Technology

Datasheet

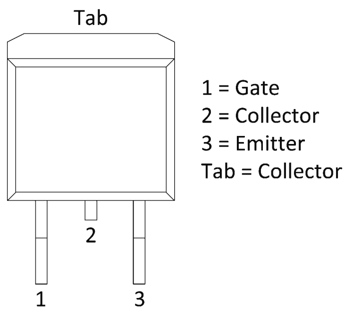
LGB18N40ATH
400 V, 18 A N-Channel Ignition IGBT



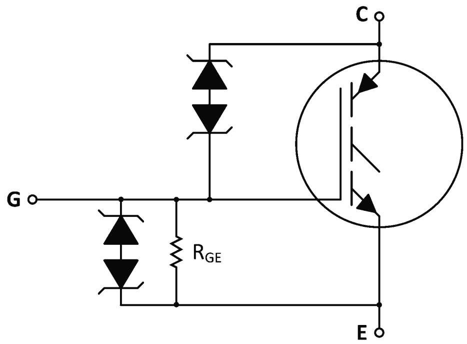
Agency Approvals

Environmental Approvals
Pb-free

Pinout Diagram



Functional Diagram



Product Summary

Characteristic	Value	Unit
V_{CES}	400	V
I_C	18	A

Description

This Logic Level Insulated Gate Bipolar Transistor (IGBT) features monolithic circuitry integrating ESD and Over−Voltage clamped protection for use in inductive coil drivers applications. Primary uses include Ignition, Direct Fuel Injection, or wherever high voltage and high current switching is required.

Features

- Ideal for Coil−on−Plug Applications
- Gate−Emitter ESD Protection
- Temperature Compensated Gate−Collector Voltage Clamp Limits Stress Applied to Load
- Integrated ESD Diode Protection
- New Design Increases Unclamped Inductive Switching (UIS) Energy Per Area
- Low Threshold Voltage Interfaces Power Loads to Logic or Microprocessor Devices
- Low Saturation Voltage
- High Pulsed Current Capability
- Integrated Gate−Emitter Resistor (R_{GE})
- AEC-Q101 Qualified
- These are Pb−Free Devices
- Emitter Ballasting for Short−Circuit Capability

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1. Maximum Ratings (T_J = 25 °C unless otherwise specified)

Characteristic	Conditions	Symbol	Value	Unit
Collector-Emitter Voltage	-	V_{CES}	430	V_{DC}
Collector-Gate Voltage	-	V_{CER}	430	V_{DC}
Gate-Emitter Voltage	-	V_{GE}	18	V_{DC}
Collector Current – Continuous	T_C = 25 °C	I_C	18	A_{DC}
Collector Current – Pulsed			50	A_{AC}
ESD – Human Body Model	R = 1500 Ω, C = 100 pF	ESD	8.0	kV
ESD – Machine Model	R = 0 Ω,C = 200 pF		800	V
Total Power Dissipation	T_C = 25 °C	P_D	115	W
	Derating for > 25 °C		0.77	W/°C
Operating and Storage Temperature Range	-	T_J, T_{stg}	-55 to +175	°C

2. Unclamped Collector-to-Emitter Avalanche Characteristics

Characteristic	Symbol	Value	Unit
Single Pulse Collector-to-Emitter Avalanche Energy			
V_{CC} = 50 V, V_{GE} = 5.0 V, P_kI_L = 21.1 A, L = 1.8 mH, Starting T_J = 25 °C	E_{AS}	400	mJ
V_{CC} = 50 V, V_{GE} = 5.0 V, P_kI_L = 18.3 A, L = 1.8 mH, Starting T_J = 125 °C		300	
Reverse Avalanche Energy			
V_{CC} = 100 V, V_{GE} = 20 V, P_kI_L = 25.8 A, L = 6.0 mH, Starting T_J = 25 °C	$E_{AS(R)}$	2000	mJ

Note: -55 °C ≤ T_J ≤ 150 °C

3. Maximum Short-Circuit Times

Characteristic	Symbol	Value	Unit
Short Circuit Withstand Time 1[1]	$t_{sc,1}$	750	µs
Short Circuit Withstand Time 2[2]	$t_{sc,2}$	5.0	ms

Note: -55 °C ≤ T_J ≤ 150 °C

Footnote 1: See Figure 17, 3 pulses with 10 ms period

Footnote 2: See Figure 18, 3 pulses with 10 ms period

4. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case	$R_{θJC}$	1.3	°C/W
Thermal Resistance, Junction to Ambient (D2PAK)[3]	$R_{θJA}$	50	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8″ from case for 5 seconds	T_L	275	°C

Footnote 3: When surface mounted to an FR4 board using the minimum recommended pad size

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5. Electrical Characteristics – Off

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Collector-Emitter Clamp Voltage	BV_{CES}	I_C = 2.0 mA	T_J = -40 °C to 150 °C	380	395	420	V_{DC}
		I_C = 10 mA		390	405	430	
Zero Gate Voltage Collector Current	I_{CES}	V_{CE} = 350 V, V_{GE} = 0 V	T_J = 25 °C	-	2.0	20	µA
			T_J = 150 °C	-	10	40[4]	
			T_J = -40 °C	-	1.0	10	
Reverse Collector-Emitter Leakage Current	I_{ECS}	V_{CE} = -24 V	T_J = 25 °C	-	0.7	2.0	mA
			T_J = 150 °C	-	12	25[4]	
			T_J = -40 °C	-	0.1	1.0	
Reverse Collector-Emitter Clamp Voltage	$BV_{CES(R)}$	I_C = -75 mA	T_J = 25 °C	27	33	37	V_{DC}
			T_J = 150 °C	30	36	40	
			T_J = -40 °C	25	32	35	
Gate-Emitter Clamp Voltage	BV_{GES}	I_G = 5.0 mA	T_J = -40 °C to 150 °C	11	13	15	V_{DC}
Gate-Emitter Leakage Current	I_{GES}	V_{GE} = 10 V	T_J = -40 °C to 150 °C	384	640	1000	μA_{DC}
Gate-Emitter Resistor	R_{GE}	-	T_J = -40 °C to 150 °C	10	16	26	kΩ

Footnote 4: Maximum value of characteristic across temperature range

6. Electrical Characteristics – On

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Collector-Emitter On-Voltage[5]	$V_{CE(on)}$	I_C = 6.0 A, V_{GE} = 4.0 V	T_J = 25 °C	1.0	1.4	1.6	V_{DC}
			T_J = 150 °C	0.9	1.3	1.6	
			T_J = -40 °C	1.1	1.45	1.7[4]	
		I_C = 8.0 A, V_{GE} = 4.0 V	T_J = 25 °C	1.3	1.6	1.9[4]	
			T_J = 150 °C	1.2	1.55	1.8	
			T_J = -40 °C	1.4	1.6	1.9[4]	
		I_C = 10 A, V_{GE} = 4.0 V	T_J = 25 °C	1.4	1.8	2.05	
			T_J = 150 °C	1.5	1.8	2.0	
			T_J = -40 °C	1.4	1.8	2.1[4]	
		I_C = 15 A, V_{GE} = 4.0 V	T_J = 25 °C	1.6	1.9	2.2	
			T_J = 150 °C	1.7	2.1	2.3	
			T_J = -40 °C	1.6	1.8	2.2	
		I_C = 10 A, V_{GE} = 4.5 V	T_J = 25 °C	1.3	1.8	2.0[4]	
			T_J = 150 °C	1.3	1.75	2.0[4]	
			T_J = -40 °C	1.4	1.8	2.0[4]	
Forward Transconductance[5]	gfs	V_{CS} = 5.0 V, I_C = 6.0 A	T_J = -40 °C to 150 °C	8.0	14	25	Mhos

Footnote 4: Maximum value of characteristic across temperature range

Footnote 5: Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%

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7. Dynamic Characteristics

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Input Capacitance	C_{ISS}	V_{CC} = 25 V, V_{GE} = 0 V, f = 1.0 MHz	T_J = -40 °C to 150 °C	400	800	1000	pF
Output Capacitance	C_{OSS}			50	75	100	
Transfer Capacitance	C_{RSS}			4.0	7.0	10	

8. Switching Characteristics

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Turn-off Delay Time (Resistive)	$t_{d(off)}$	V_{CC} = 300 V, I_C = 6.5 A, R_G = 1.0 kΩ, R_L = 46 Ω	T_J = 25 °C	-	4.0	10	µs
Fall Time (Resistive)	t_f			-	9.0	15	
Turn-on Delay Time	$t_{d(on)}$	V_{CC} = 10 V, I_C = 6.5 A, R_G = 1.0 kΩ, R_L = 1.5 Ω	T_J = 25 °C	-	0.7	4.0	µs
Rise Time	t_r			-	4.5	7.0	

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9. Figure Data

Figure 1. Output Characteristics (T_J = 25 °C)

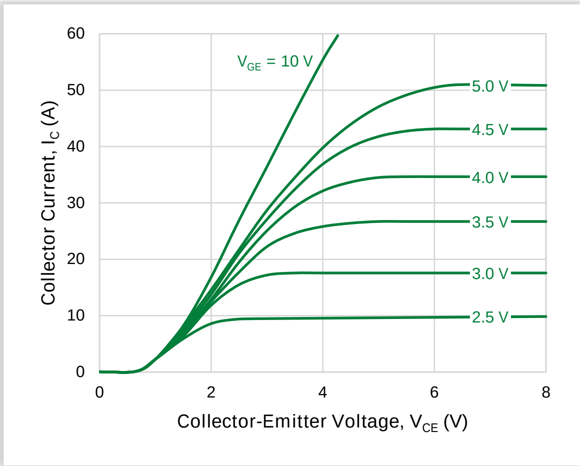

Figure 2. Output Characteristics (T_J = -40 °C)

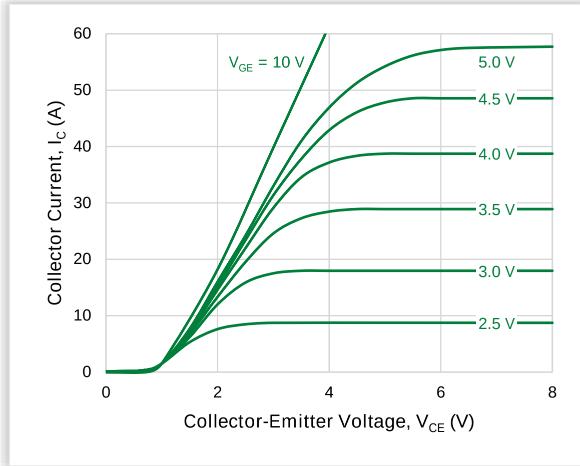

Figure 3. Output Characteristics (T_J = 150 °C)

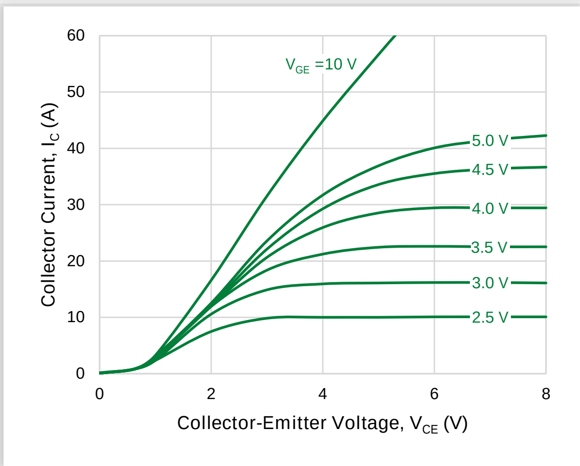

Figure 4. Transfer Characteristics

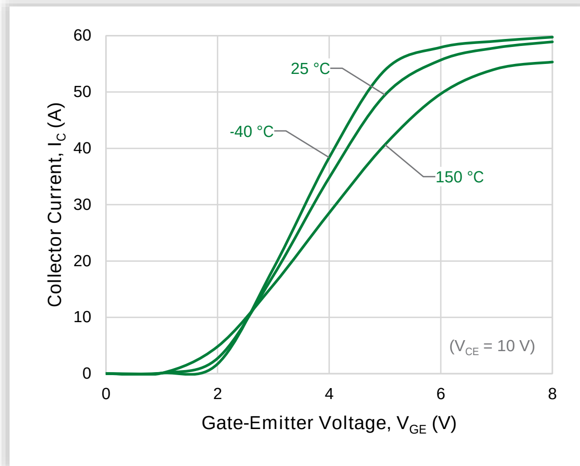

Figure 5. Collector-Emitter Saturation Voltage vs. Junction Temperature

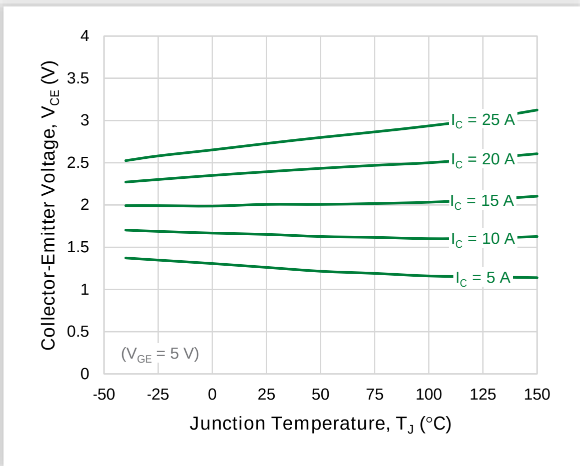

Figure 6. Collector-Emitter Voltage vs. Gate-Emitter Voltage (T_J = 25 °C)

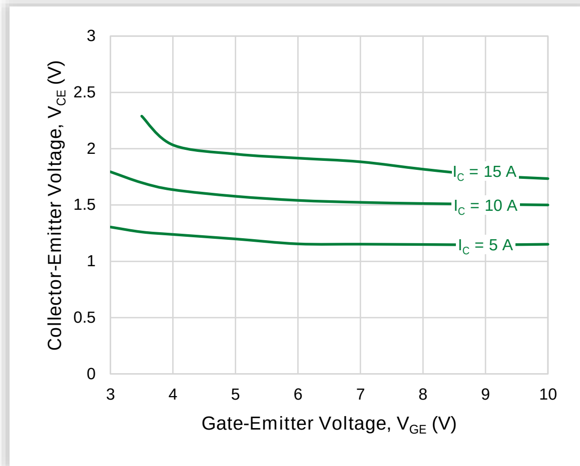

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Figure 7. Collector-Emitter Voltage vs. Gate-Emitter Voltage (T_J = 150 °C)

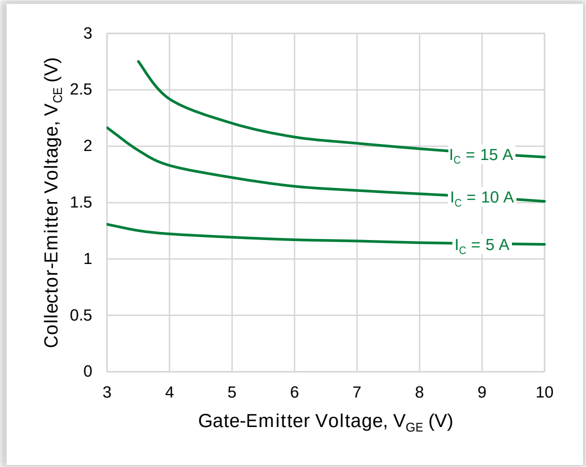

Figure 8. Capacitance Variation

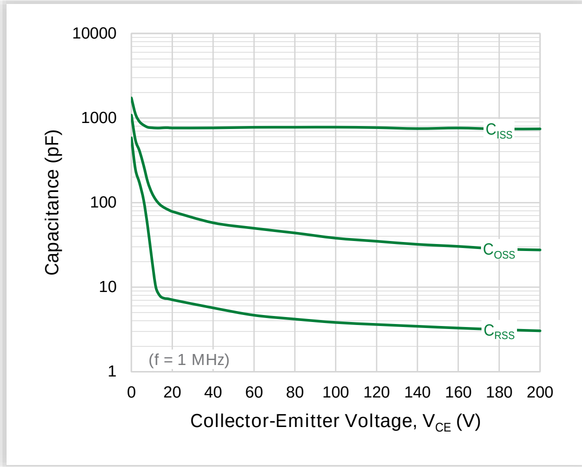

Figure 9. Gate Threshold Voltage vs. Temperature

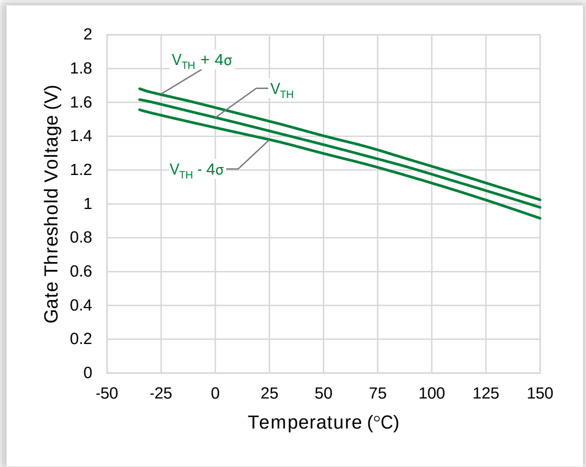

Figure 10. Minimum Open Secondary Latch Current vs. Temperature

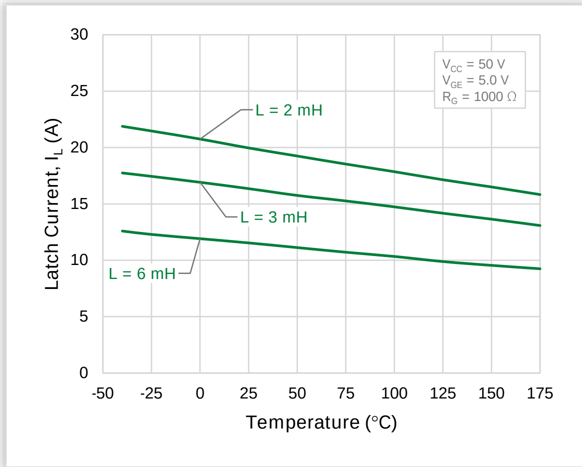

Figure 11. Typical Open Secondary Latch Current vs. Temperature

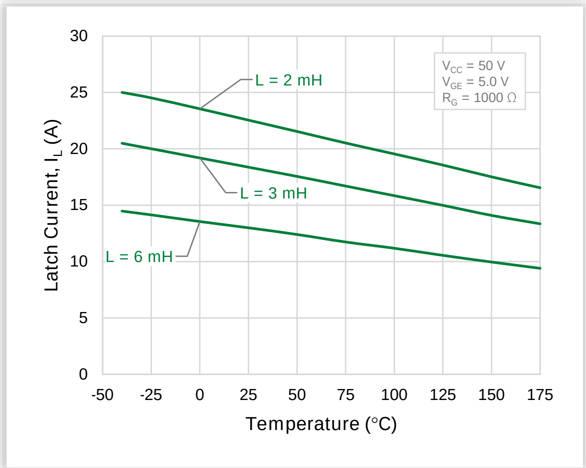

Figure 12. Inductive Switching Fall Time vs. Temperature

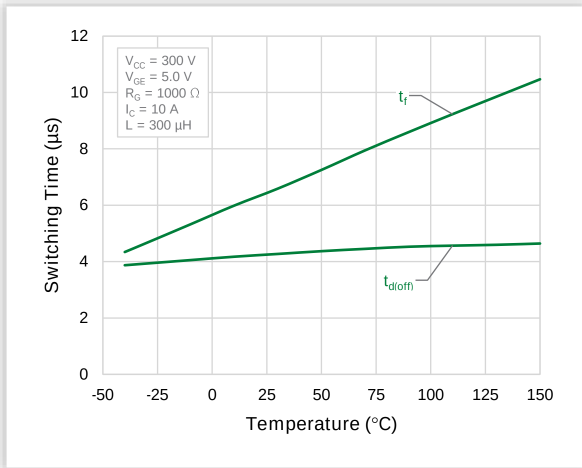

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Figure 13. Single Pulse Safe Operating Area

(Mounted on an Infinite Heatsink at T_A = 25 °C)

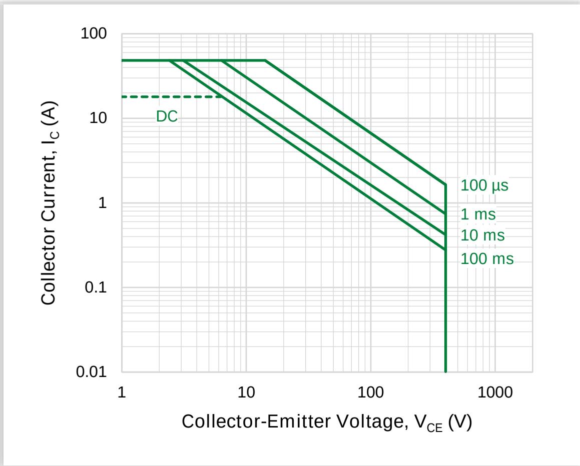

Figure 14. Single Pulse Safe Operating Area

(Mounted on an Infinite Heatsink at T_A = 125 °C)

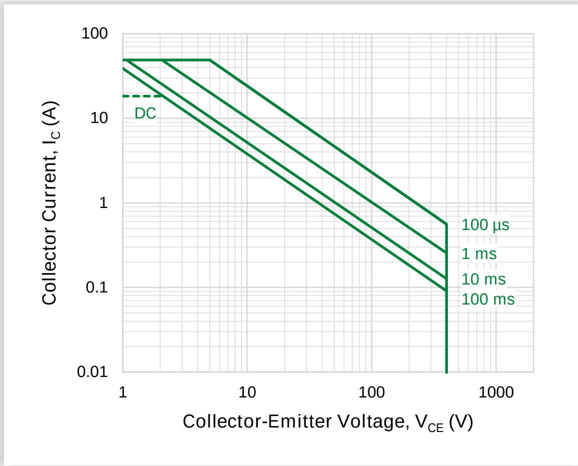

Figure 15. Pulse Train Safe Operating Area

(Mounted on an Infinite Heatsink at T_A = 25 °C)

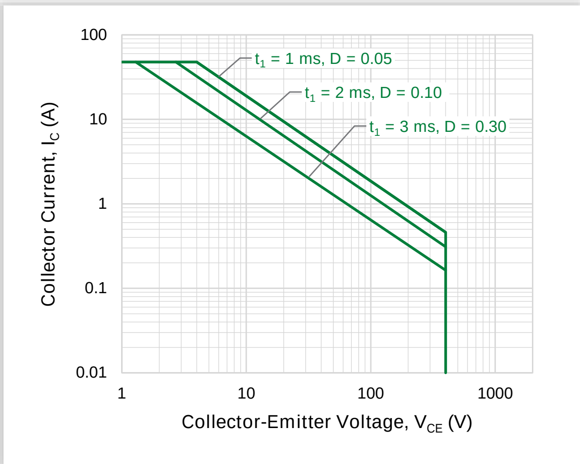

Figure 16. Pulse Train Safe Operating Area

(Mounted on an Infinite Heatsink at T_A = 125 °C)

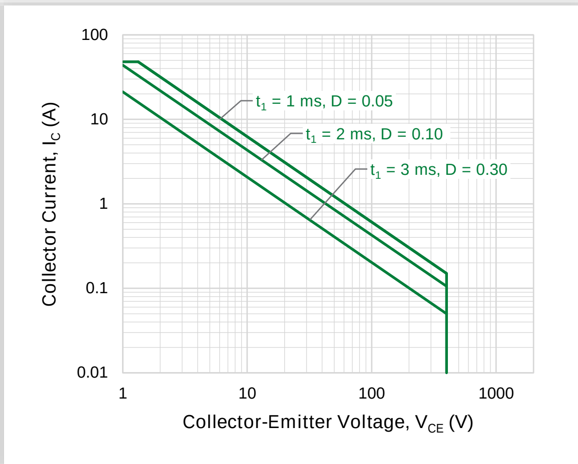

Figure 17. Circuit Configuration for Short Circuit Test 1

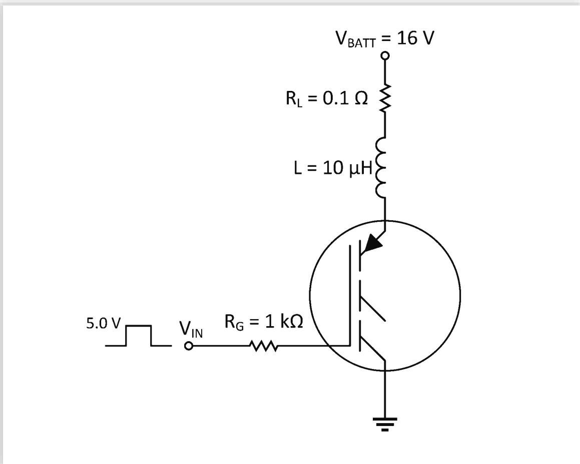

Figure 18. Circuit Configuration for Short Circuit Test 2

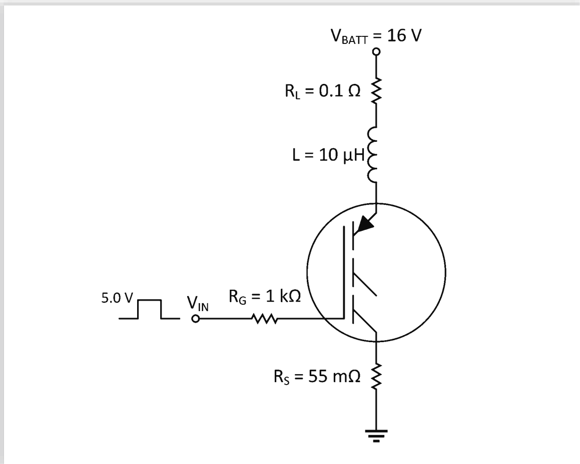

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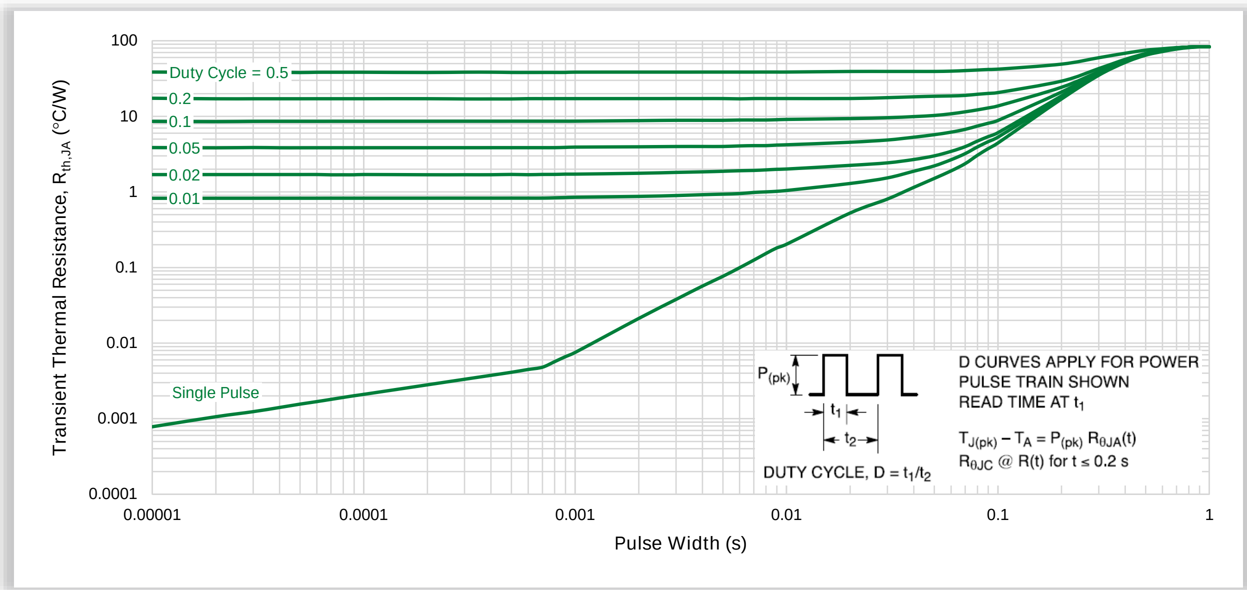
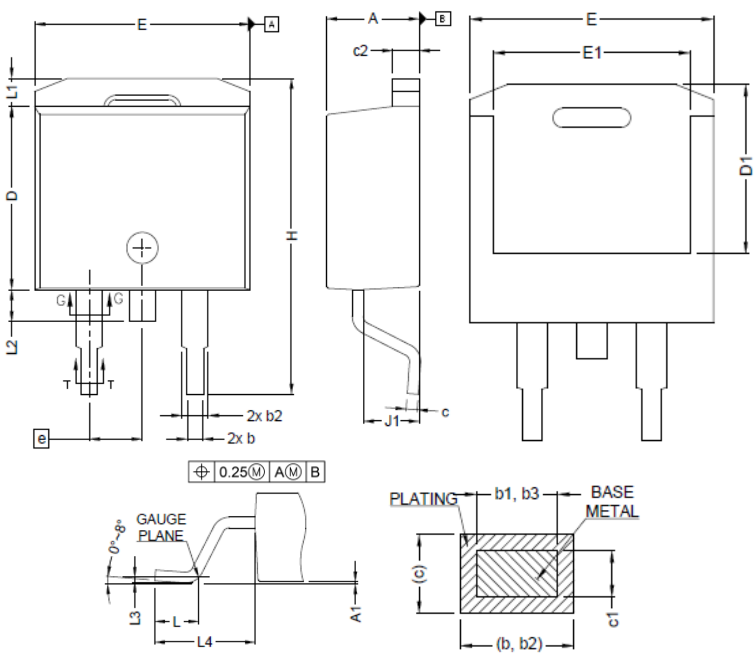
Figure 19. Transient Thermal Resistance

(Non–normalized Junction–to–Ambient mounted on minimum pad area)



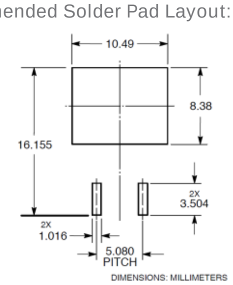
10. Package Dimensions



(Section G-G & T-T)

Symbol	Millimeters		
	Min	Nom	Max
A	4.360	-	4.560
A1	0.000	-	0.250
b	0.700	-	0.900
b1	0.510	-	0.890
b2	1.200	-	1.460
b3	1.170	-	1.370
c	0.380	-	0.694
c1	0.380	-	0.534
c2	1.190	-	1.340
D	8.600	-	9.000
D1	6.900	-	7.500
E	10.150	-	10.550
E1	8.100	-	8.700
e	2.540 BSC		
H	15.000	-	15.600
L	1.900	-	2.500
L1	-	-	1.650
L2	-	-	1.780
L3	0.250		
L4	4.780	-	5.280
J1	2.560	-	2.960

Recommended Solder Pad Layout:



Notes:

1. Dimensioning & tolerancing confirm to ASME Y14.5M-1994.
2. All dimensions are in millimeters. Angles are in degrees.
3. Heatsink side flash is max 0.8 mm.
4. Radius on terminal is optional

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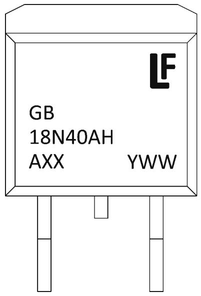
11. Part Numbering and Marking



GB18N40AH	= Device Code
A	= Assembly Location
XX	= Lot Number
Y	= Year
WW	= Work Week
H	= Ballast Structure

12. Packing Options

Part Number	Package	Packing Mode	M.O.Q.
LGB18N40ATH	D2PAK (Pb-Free)	Tape & Reel	2500

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