GEN2 SiC Schottky Diode LSIC2SD120A05, 1200 V, 5 A, TO-220-2L



LSIC2SD120A05

HF RoHS 🔞



Circuit Diagram TO-220-2L



Description

This series of silicon carbide (SiC) Schottky diodes has negligible reverse recovery current, high surge capability, and a maximum operating junction temperature of 175 °C. These diodes series are ideal for applications where improvements in efficiency, reliability, and thermal management are desired.

Features

- Positive temperature coefficient for safe operation and ease of paralleling
- 175 °C maximum operating junction temperature
- Excellent surge capability

Applications

- Boost diodes in PFC or DC/DC stages
- Switch-mode power supplies
- Uninterruptible power supplies
- Solar inverters

diodes

Extremely fast,

switching behavior

• Dramatically reduced

compared to Si bipolar

switching losses

temperature-independent

- Industrial motor drives
- EV charging stations

Environmental

- Littelfuse "RoHS" logo = RoHS RoHS conform
- Littelfuse "HF" logo = **HF** Halogen Free
- Littelfuse "PB-free" logo = PB-free lead plating

Maximum I	Ratings
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Characteristics	Symbol	Conditions	Value	Unit		
Repetitive Peak Reverse Voltage	V _{RRM}	-	1200	V		
DC Blocking Voltage	V _R	T _j = 25 °C	1200	V		
		T _c = 25 °C	17.5			
Continuous Forward Current	I _F	T _c = 135 °C	8.5	А		
		T _c = 158 °C	5			
Non-Repetitive Forward Surge Current	I _{FSM}	$T_c = 25 \text{ °C}, T_p = 10 \text{ ms}, \text{ Half sine pulse}$	40	A		
Power Dissipation	D	$T_c = 25 \text{ °C}$	100	۱۸/		
	P _{Tot}	T _c = 110 °C	43.3	vv		
Operating Junction Temperature	TJ	-	-55 to 175	°C		
Storage Temperature	T _{STG}	-	-55 to 150	°C		
Soldering Temperature (reflow MSL 1)	T _{sold}	-	260	°C		



Electrical Characteristics

	Symbol	Conditions	Value			
Characteristics			Min.	Тур.	Max.	Unit
Forward Voltago	V _F	I _F = 5 A, T _J = 25 °C	-	1.5	1.8	V
Forward voltage		I _F = 5 A, T _J = 175 °C	-	2.1		
Devenue Comment	I _R	$V_{_{ m R}} = 1200 \text{ V}$, $T_{_{ m J}} = 25 \ ^{\circ}\text{C}$	-	<1	100	- μΑ
		V _R = 1200 V , T _J = 175 °C	-	5		
Total Capacitance	С	V _R = 1 V, f=1 MHz	-	310		
		$V_{_{ m R}}$ = 400 V, f = 1 MHz	-	29		pF
		V _R = 800 V, f = 1 MHz	-	21		
Total Capacitive Charge	Q _c	$V_{R} = 800 \text{ V}, \ Q_{c} = \int_{0}^{V_{R}} c(v) dv$	_	30		nC

Footnote: T_{1} = +25 °C unless otherwise specified

Thermal Characteristics						
			Value			
Characteristics	Symbol	mbol Conditions	Min.	Тур.	Max.	Unit
Thermal Resistance	R _{ejc}	-	-	1.50		°C/W



Figure 1: Typical Foward Characteristics

Figure 2: Typical Reverse Characteristics



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Figure 3: Power Derating



Figure 4: Current Derating



Figure 5: Capacitance vs. Reverse Voltage



Figure 6: Capacitive Charge vs. Reverse Voltage





Figure 7: Stored Energy vs. Reverse Voltage



Figure 8: Transient Thermal Impedance



Part Numbering and Marking System



Packing Options

Part Number	art Number Marking		M.O.Q
LSIC2SD120A05	SIC2SD120A05	Tube	1000

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Dimensions-Package TO-220-2L





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E1

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D2

SH (se)	Symbol	Millimeters				
D FLA to no		Min	Nominal	Max		
-MOLI (Refer	А	4.320	4.450	4.570		
+	A1	1.140	1.270	1.400		
	A2	2.500	-	2.740		
	b	0.690	-	0.880		
	b1	0.680	-	0.870		
	b2	1.230	-	1.390		
	b3	1.220	1.270	1.380		
	С	0.360	-	0.503		
	c1	0.630	-	0.527		
	D	14.900	-	15.600		
	D1	8.615	-	9.017		
	D2	12.840	-	12.950		
	E	10.000	10.180	10.360		
	E1	7.570	7.610	7.680		
	e1	2.490	2.540	2.590		
	е	5.030	5.080	5.130		
	H1	6.295	6.545	6.795		
	L	13.000	13.500	14.00		
	L1	2.390	-	3.250		
	øP	3.710	3.840	3.960		
	Q	2.650	-	3.050		
	R	-	-	0.254		

Millimeters

Recommended Solder Pad Layout



NOTES: 1. DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 MM PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF PLASTIC BODY.

2. DIMENSIONS E2 & H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES RE ALLOWED.

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Packing Specification (Tube for TO-220-2L)



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