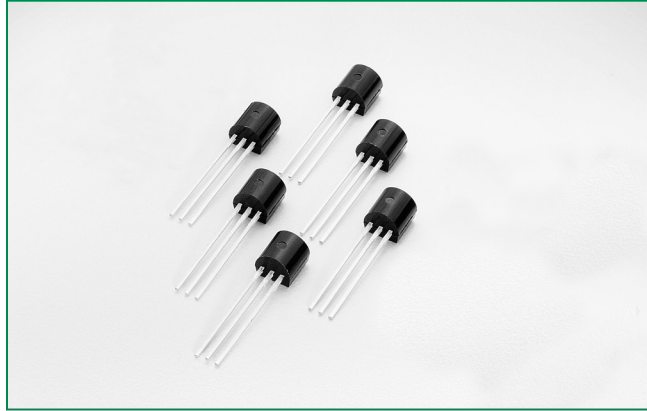


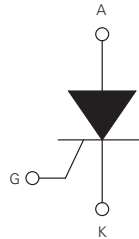
**S802ECS**



**Main Features**

Symbol	Value	Unit
$I_{T(RMS)}$	1.5	A
$V_{DRM}/V_{RRM}$	800	V
$V_{DSM}$ ( $t_p = 50 \mu s$ )	1250	V
$V_{RSM}$ ( $t_p = 50 \mu s$ )	900	V
$I_{GT}$	20 to 100	$\mu A$

**Schematic Symbol**



\* TO92 with "GAK" pin output

**Description**

The S802ECS offers a high static dv/dt with a low turn off ( $t_q$ ) time. It is specifically designed for GFCI (Ground Fault Circuit Interrupter) and AFCI (Arc Fault Circuit Interrupter), RCD (Residual Current Device) and RCBO (Residual Current Circuit Breaker with Overload Protection) applications. All SCR junctions are glass-passivated to ensure long term reliability and parametric stability.

**Features**

- Thru-hole packages
- Surge current capability < 20Amps
- Blocking voltage ( $V_{DRM}/V_{RRM}$ ) capability - up to 800V
- Non-repetitive direct surge peak off-state voltage ( $V_{DSM}$ ) up to 1250V
- Non-repetitive reverse surge peak off-state voltage ( $V_{RSM}$ ) up to 900V
- High dv/dt noise immunity
- Improved turn-off time ( $t_q$ )
- Sensitive gate for direct microprocessor interface
- Halogen free and RoHS compliant

**Absolute Maximum Ratings**

Symbol	Parameter	Value	Unit	
$I_{T(RMS)}$	RMS on-state current (full sine wave)	$T_C = 52^\circ C$	1.5 A	
$I_{T(AV)}$	Average on-state current	$T_C = 52^\circ C$	0.9 A	
$I_{TSM}$	Non repetitive surge peak on-state current (Sine half wave, $T_J$ initial = $25^\circ C$ )	F = 50Hz	20 A	
		F = 60Hz	24 A	
$I^2t$	$I^2t$ Value for fusing	$t_p = 10$ ms	F = 50 Hz	2 A <sup>2</sup> s
di/dt	Critical rate of rise of on-state current $I_G = 10mA$	$T_J = 125^\circ C$	80	A/ $\mu s$
$I_{GM}$	Peak Gate Current	$t_p = 20 \mu s$	$T_J = 125^\circ C$	0.5 A
$P_{G(AV)}$	Average gate power dissipation	—	$T_J = 125^\circ C$	0.2 W
$T_{stg}$	Storage junction temperature range	—	—	-40 to 150 $^\circ C$
$T_J$	Operating junction temperature range	—	—	-40 to 125 $^\circ C$

### Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Description	Test Conditions	Limit	Value	Unit	
I <sub>GT</sub>	DC Gate Trigger Current	V <sub>D</sub> = 6V R <sub>L</sub> = 100 Ω	MIN.	20	μA	
			MAX.	100	μA	
V <sub>GT</sub>	DC Gate Trigger Voltage	V <sub>D</sub> = 6V R <sub>L</sub> = 100 Ω	MAX.	0.8	V	
V <sub>GRM</sub>	Peak Reverse Gate Voltage	I <sub>RG</sub> = 10μA	MIN.	8	V	
I <sub>H</sub>	Holding Current	R <sub>GK</sub> = 1 kΩ Initial Current = 20mA	MAX.	3	mA	
dv/dt	Critical Rate-of-Rise of Off-State Voltage	T <sub>J</sub> = 125°C V <sub>D</sub> = 67% of V <sub>DRM</sub> Exp. Waveform	R <sub>GK</sub> = 1 kΩ	MIN.	40	V/μs
			R <sub>GK</sub> = 220 Ω	MIN.	250	
V <sub>GD</sub>	Gate Non-Trigger Voltage	V <sub>D</sub> = 1/2 V <sub>DRM</sub> R <sub>GK</sub> = 1 kΩ T <sub>J</sub> = 125°C	MIN.	0.2	V	
t <sub>q</sub>	Turn-Off Time	I <sub>T</sub> = 0.5A	MAX.	35	μs	
t <sub>gt</sub>	Turn-On Time	I <sub>G</sub> = 10mA P <sub>W</sub> = 15μsec I <sub>T</sub> = 1.6A(pk)	TYP.	2.3	μs	

### Static Characteristics (T<sub>J</sub> = 25°C, unless otherwise specified)

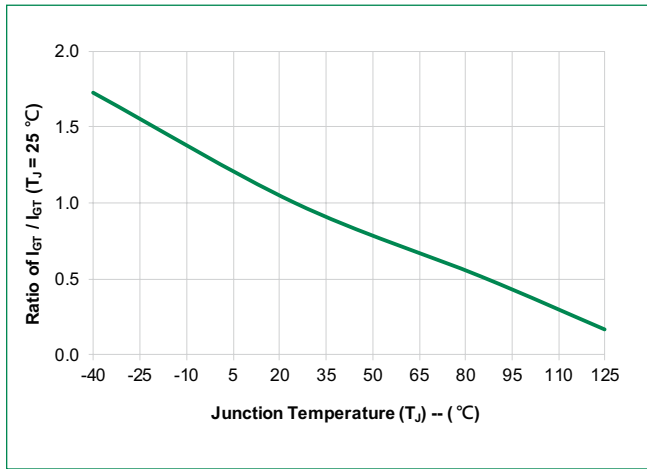
Symbol	Description	Test Conditions	Limit	Value	Unit
V <sub>TM</sub>	Peak On-State Voltage	1.5A device I <sub>TM</sub> = 4A t <sub>p</sub> = 380 μs	MAX.	1.6	V
V <sub>T0</sub>	Threshold Voltage		MAX.	1.03	V
R <sub>D</sub>	Dynamic Resistance		MAX.	106	mΩ
I <sub>DRM</sub> / I <sub>RRM</sub>	Off-State Current, Peak Repetitive	T <sub>J</sub> = 25°C	MAX.	3	μA
		T <sub>J</sub> = 125°C	MAX.	500	μA

### Thermal Resistances

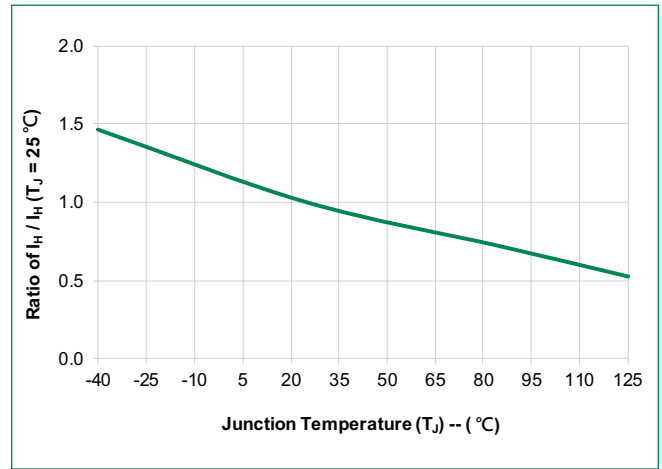
Symbol	Description	Test Conditions	Value	Unit
R <sub>th(JC)</sub>	Junction to case (AC)	I <sub>T</sub> = 1.5A <sub>(RMS)</sub> <sup>1</sup>	35	°C/W
R <sub>th(j-a)</sub>	Junction to ambient	I <sub>T</sub> = 1.5A <sub>(RMS)</sub> <sup>1</sup>	150	°C/W

<sup>1</sup> 60Hz AC resistive load condition, 100% conduction.

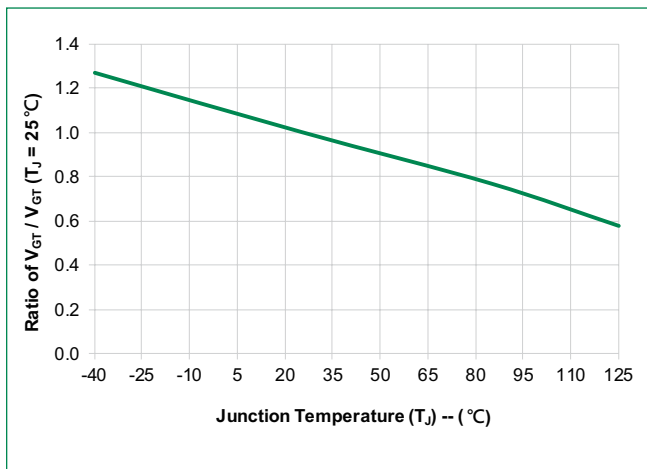
**Figure 1: Normalized DC Gate Trigger Current For All Quadrants vs. Junction Temperature**



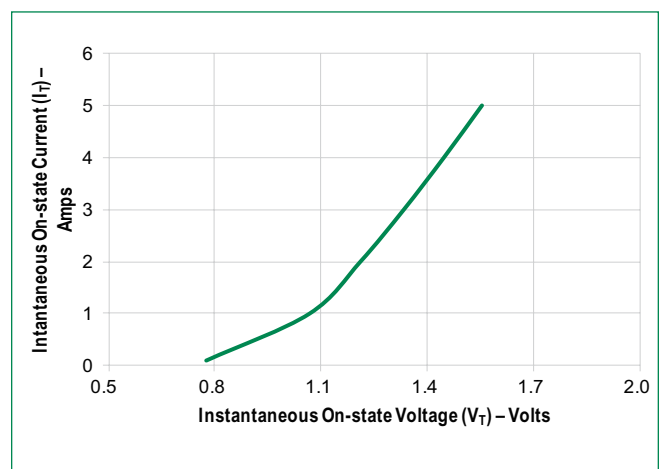
**Figure 2: Normalized DC Holding Current vs. Junction Temperature**



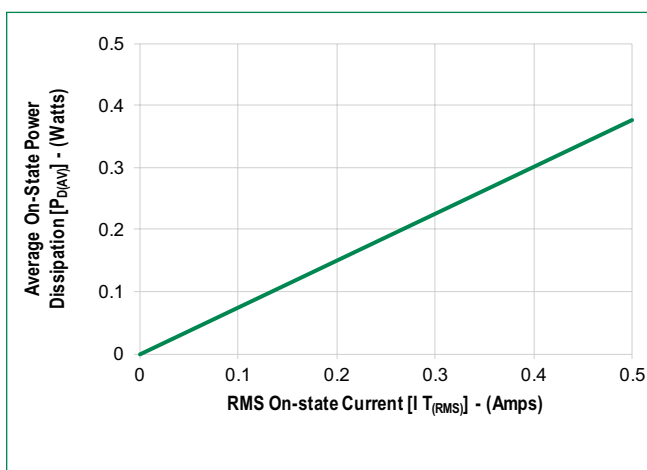
**Figure 3: Normalized DC Gate Trigger Voltage vs. Junction Temperature**



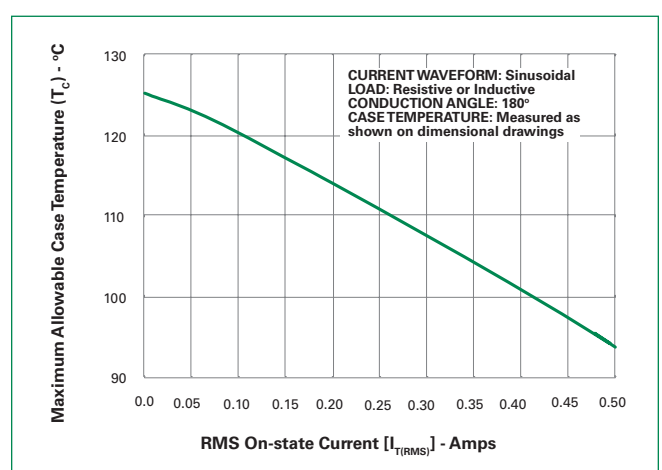
**Figure 4: On-State Current vs. On-State Voltage (Typical)**



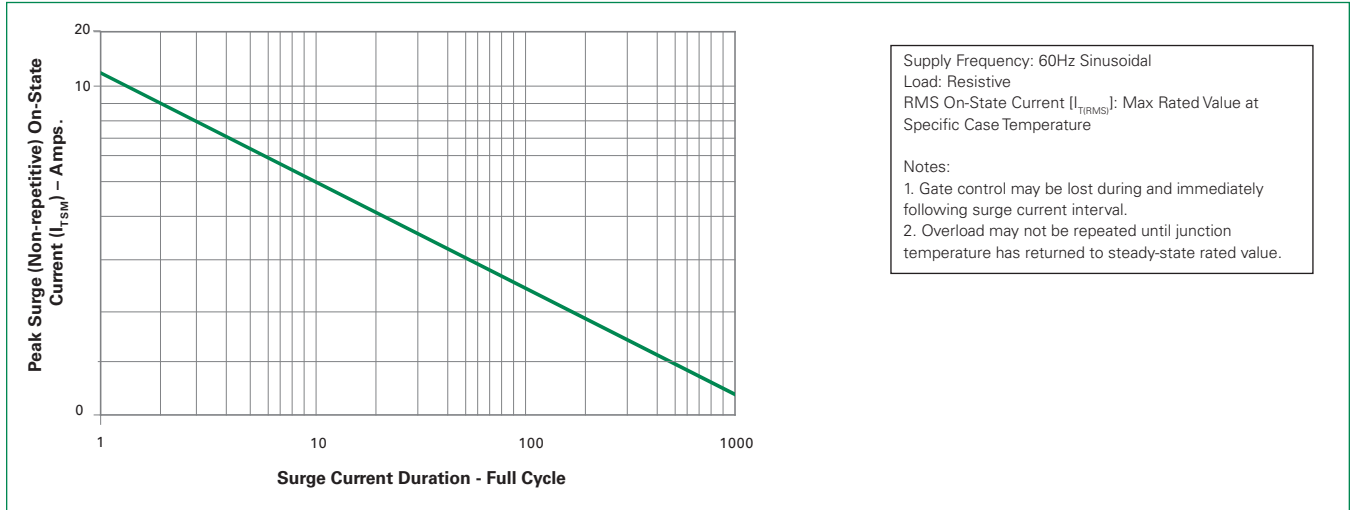
**Figure 5: Power Dissipation (Typical) vs. RMS On-State Current**



**Figure 6: Maximum Allowable Case Temperature vs. On-State Current**

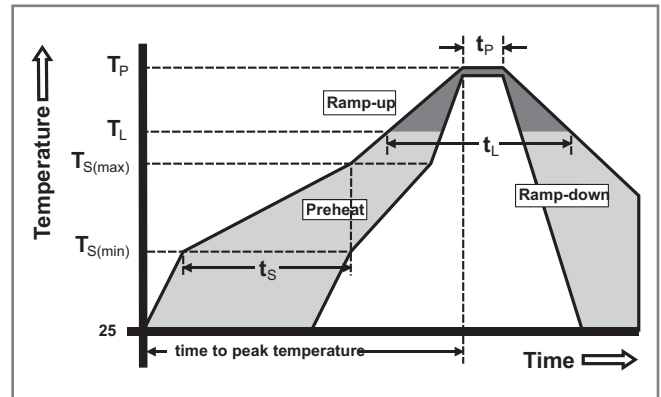


**Figure 7: Surge Peak On-State Current vs. Number of Cycles**



**Soldering Parameters**

Reflow Condition		Pb – Free assembly
Pre Heat	- Temperature Min (T <sub>s(min)</sub> )	150°C
	- Temperature Max (T <sub>s(max)</sub> )	200°C
	- Time (min to max) (t <sub>s</sub> )	60 – 180 secs
Average ramp up rate (Liquidus Temp) (T <sub>L</sub> ) to peak		5°C/second max
T <sub>S(max)</sub> to T <sub>L</sub> - Ramp-up Rate		5°C/second max
Reflow	- Temperature (T <sub>L</sub> ) (Liquidus)	217°C
	- Time (min to max) (t <sub>s</sub> )	60 – 150 seconds
Peak Temperature (T <sub>p</sub> )		260 <sup>+0/-5</sup> °C
Time within 5°C of actual peak Temperature (t <sub>p</sub> )		20 – 40 seconds
Ramp-down Rate		5°C/second max
Time 25°C to peak Temperature (T <sub>p</sub> )		8 minutes Max.
Do not exceed		280°C



**Physical Specifications**

<b>Terminal Finish</b>	100% Matte Tin-plated.
<b>Body Material</b>	UL Recognized compound meeting flammability rating V-0.
<b>Lead Material</b>	Copper Alloy

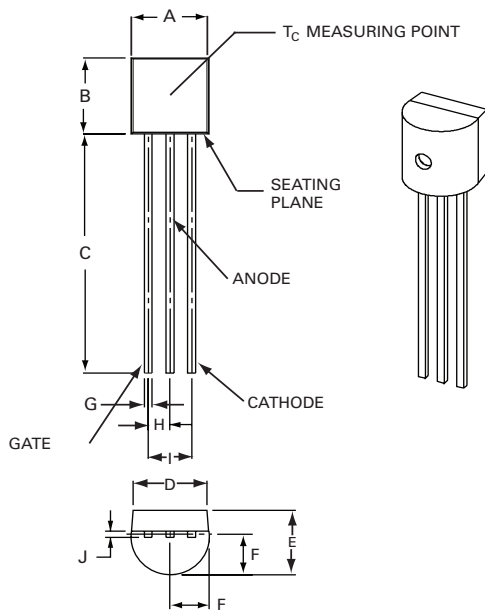
**Design Considerations**

Careful selection of the correct component for the application's operating parameters and environment will go a long way toward extending the operating life of the Thyristor. Good design practice should limit the maximum continuous current through the main terminals to 75% of the component rating. Other ways to ensure long life for a power discrete semiconductor are proper heat sinking and selection of voltage ratings for worst case conditions. Overheating, overvoltage (including dv/dt), and surge currents are the main killers of semiconductors. Correct mounting, soldering, and forming of the leads also help protect against component damage.

**Reliability/Environmental Tests**

Test	Specifications and Conditions
<b>AC Blocking</b>	MIL-STD-750, M-1040, Cond A Applied Peak AC voltage @ 125°C for 1008 hours
<b>Temperature Cycling</b>	MIL-STD-750, M-1051, 1000 cycles; -55°C to +150°C; 15-min dwell-time
<b>Temperature/Humidity</b>	EIA / JEDEC, JESD22-A101 1008 hours; 320V - DC: 85°C; 85% rel humidity
<b>UHAFT</b>	JESD22-A118, 96 hours, 130°C, 85%RH
<b>High Temp Storage</b>	MIL-STD-750, M-1031, 1008 hours; 150°C
<b>Low-Temp Storage</b>	1008 hours; -40°C
<b>Resistance to Solder Heat</b>	MIL-STD-750 Method 2031
<b>Solderability</b>	ANSI/J-STD-002, category 3, Test A
<b>Lead Bend</b>	MIL-STD-750, M-2036 Cond E

**Dimensions – TO-92**



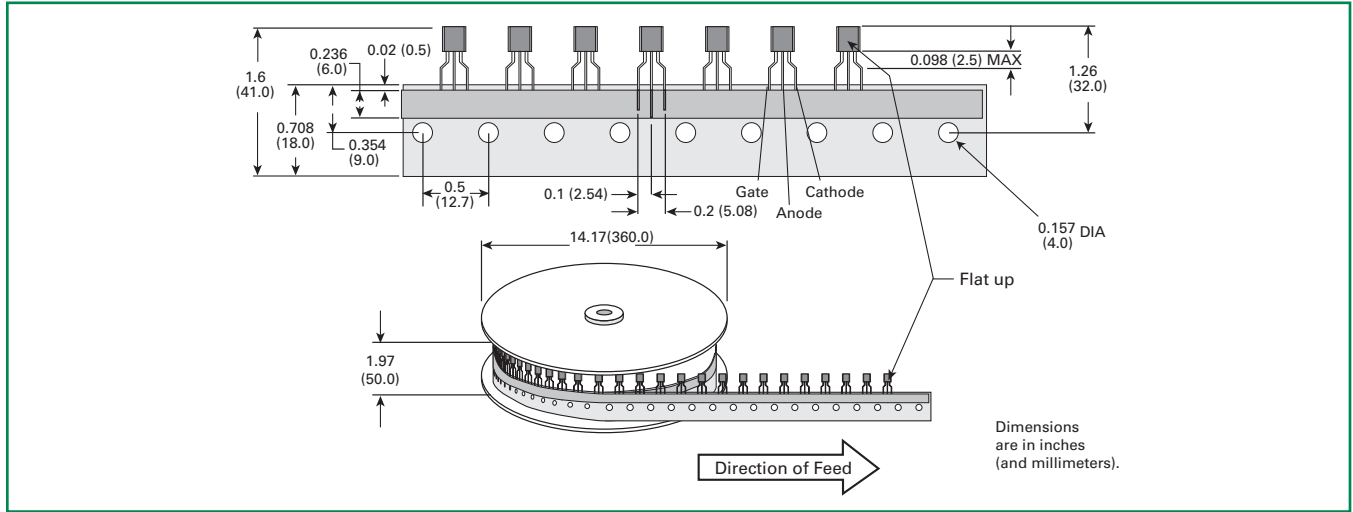
Dimension	Inches		Millimeters	
	Min	Max	Min	Max
A	0.175	0.205	4.450	5.200
B	0.170	0.210	4.320	5.330
C	0.500		12.70	
D	0.135		3.430	
E	0.125	0.165	3.180	4.190
F	0.080	0.105	2.040	2.660
G	0.016	0.021	0.407	0.533
H	0.045	0.055	1.150	1.390
I	0.095	0.105	2.420	2.660
J	0.015	0.020	0.380	0.500

**Packing Option**

Part Number	Marking	Weight	Packing Mode	Base Quantity
S802ECS	S802ECS	0.217G	Bulk	2500
S802ECSR	S802ECS	0.217G	Tape & Reel	2000
S802ECSAP	S802ECS	0.217G	Ammo Pack	2000

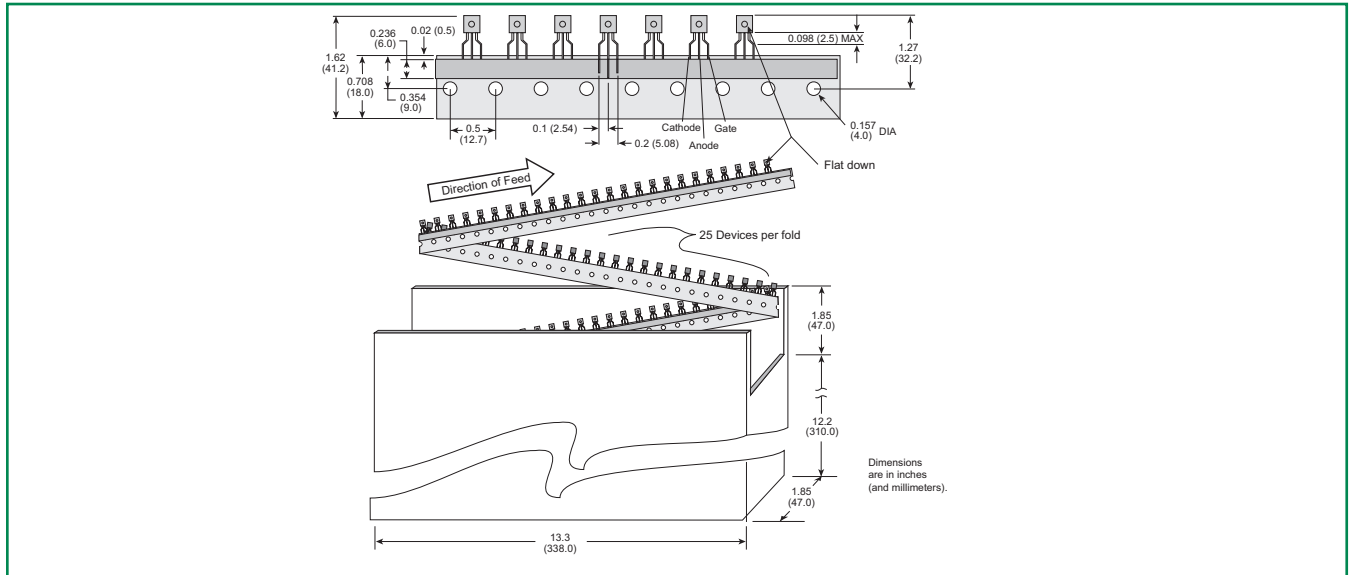
### TO-92 (3-lead) Reel Pack (RP) Radial Leaded Specifications

Meets all EIA-468-C Standards

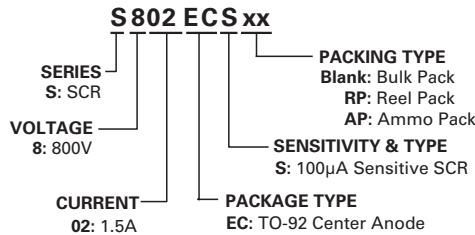


### TO-92 (3-lead) Ammo Pack (AP) Radial Leaded Specifications

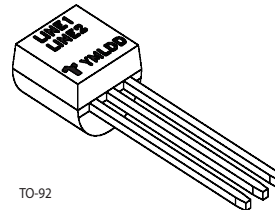
Meets all EIA-468-C Standards



### Part Numbering System



### Part Marking System



Line1 = Littelfuse Part Number  
 Line2 = continuation...Littelfuse Part Number  
 Y = Last Digit of Calendar Year  
 M = Letter Month Code (A-L for Jan-Dec)  
 L = Location Code  
 DD = Calendar Date

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