Lonten N-channel 600V, 2A Power MOSFET

Description

The Power MOSFET is fabricated using the advanced planer VDMOS technology. The resulting device has low conduction resistance, superior switching performance and high avalance energy.

Features

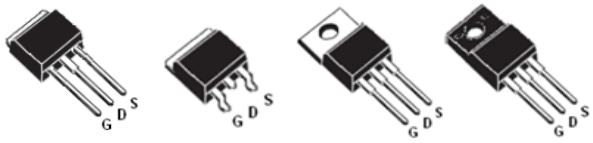
- Low $R_{DS(on)}$
- Low gate charge (typ. Q_g = 10.2 nC)
- 100% UIS tested
- RoHS compliant

Applications

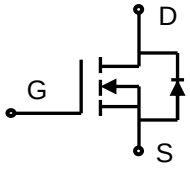
- Power factor correction.
- Switched mode power supplies.
- LED driver.

Product Summary

V_{DSS}	600V
I_D	2A
$R_{DS(on),max}$	4.5Ω
$Q_{g,typ}$	10.2 nC



TO-251 **TO-252** **TO-220** **TO-220F**



N-Channel MOSFET

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	600	V
Continuous drain current (T_C = 25°C)	I_D	2	A
(T_C = 100°C)		1.3	A
Pulsed drain current [1]	I_{DM}	8	A
Gate-Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse [2]	E_{AS}	80	mJ
Peak diode recovery dv/dt [3]	dv/dt	5	V/ns
Power Dissipation TO-220F (T_C = 25°C)	P_D	27	W
Derate above 25°C		0.22	W/°C
Power Dissipation TO-220\ TO-251\ TO-252 (T_C = 25°C)		35	W
Derate above 25°C		0.28	W/°C
Operating juncition and storage temperature range	T_J, T_{STG}	-55 to +150	°C
Continuous diode forward current	I_S	2	A
Diode pulse current	$I_{S,pulse}$	8	A

Thermal Characteristics

Parameter	Symbol	Value		Unit
		TO-220F	TO-220\TO-251\TO-252	
Thermal resistance, Junction-to-case	$R_{\theta JC}$	4.63	3.57	°C/W
Thermal resistance, Junction-to-ambient	$R_{\theta JA}$	100	62	°C/W

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LNC2N60	TO-220	LNC2N60	50	
LND2N60	TO-220F	LND2N60	50	
LNG2N60	TO-252	LNG2N60		3000
LNH2N60	TO-251	LNH2N60	80	

Electrical Characteristics

T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0 V, I_D=0.25 mA	600	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=0.25 mA	2	-	4	V
Drain cut-off current	I_{DSS}	V_{DS}=600 V, V_{GS}=0 V, T_j = 25°C T_j = 125°C	 - -	 -	 1 100	 μA
Gate leakage current, Forward	I_{GSSF}	V_{GS}=30 V, V_{DS}=0 V	-	-	100	nA
Gate leakage current, Reverse	I_{GSSR}	V_{GS}=-30 V, V_{DS}=0 V	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D=1 A	-	3.6	4.5	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V_{DS} = 25 V, V_{GS} = 0 V,	-	338	-	pF
Output capacitance	C_{oss}	f = 1 MHz	-	36	-	
Reverse transfer capacitance	C_{rss}		-	3.4	-	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 300 V, I_D = 2 A	-	16.8	-	ns
Rise time	t_r	R_G = 10 Ω, V_{GS}=15 V	-	35.5	-	
Turn-off delay time	$t_{d(off)}$		-	34.3	-	
Fall time	t_f		-	24.7	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	V_{DD}=480 V, I_D=2 A,	-	2.6	-	nC
Gate to drain charge	Q_{gd}	V_{GS}=0 to 10 V	-	4.7	-	
Gate charge total	Q_g		-	10.2	-	
Gate plateau voltage	$V_{plateau}$		-	5	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=2 A	-	-	1.3	V
Reverse recovery time	t_{rr}	V_R=400 V, I_F=2 A,	-	194	-	ns
Reverse recovery charge	Q_{rr}	dI_F/dt=100 A/μs	-	0.7	-	μC
Peak reverse recovery current	I_{rrm}		-	7.4	-	A

Notes:

1. Pulse width limited by maximum junction temperature.
2. L=10mH, I_{AS} = 4A, Starting T_j= 25°C.
3. I_{SD} = 2A, di/dt≤100A/us, V_{DD}≤BV_{DS}, Starting T_j= 25°C.

Electrical Characteristics Diagrams

Figure 1. Typical Output Characteristics

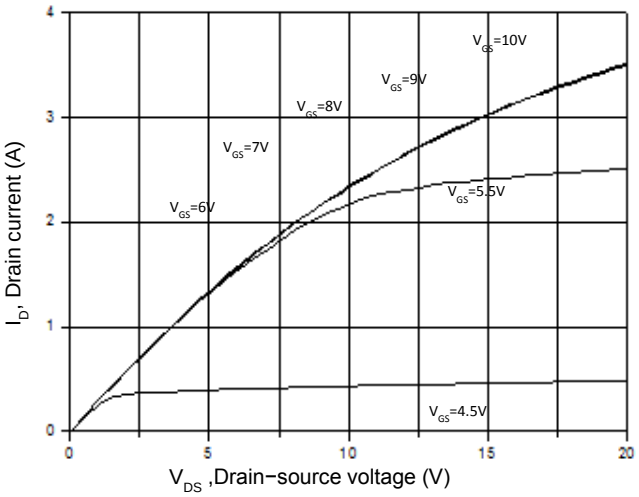


Figure 2. Transfer Characteristics

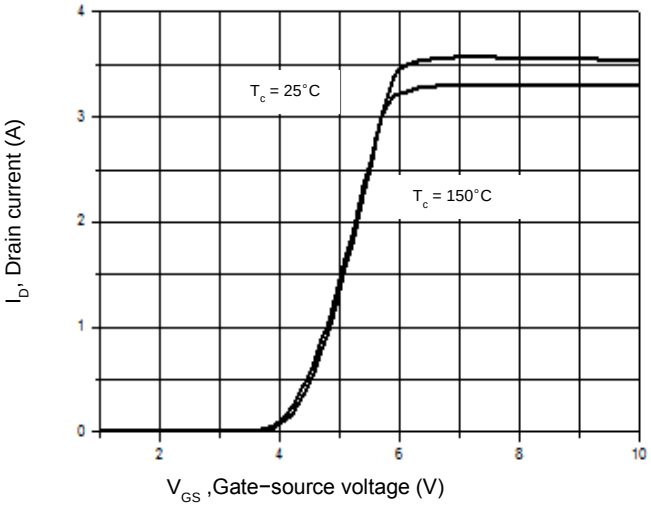


Figure 3. On-Resistance Variation vs. Drain Current

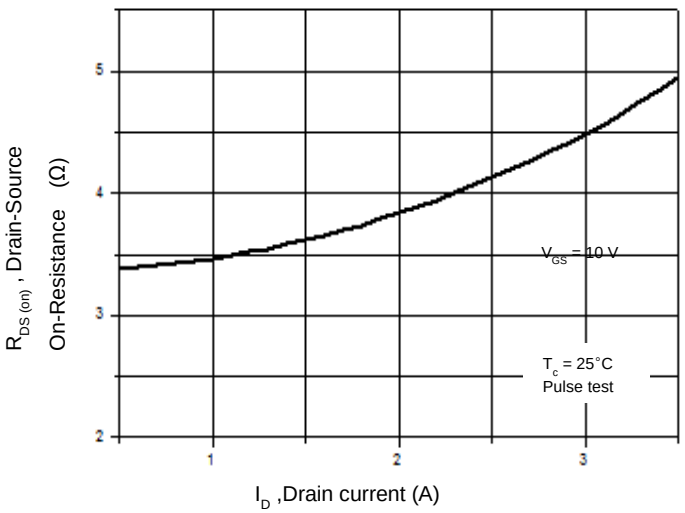


Figure 4. Threshold Voltage vs. Temperature

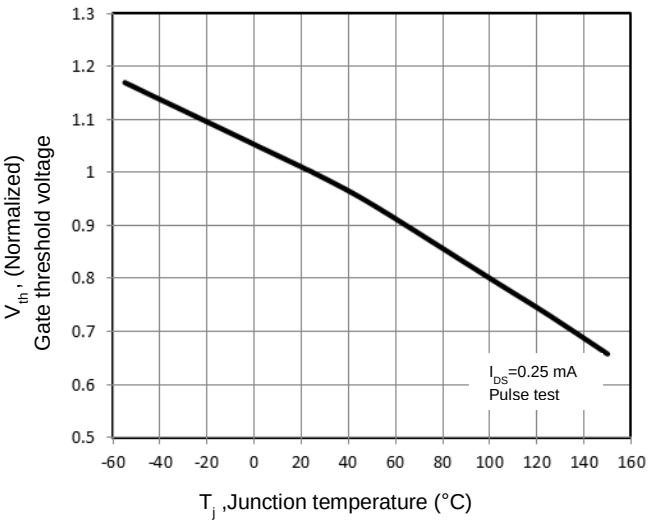


Figure 5. Breakdown Voltage vs. Temperature

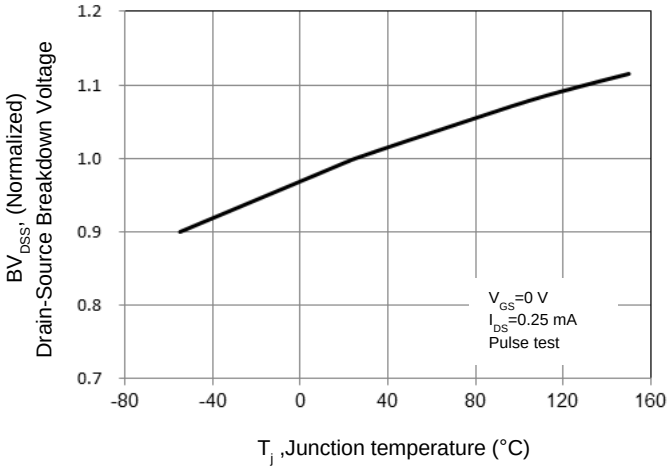


Figure 6. On-Resistance vs. Temperature

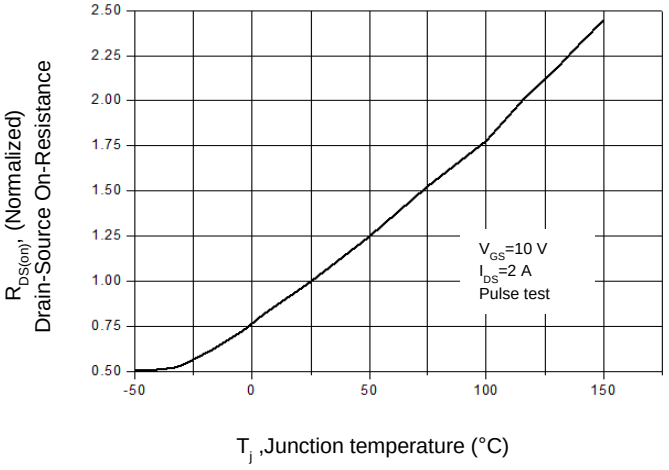


Figure 7. Capacitance Characteristics

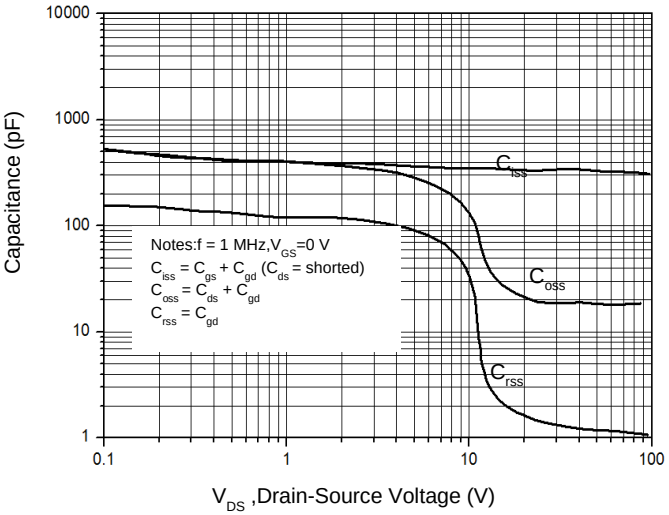


Figure 8. Gate Charge Characterist

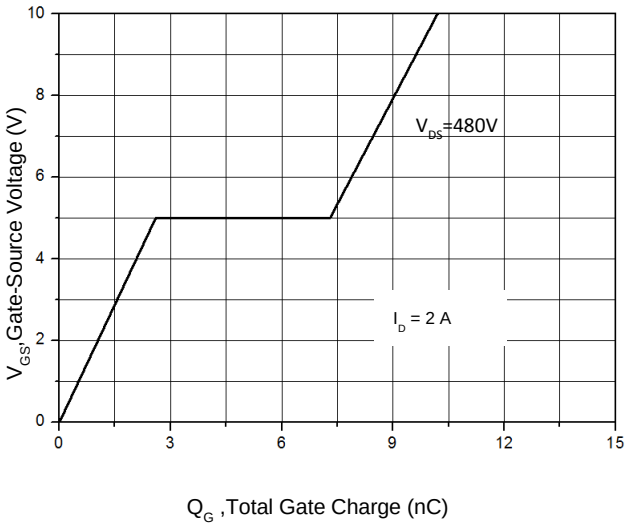


Figure 9. Maximum Safe Operating Area

TO-220F

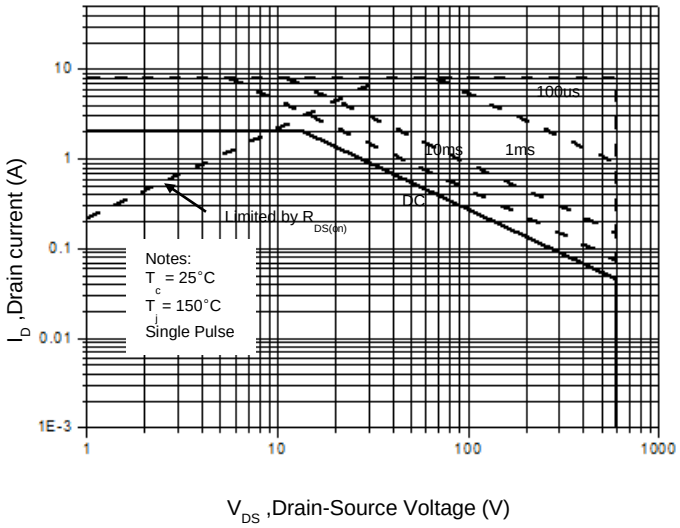


Figure 10. Maximum Safe Operating Area

TO-220/ TO-251/TO-252

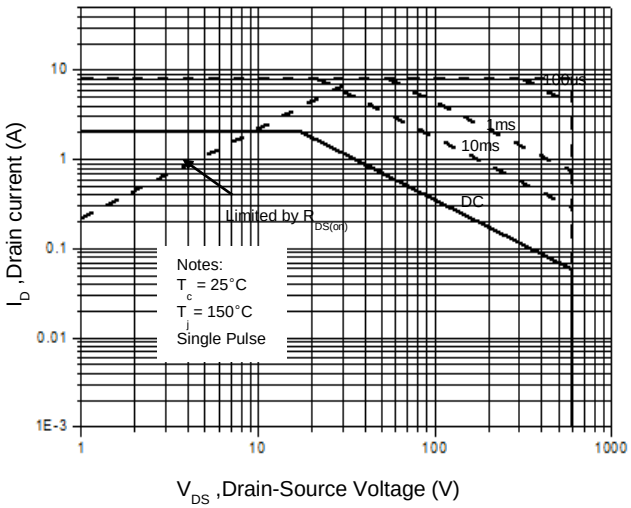


Figure 11. Power Dissipation vs. Temperature

TO-220F

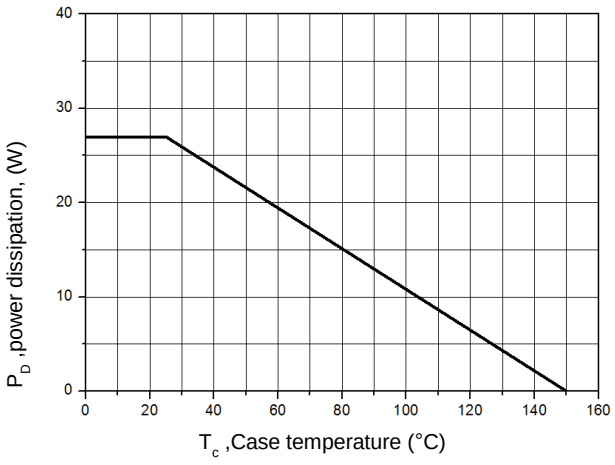


Figure 12. Power Dissipation vs. Temperature

TO-220/ TO-251/TO-252

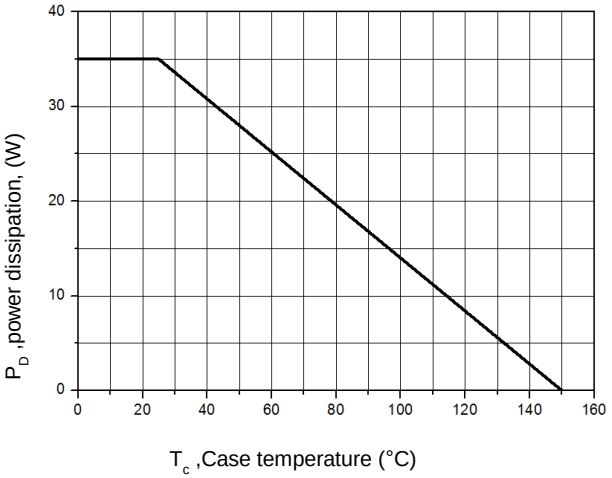


Figure 13. Continuous Drain Current vs. Temperature

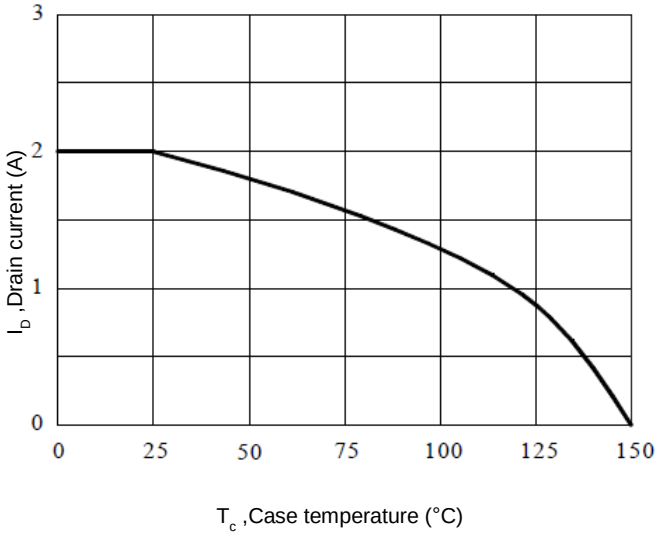


Figure 14. Body Diode Transfer Characteristics

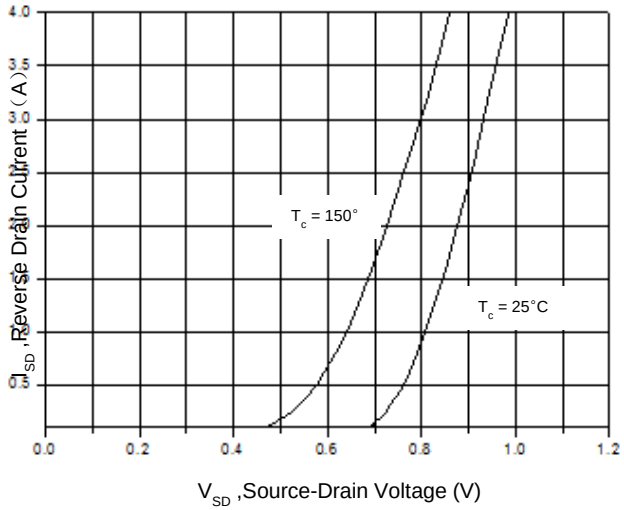


Figure 15 Transient Thermal Impendance,Junction to Case, TO-220F

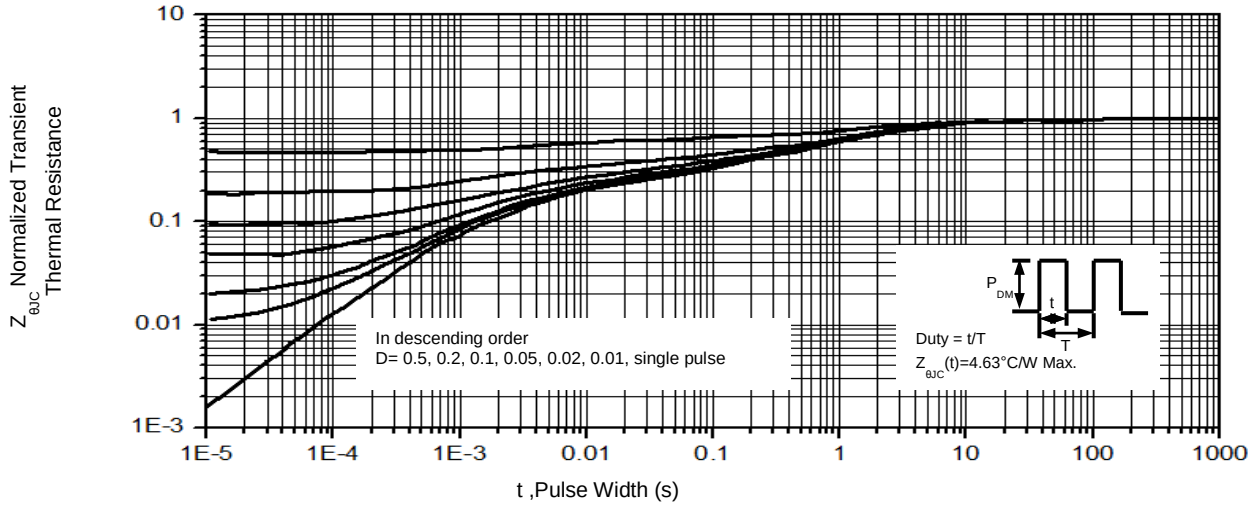
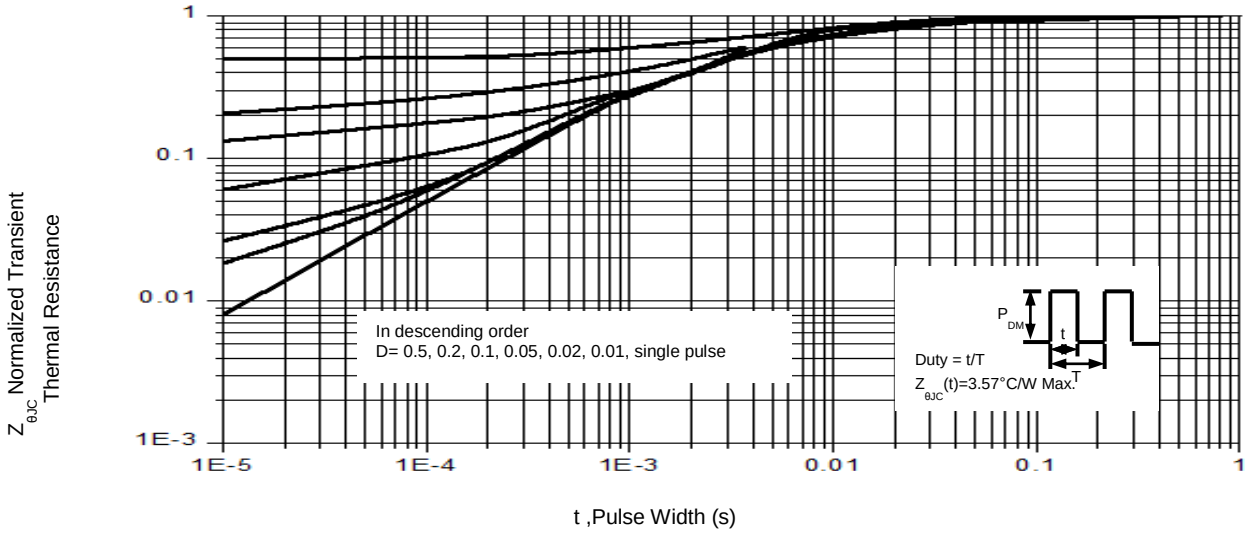
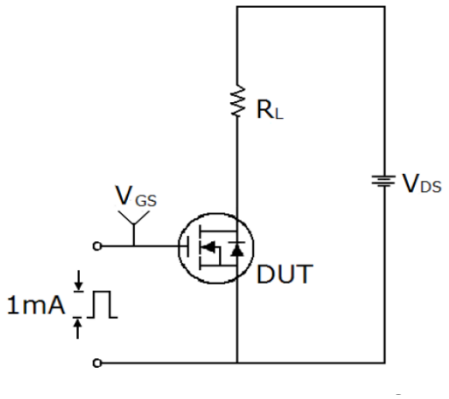
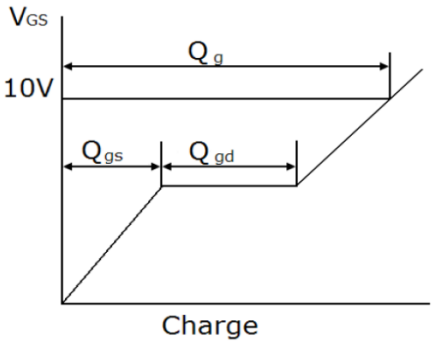


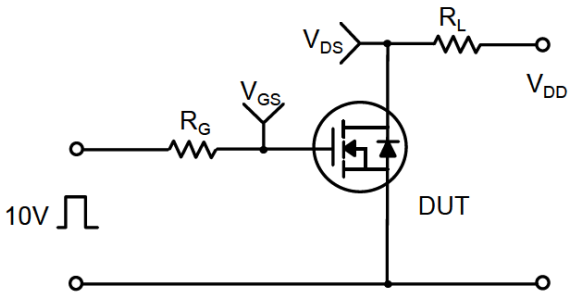
Figure 16. Transient Thermal Impendance,Junction to Case, TO-220/ TO-251/TO-252

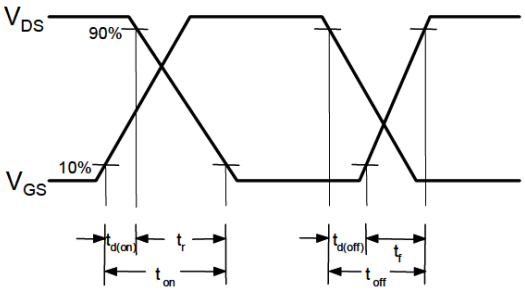


Gate Charge Test Circuit & Waveform

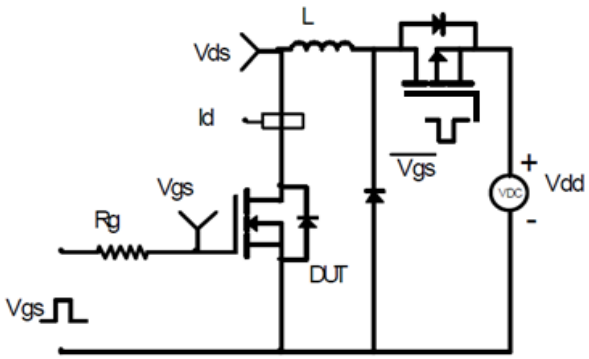


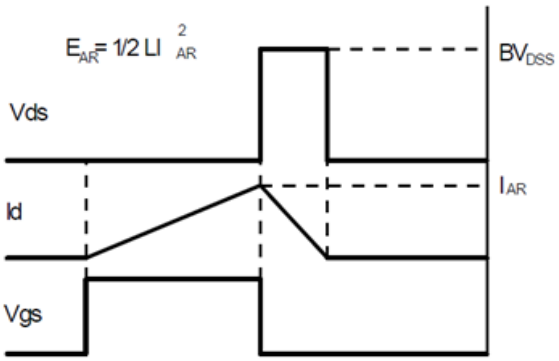






Unclamped Inductive Switching Test Circuit & Waveforms

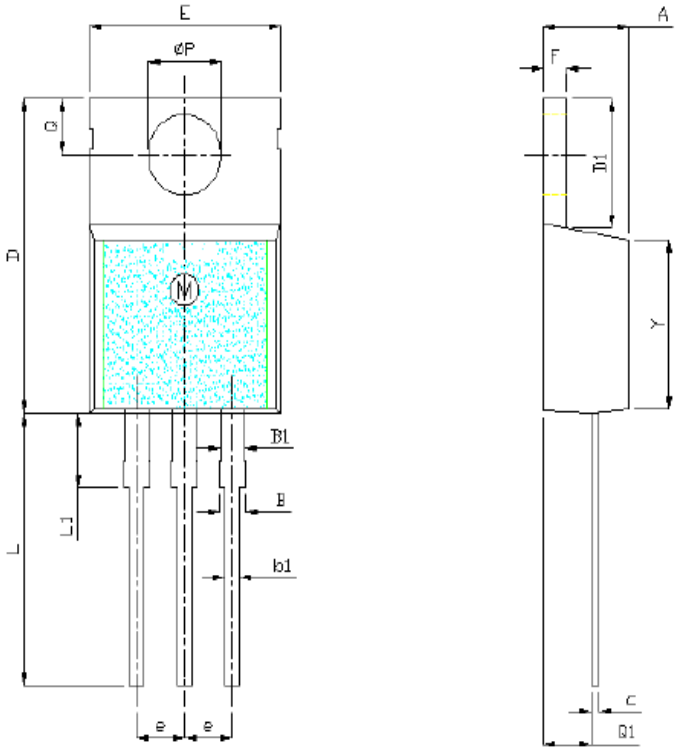




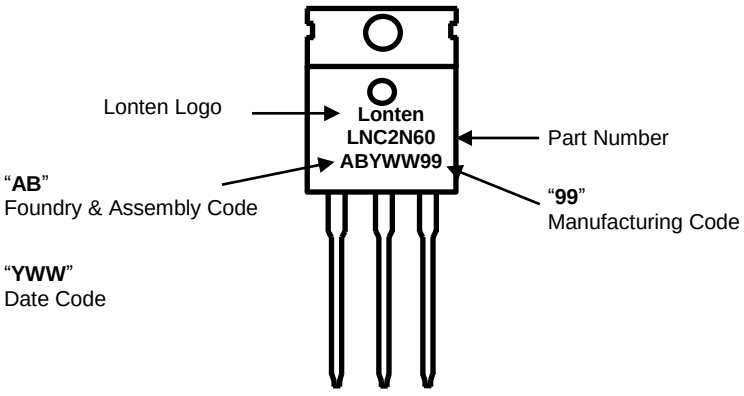
Mechanical Dimensions for TO-220

UNIT: mm

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
A	4		4.8	e	2.44	2.54	2.64
B	1.2		1.4	F	1.1		1.4
B1	1		1.4	L	12.5		14.5
b1	0.75		0.95	L1	3	3.5	4
c	0.4		0.55	ΦP	3.7	3.8	3.9
D	15		16.5	Q	2.5		3
D1	5.9		6.9	Q1	2		2.9
E	9.9		10.7	Y	8.02	8.12	8.22



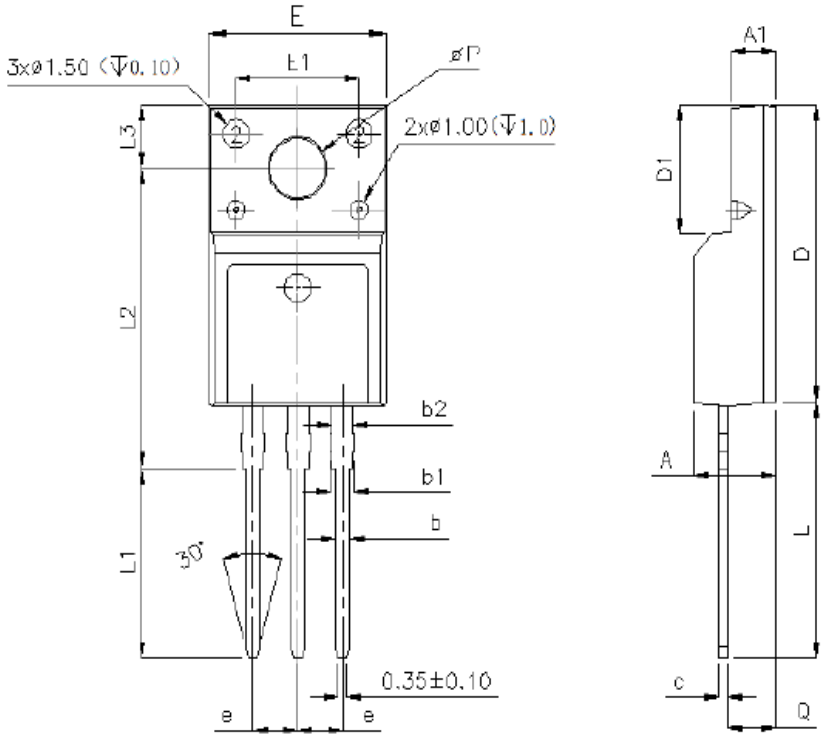
TO-220 Part Marking Information



Mechanical Dimensions for TO-220F

UNIT: mm

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
A	4.5		4.9	E1	6.5	7	7.5
A1	2.3		2.9	e	2.44	2.54	2.64
b	0.65		0.9	L	12.5		14.3
b1	1.1		1.7	L1	9.45		10.05
b2	1.2		1.4	L2	15		16
c	0.35		0.65	L3	3.2		4.4
D	14.5		16.5	ΦP	3		3.3
D1	6.1		6.9	Q	2.5		2.9
E	9.6		10.3				



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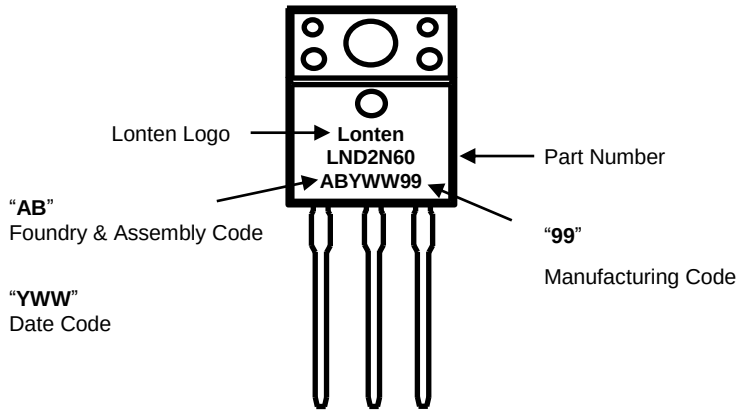
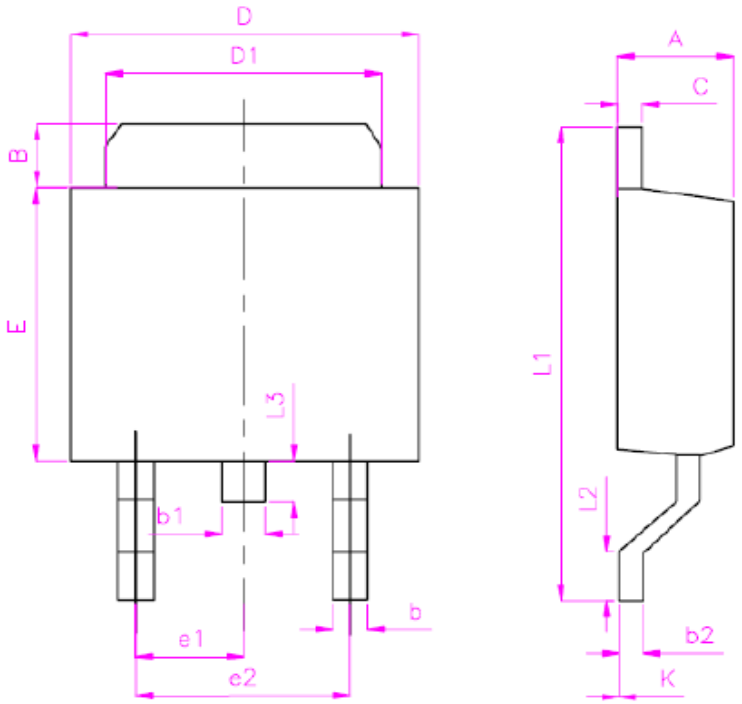


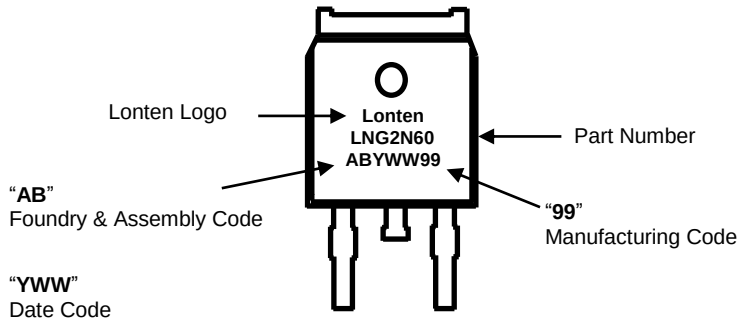
Mechanical Dimensions for TO-252

UNIT: mm

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
A	2.10		2.50	E	5.80		6.30
B	0.80		1.25	e1	2.25	2.30	2.35
b	0.50		0.85	e2	4.45		4.75
b1	0.50		0.90	L1	9.50		10.20
b2	0.45		0.60	L2	0.90		1.45
C	0.45		0.60	L3	0.60		1.10
D	6.35		6.75	K	-0.1		0.10
D1	5.10		5.50				



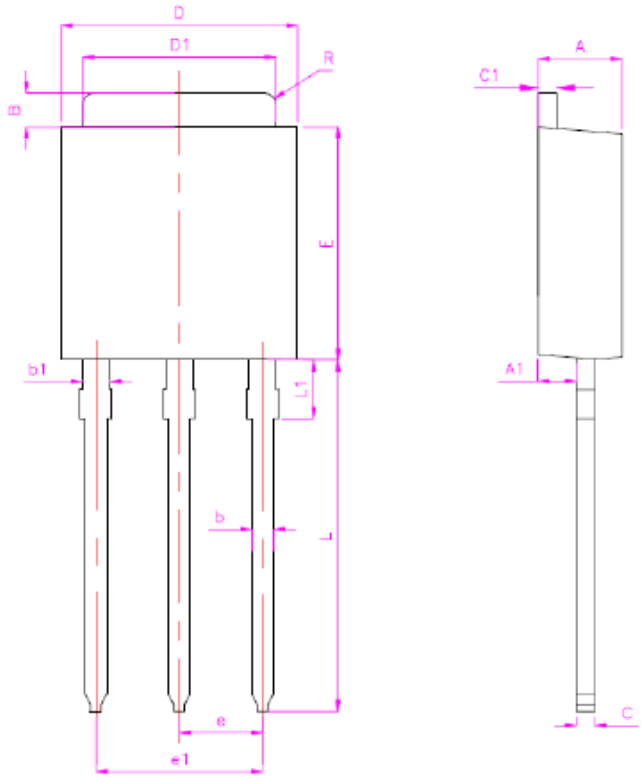
TO-252 Part Marking Information



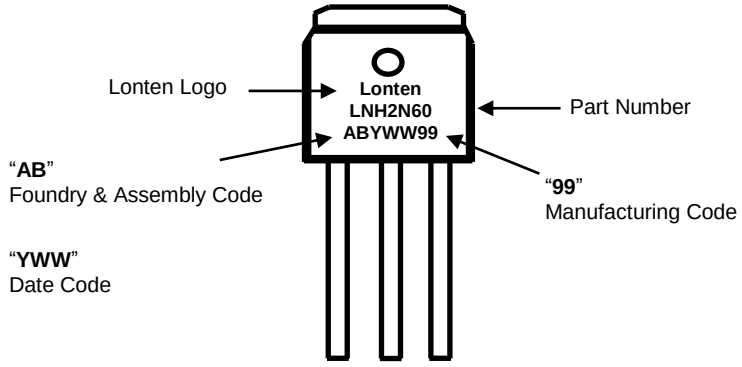
Mechanical Dimensions for TO-251

UNIT: mm

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
A	2.10		2.50	D1	5.10		5.50
A1	0.95		1.30	E	5.80		6.30
B	0.80		1.25	e	2.25	2.30	2.35
b	0.50		0.80	L	7.70		8.50
b1	0.70		0.90	L1	1.45		1.95
C	0.45		0.60	R		0.30	
C1	0.45		0.60				
D	6.35		6.75				



TO-251 Part Marking Information



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Jun. 2018 Revision 1.1

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