

Lonten N-channel 650V, 7A Power MOSFET

Description

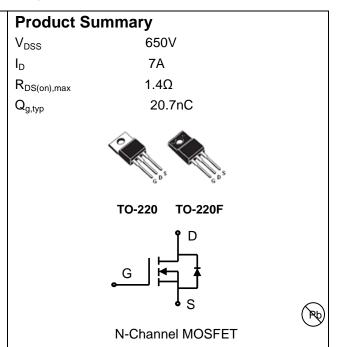
The Power MOSFET is fabricated using the advanced planar VDMOS technology. The resulting device has low conduction resistance, superior switching performance and high avalance energy.

Features

- ◆ Low R_{DS(on)}
- Low gate charge (typ. Q_g =20.7nC)
- ◆ 100% UIS tested
- RoHS compliant

Applications

- Power faction correction.
- Switched mode power supplies.
- ◆ LED driver.



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	650	V
Continuous drain current (T _C = 25°C)	I _D	7	A
(T _C = 100°C)		4.3	Α
Pulsed drain current 1)	I _{DM}	28	A
Gate-Source voltage	V _{GSS}	±30	V
Avalanche energy, single pulse 2)	E _{AS}	352	mJ
Peak diode recovery dv/dt 3)	dv/dt	5	V/ns
Power Dissipation TO-220F (T _C = 25°C)		39	W
Derate above 25°C		0.31	W/°C
Power Dissipation	P _D		
TO-220 (T _C = 25°C)		100	W
Derate above 25°C		0.8	W/°C
Operating juncition and storage temperature range	T _J , T _{STG}	-55 to +150	°C
Continuous diode forward current	Is	7	Α
Diode pulse current	I _{S,pulse}	28	Α

Thermal Characteristics

Peremeter	Cumbal		Value	l lmit	
Parameter	Symbol	TO-220F	TO-220	Unit	
Thermal resistance, Junction-to-case	R _{eJC}	3.2	1.25	°C/W	
Thermal resistance, Junction-to-ambient	$R_{\theta JA}$	62.5	110	°C/W	



Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LNC7N65	TO-220	LNC7N65	50	
LND7N65	TO-220F	LND7N65	50	

Electrical Characteristics T_c = 25°C unless otherwise noted

	650	-	-	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		-	-	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	2	-		V
$T_{j} = 25^{\circ}C$ $T_{j} = 125^{\circ}C$ $T_{j} = 125^{\circ}C$ Gate leakage current, Forward I_{GSSF} $V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$ $Gate leakage current, Reverse \qquad I_{GSSR}$ $V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$ $Drain\text{-source on-state resistance}$ $R_{DS(on)}$ $V_{GS} = 10 \text{ V}, I_{D} = 3.5 \text{ A}$ $Dynamic characteristics$ $Input capacitance \qquad C_{iss}$ $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ $Output capacitance \qquad C_{oss}$ $f = 1 \text{ MHz}$		1	4	V
$T_{j} = 125^{\circ}C$ Gate leakage current, Forward I_{GSSF} $V_{GS}=30 \text{ V}, V_{DS}=0 \text{ V}$ Gate leakage current, Reverse I_{GSSR} $V_{GS}=-30 \text{ V}, V_{DS}=0 \text{ V}$ Drain-source on-state resistance $R_{DS(on)}$ $V_{GS}=10 \text{ V}, I_{D}=3.5 \text{ A}$ Dynamic characteristics Input capacitance C_{iss} $V_{DS}=25 \text{ V}, V_{GS}=0 \text{ V},$ Output capacitance C_{oss} $f=1 \text{ MHz}$				
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	-	-	1	μΑ
	-		100	
$ \begin{array}{ccc} & & & & & & & & & & & & \\ & Drain\text{-source on-state resistance} & & & & & & & & \\ \hline \textbf{Dynamic characteristics} & & & & & & & \\ & Input capacitance & & & & & & & \\ \hline \textbf{Output capacitance} & & & & & & & \\ \hline \textbf{C}_{iss} & & & & & & & \\ \hline \textbf{V}_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \\ \hline \textbf{Output capacitance} & & & & & \\ \hline \textbf{C}_{oss} & & & & & \\ \hline \textbf{f} = 1 \text{ MHz} \\ \hline \end{array} $	-	-	100	nA
	-	-	-100	nA
	-	1.2	1.4	Ω
Output capacitance C_{oss} $f = 1 \text{ MHz}$				
· · ·	-	1090	-	
	-	111	-	pF
Reverse transfer capacitance C _{rss}	-	6.1	-	1
Turn-on delay time $t_{d(on)} \hspace{1cm} V_{DD} = 325 \text{ V, } I_D = 7 \text{ A}$	-	12.2	-	
Rise time $t_{r} \hspace{1cm} R_{G} = 10 \; \Omega, \; V_{GS} = 15 \; V$	-	33.4	-	ns
Turn-off delay time t _{d(off)}	-	53.6	-	1
Fall time t _f	-	15	-	1
Gate charge characteristics	4			
Gate to source charge Q_{gs} V_{DD} =520 V, I_D =7 A,	-	5.7	-	
Gate to drain charge Q_{gd} V_{GS} =0 to 10 V	-	7.2	-	nC
Gate charge total Q _g	-	20.7	-	1
Gate plateau voltage V _{plateau}	-	5	-	V
Reverse diode characteristics	•		•	
Diode forward voltage V_{SD} V_{GS} =0 V, I_F =7 A	-	0.85	1.5	V
Reverse recovery time t_{rr} $V_R=325~V,~I_F=7~A,$	-	373.2	-	ns
Reverse recovery charge Q_{rr} $dI_F/dt=100 \text{ A/µs}$		1		
Peak reverse recovery current I _{rm}	-	2.1	-	μC

Notes

- 1. Pulse width limited by maximum junction temperature.
- 2. L=10mH, I_{AS} = 8.4A, Starting $T_{j}\text{=}$ 25°C.
- 3. I_{SD} = 7A, di/dt \leq 100A/us, $V_{DD}\leq$ B V_{DS} , Starting T_{j} = 25°C.

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Electrical Characteristics Diagrams

Figure 1. Typical Output Characteristics

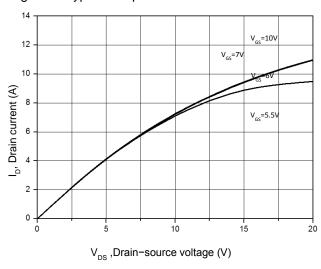


Figure 3. On-Resistance Variation vs. Drain Current

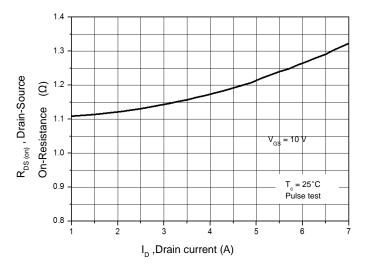


Figure 5. Breakdown Voltage vs. Temperature

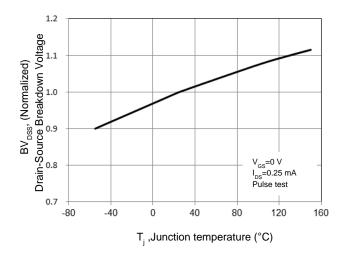


Figure 2. Transfer Characteristics

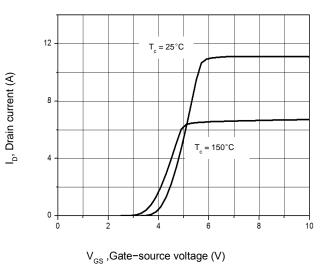


Figure 4. Threshold Voltage vs. Temperature

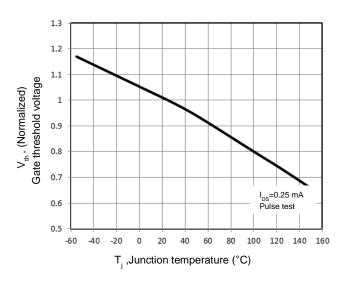


Figure 6. On-Resistance vs. Temperature

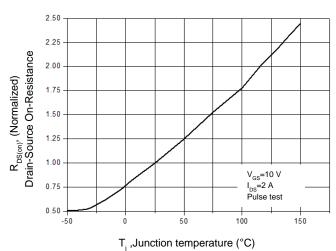




Figure 7. Capacitance Characteristics

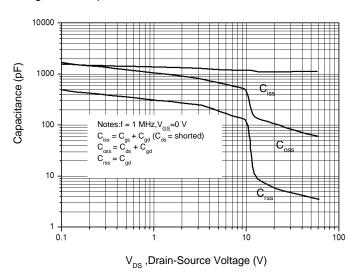


Figure 9. Maximum Safe Operating Area
TO-220F

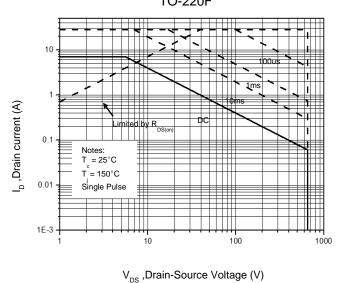


Figure 11. Power Dissipation vs. Temperature TO-220F

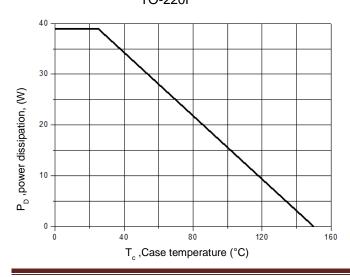


Figure 8. Gate Charge Characterist

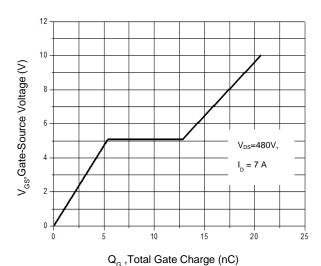
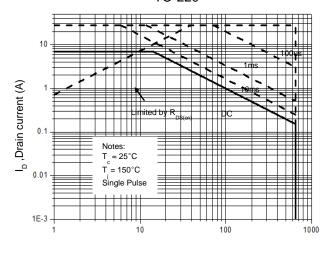


Figure 10. Maximum Safe Operating Area TO-220



V_{DS} ,Drain-Source Voltage (V)

Figure 12. Power Dissipation vs. Temperature TO-220

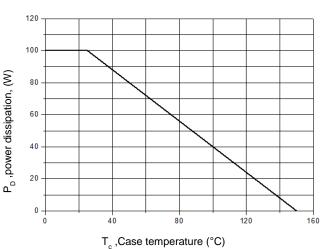




Figure 13. Continuous Drain Current vs. Temperature

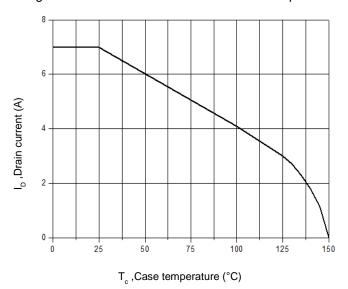


Figure 14. Body Diode Transfer Characteristics

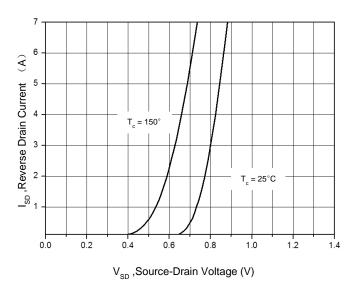


Figure 15 Transient Thermal Impendance, Junction to Case, TO-220F

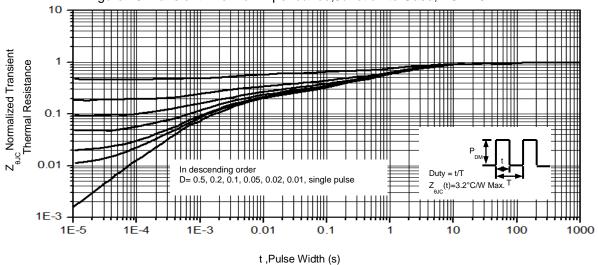
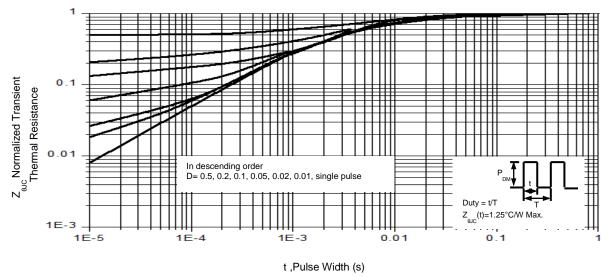


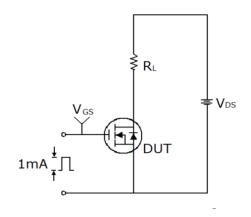
Figure 16. Transient Thermal Impendance, Junction to Case, TO-220

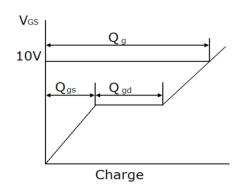


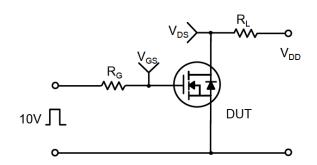
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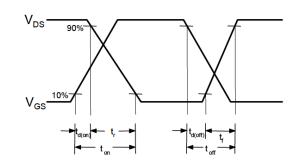


Gate Charge Test Circuit & Waveform

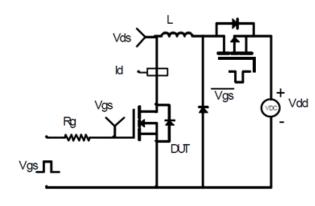


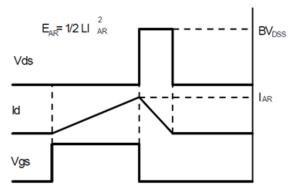






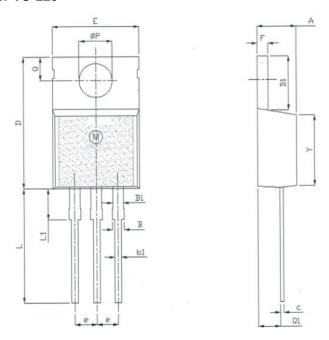
Unclamped Inductive Switching Test Circuit & Waveforms







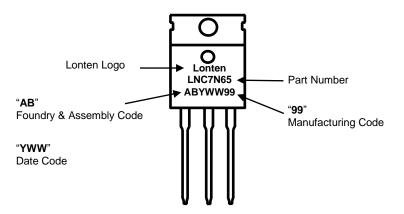
Mechanical Dimensions for TO-220



UNIT: mm

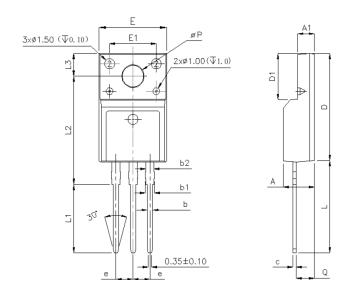
SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
Α	4		4. 8	е	2. 44	2. 54	2. 64
В	1. 2		1.4	F	1.1		1.4
B1	1		1.4	L	12. 5		14. 5
b1	0. 75		0. 95	L1	3	3. 5	4
С	0. 4		0. 55	ФР	3. 7	3. 8	3. 9
D	15		16. 5	Q	2. 5		3
D1	5. 9		6. 9	Q1	2		2. 9
E	9. 9		10. 7	Y	8. 02	8. 12	8. 22

TO-220 Part Marking Information





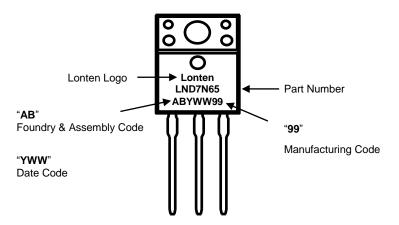
Mechanical Dimensions for TO-220F



UNIT: mm

SYMBOL	MIN	NOM	MAX	SYMBOL	MIN	NOM	MAX
Α	4. 5		4. 9	E1	6. 5	7	7. 5
A 1	2. 3		2. 9	е	2. 44	2. 54	2. 64
b	0. 65		0. 9	L	12. 5		14. 3
b1	1.1		1.7	L1	9. 45		10. 05
b2	1. 2		1.4	L2	15		16
С	0. 35		0. 65	L3	3. 2		4. 4
D	14. 5		16. 5	ФР	3		3. 3
D1	6. 1		6. 9	Q	2. 5		2. 9
E	9. 6		10. 3				

TO-220F Part Marking Information





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