

Lonten N-channel 45V, 70A, 9mΩ Power MOSFET

Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- \bullet 45V,70A,R_{DS(ON).max}=9m Ω @V_{GS}=10V
- Improved dv/dt capability
- Fast switching
- ♦ 100% EAS Guaranteed
- Green device available

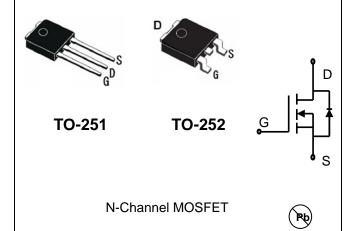
Applications

- Motor Drives
- UPS
- DC-DC Converter

Product Summary

 $\begin{array}{ll} V_{DSS} & 45V \\ R_{DS(on).max} @ \ V_{GS} \!\!=\!\! 10V & 9m\Omega \\ I_D & 70A \end{array}$

Pin Configuration



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	45	V	
Continuous drain current (T _C = 25°C)		70	Α	
Continuous drain current (T _C = 100°C)	I _D	42	А	
Pulsed drain current ¹⁾	I _{DM}	280	А	
Gate-Source voltage	V _{GSS}	±20	V	
Avalanche energy ²⁾	E _{AS}	110	mJ	
Power Dissipation ($T_C = 25^{\circ}C$)	P _D	83	W	
Storage Temperature Range	T _{STG}	-55 to +150	°C	
Operating Junction Temperature Range	TJ	-55 to +150	°C	

Thermal Characteristics

Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	1.5	°C/W	



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Package Marking and Ordering Information

Device	Device Package	Marking
LNH045R090	TO-251	LNH045R090
LNG045R090	TO-252	LNG045R090

Electrical Characteristics T_J = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics	ı			•		
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =250uA	45			V
Gate threshold voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.9		1.8	V
		V _{DS} =45 V, V _{GS} =0 V, T _J = 25°C			1	μΑ
Drain-source leakage current	I _{DSS}	V _{DS} =36 V, V _{GS} =0 V, T _J = 125°C			10	μΑ
Gate leakage current, Forward	I _{GSSF}	V _{GS} =20 V, V _{DS} =0 V			100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-20 V, V _{DS} =0 V			-100	nA
Drain source on state registeres	В	V _{GS} =10 V, I _D =20 A		6.2	9	mΩ
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =4.5 V, I _D =10 A		8	11.5	mΩ
Forward transconductance	g _{fs}	V _{DS} =5 V , I _D =10A		36		S
Dynamic characteristics				•		
Input capacitance	C _{iss}			2440		
Output capacitance	Coss	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		190		pF
Reverse transfer capacitance	C _{rss}	- F = 1MHz		126		
Turn-on delay time	t _{d(on)}			14		
Rise time	t _r	$V_{DD} = 25V, V_{GS} = 10V, I_{D} = 10 A,$		110		- ns
Turn-off delay time	t _{d(off)}	$R_G=27\Omega$		322		
Fall time	t _f]		91		
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, F=1MHz		1.84		Ω
Gate charge characteristics						•
Gate to source charge	Q_{gs}	.,		8.6		
Gate to drain charge	Q_{gd}	V _{DS} =25 V, I _D =10A,		8.2		nC
Gate charge total	Q_g	- V _{GS} = 10 V		49.3		
Drain-Source diode characterist	ics and Maxi	mum Ratings				•
Continuous Source Current	Is				70	Α
Pulsed Source Current	I _{SM}				280	Α
Diode Forward Voltage ³⁾	V _{SD}	V _{GS} =0V, I _S =10A, T _J =25℃			1.2	V
Reverse Recovery Time	t _{rr}	1 404 W/W 4004/ T 07°0		23.3		ns
Reverse Recovery Charge	Q _{rr}	l _s =10A,di/dt=100A/us, T _J =25℃		14.4		nC
		1		1	1	<u> </u>

Notes:

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: V_{DD} =25V, V_{GS} =10V, L=0.5mH, I_{AS} =21A, R_G =25 Ω , Starting T_J =25 $^{\circ}$ C.
- 3: Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

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Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

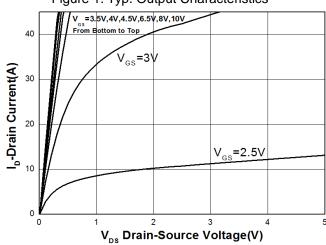


Figure 2. Transfer Characteristics

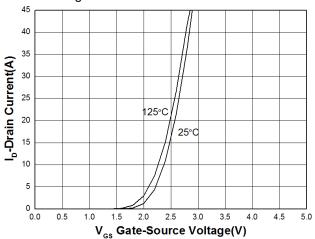


Figure 3. Capacitan ce Characteristics

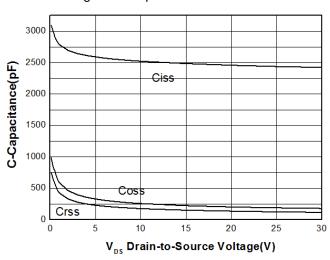


Figure 4. Gate Charge Waveform

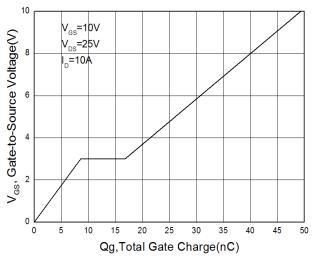


Figure 5. Body-Diode Characteristics

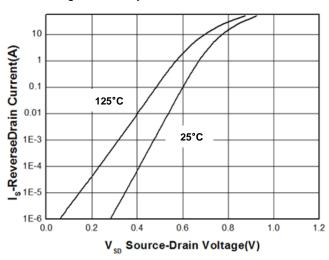
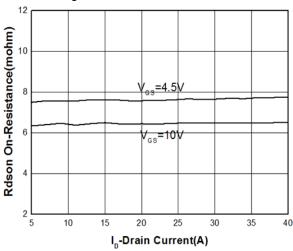


Figure 6. Rdson-Drain Current



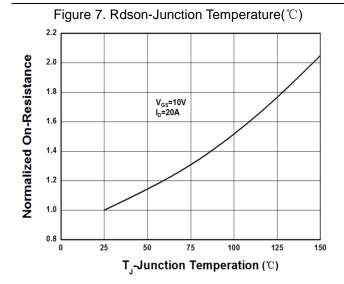
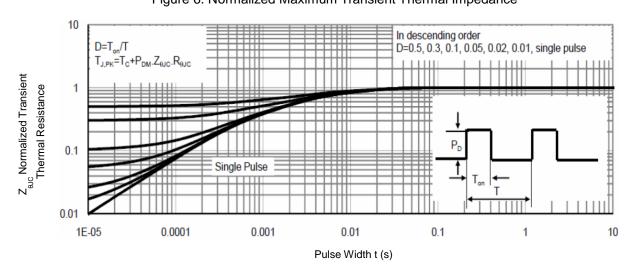


Figure 6. Normalized Maximum Transient Thermal Impedance

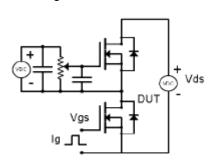


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Test Circuit & Waveform

Figure 8. Gate Charge Test Circuit & Waveform



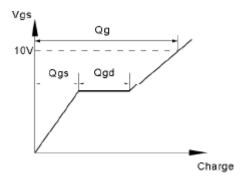
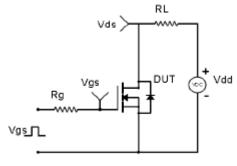


Figure 9. Resistive Switching Test Circuit & Waveforms



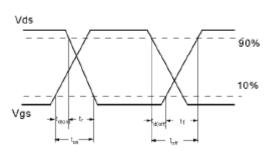
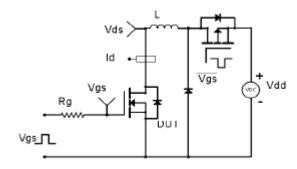


Figure 10. Unclamped Inductive Switching (UIS) Test Circuit & Waveform



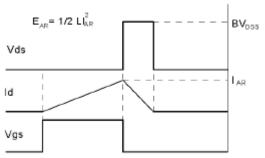
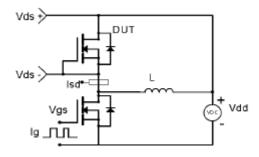
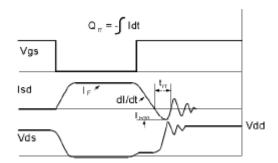


Figure 11. Diode Recovery Circuit & Waveform

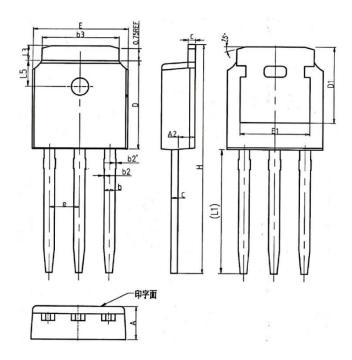




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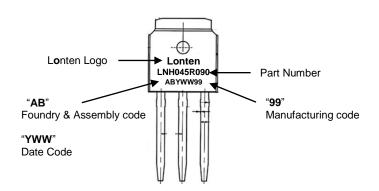


Mechanical Dimensions for TO-251



COMMON DIMENSIONS						
SYMBOL	MM			INCH		
STIVIBOL	MIN	NOM	MAX	MIN	NOM	MAX
Α	2.20	2.30	2.38	0.087	0.091	0.094
A2	0.97	1.07	1.17	0.038	0.042	0.046
b	0.68	0.78	0.90	0.027	0.031	0.035
b2	0.00	0.04	0.10	0.000	0.002	0.004
b2'	0.00	0.04	0.10	0.000	0.002	0.004
b3	5.20	5.33	5.46	0.205	0.210	0.215
С	0.43	0.53	0.61	0.017	0.021	0.024
D	5.98	6.10	6.22	0.235	0.240	0.245
D1	5.30REF			0.209REF		
E	6.40	6.60	6.73	0.252	0.260	0.265
E1	4.63	-	ı	0.182	-	-
е	2.286BSC			Ó	0.090BS0	
Н	16.22	16.52	16.82	0.639	0.650	0.662
L1	9.15	9.40	9.65	0.360	0.370	0.380
L3	0.88	1.02	1.28	0.035	0.040	0.050
L5	1.65	1.80	1.95	0.065	0.071	0.077

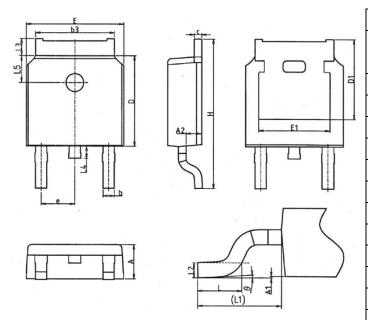
TO-251 Part Marking Information



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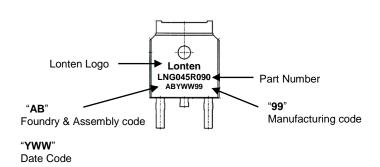


Mechanical Dimensions for TO-252



COMMON DIMENSIONS						
SYMBOL	MM			INCH		
STIVIBOL	MIN	NOM	MAX	MIN	NOM	MAX
А	2.20	2.30	2.38	0.087	0.091	0.094
A1	0.00	-	0.20	0.000	-	0.008
A2	0.97	1.07	1.17	0.038	0.042	0.046
b	0.68	0.78	0.90	0.027	0.031	0.035
b3	5.20	5.33	5.46	0.205	0.210	0.215
С	0.43	0.53	0.61	0.017	0.021	0.024
D	5.98	6.10	6.22	0.235	0.240	0.245
D1		5.30REF	-	0.209REF		
E	6.40	6.60	6.73	0.252	0.260	0.265
E1	4.63	1	1	0.182	1	•
е		2.286BS	С	0.090BSC		
Н	9.40	10.10	10.50	0.370	0.398	0.413
L	1.38	1.50	1.75	0.054	0.059	0.069
L1	2.90REF			0.114REF		
L2	0.51BSC			0.020BSC		
L3	0.88	-	1.28	0.035	-	0.050
L4	0.50	-	1.00	0.020	-	0.039
L5	1.65	1.80	1.95	0.065	0.071	0.077
θ	0°	-	8°	0°	-	8°

TO-252 Part Marking Information



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