

# Lonten P-channel -30V, -4.3A, 46mΩ Power MOSFET

Description	Product Summary		
These P-Channel enhancement mode power field	V <sub>DSS</sub> -30V		
effect transistors are using trench DMOS	$R_{DS(on).max}$ ( $W_{GS}$ =-10V 46m $\Omega$		
technology. This advanced technology has been	ID -4.3A		
especially tailored to minimize on-state resistance,			
provide superior switching performance, and with			
stand high energy pulse in the avalanche and			
commutation mode. These devices are well suited	Pin Configuration		
for high efficiency fast switching applications.			
<ul> <li>Features</li> <li>-30V,-4.3A,R<sub>DS(ON).max</sub>=46mΩ@V<sub>GS</sub>=-10V</li> <li>Improved dv/dt capability</li> <li>Fast switching</li> <li>Green device available</li> </ul>	SOT-23-3		
Applications			
PWM applications			
♦ Load switch	P-Channel MOSFET		
Portable Equipment	<u> </u>		

#### Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-30	V
Continuous drain current ( $T_A = 25^{\circ}C$ )		-4.3	А
Continuous drain current ( $T_A$ = 100°C )	I <sub>D</sub>	-2.7	A
Pulsed drain current <sup>1)</sup>	I <sub>DM</sub>	-17.2	А
Gate-Source voltage	V <sub>GSS</sub>	±20	V
Power Dissipation ( $T_A = 25^{\circ}C$ )	P <sub>D</sub>	1.3	W
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C
Operating Junction Temperature Range	TJ	-55 to +150	°C

### **Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JA}$	96	°C/W



### Package Marking and Ordering Information

Device	Device Package	Marking
LPSA3487	SOT-23-3	3487

#### Electrical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics	I			<u>.</u>		1
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =-250uA	-30			V
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=-250$ uA	-1.2	-1.7	-2.2	V
		$V_{DS}$ =-30 V, $V_{GS}$ =0 V, $T_{J}$ = 25°C			-1	μΑ
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0 V, T <sub>J</sub> = 125°C			-10	μA
Gate leakage current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =20 V, V <sub>DS</sub> =0 V			100	nA
Gate leakage current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-20 V, V <sub>DS</sub> =0 V			-100	nA
Drain course on state registence	Р	V <sub>GS</sub> =-10 V, I <sub>D</sub> =-4.3 A		33	46	mΩ
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5 V, I <sub>D</sub> =-3A		43	72	mΩ
Forward transconductance	<b>g</b> <sub>fs</sub>	$V_{DS} = -5 V$ , $I_D = -4.3 A$		10		S
Dynamic characteristics						
Input capacitance	C <sub>iss</sub>			940		pF
Output capacitance	C <sub>oss</sub>	$V_{DS} = -15 V, V_{GS} = 0 V,$ F = 1MHz		103		
Reverse transfer capacitance	C <sub>rss</sub>			88		
Turn-on delay time	t <sub>d(on)</sub>			4.0		
Rise time	tr	VDD=-10V,VGS=-4.5V, I <sub>D</sub> =-4.3A, Rg=3Ω		31.1		
Turn-off delay time	t <sub>d(off)</sub>			38.9		. ns
Fall time	t <sub>f</sub>			8.9		
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,f=1MHz		11		Ω
Gate charge characteristics		· · ·				
Gate to source charge	Q <sub>gs</sub>			2.4		
Gate to drain charge	Q <sub>gd</sub>	$V_{DS}$ =-15 V, I <sub>D</sub> =-4.3A,		2.9		nC
Gate charge total	Qg	- V <sub>GS</sub> =-10 V		14.8		
Drain-Source diode characteris	tics and Maxi	num Ratings				
Continuous Source Current	Is				-4.3	А
Pulsed Source Current <sup>2)</sup>	I <sub>SM</sub>				-17.2	А
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25℃			-1.2	V

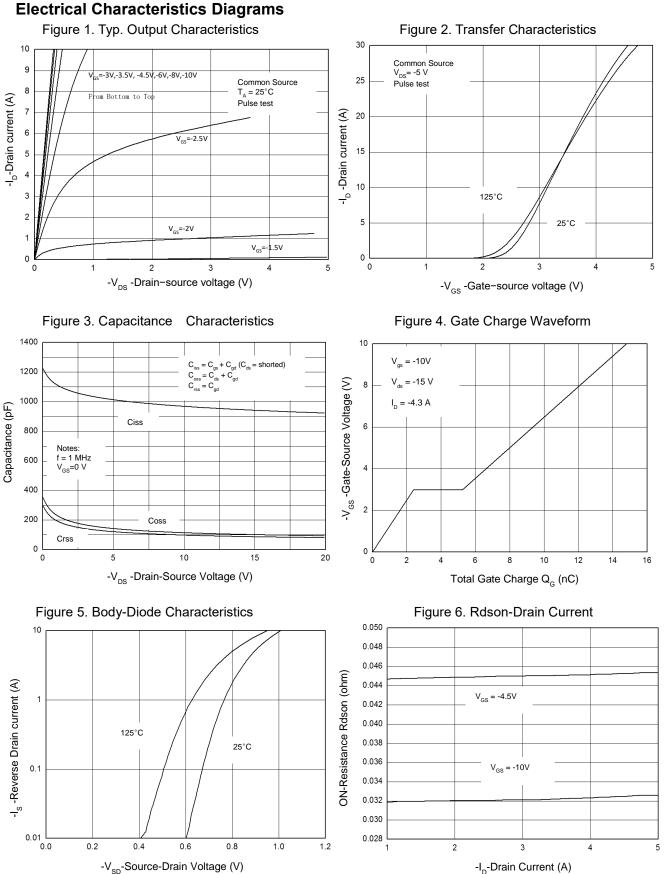
Notes:

1: Repetitive Rating: Pulse width limited by maximum junction temperature.

2: Pulse Test: Pulse Width  $\leq$ 300  $\mu$  s, Duty Cycle $\leq$ 2%.



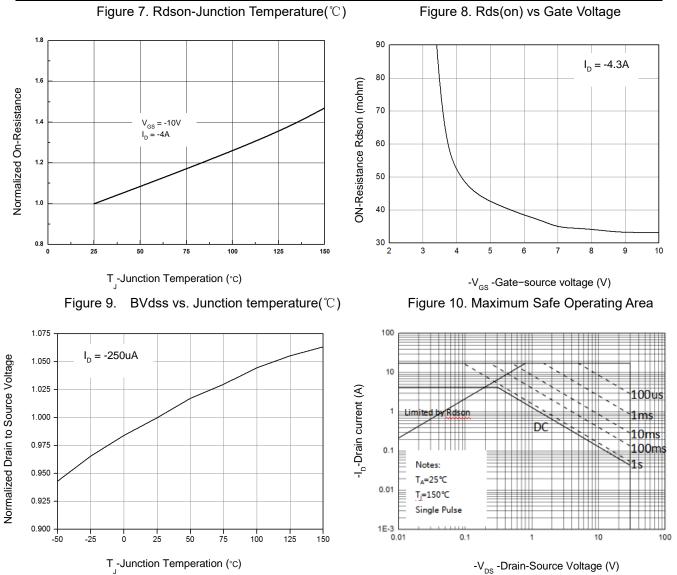
## **LPSA3487**



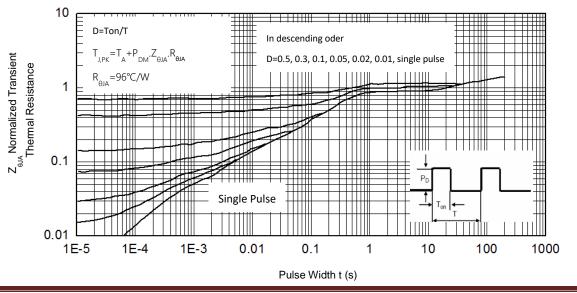
-I<sub>D</sub>-Drain Current (A)



LPSA3487



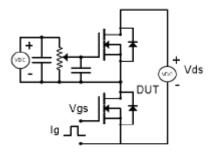






#### **Test Circuit & Waveform**

Figure 8. Gate Charge Test Circuit & Waveform



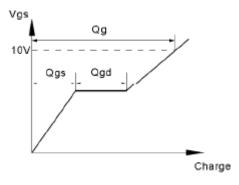


Figure 9. Resistive Switching Test Circuit & Waveforms

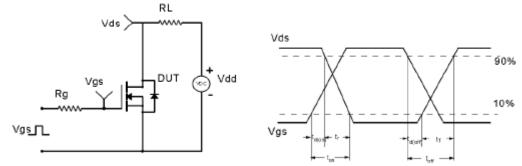
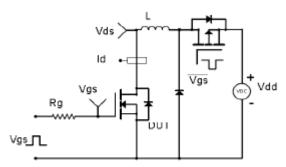


Figure 10. Unclamped Inductive Switching (UIS) Test Circuit & Waveform



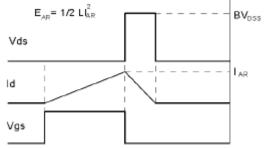
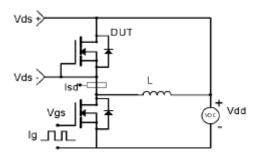
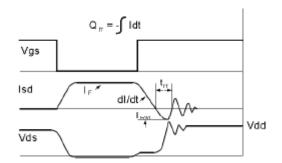


Figure 11. Diode Recovery Circuit & Waveform

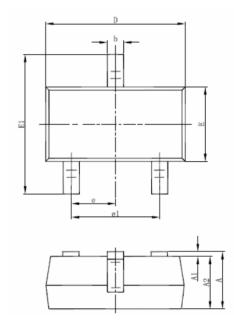




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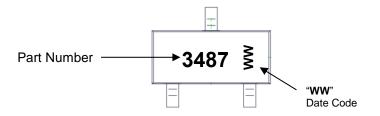


### **Mechanical Dimensions for SOT-23-3**



COMMON DIMENSIONS					
SYMBOL	MILLIMETERS		INCHS		
STIVIDUL	MIN	MAX	MIN	MAX	
А	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A2	1.00	1.20	0.039	0.047	
b	0.30	0.50	0.012	0.020	
с	0.04	0.21	0.002	0.008	
D	2.80	3.00	0.110	0.118	
E	1.50	1.70	0.059	0.067	
E1	2.60	3.00	0.102	0.118	
е	0.95 TYP.		0.037 TYP.		
e1	1.90 TYP.		0.075 TYP.		
L	0.25	0.55	0.010	0.022	
θ	0°	8°	0°	8°	

**SOT-23-3 Part Marking Information** 





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