

Lonten P-channel -20V, -2A, 110mΩ Power MOSFET

Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

Features

- $-20V, -2A, R_{DS(ON), max} = 110m\Omega@V_{GS} = -4.5V$
- Improved dv/dt capability
- Fast switching
- Green device available

Applications

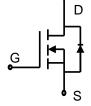
- PWM applications
- Load switch
- Portable Equipment

Product Summary

 $\begin{array}{ll} V_{DSS} & -20V \\ R_{DS(on).max} @ \ V_{GS} \!\!=\!\! -4.5V & 110 m\Omega \\ I_D & -2A \end{array}$

Pin Configuration





SOT-23

P-Channel MOSFET



Absolute Maximum Ratings T_A = 25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{ extsf{DSS}}$	-20	V
Continuous drain current (T _A = 25°C)	1	-2	А
Continuous drain current (T _A = 100°C)	I _D	-1.4	A
Pulsed drain current ¹⁾	I _{DM}	-10	A
Gate-Source voltage	V_{GSS}	±12	V
Power Dissipation (T _A = 25°C)	P _D	0.78	W
Storage Temperature Range	T _{STG}	-55 to +150	°C
Operating Junction Temperature Range	T₃	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta,IA}$	160	°C/W



Package Marking and Ordering Information

Device	Device Package	Marking
LPSC2301	SOT-23	2301

Electrical Characteristics T₁ = 25°C unless otherwise noted

Electrical Characteristics T _J = 25°C unless otherwise noted						
Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics						1
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =-250uA	-20			V
Gate threshold voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.75	-1	V
Drain-source leakage current	I _{DSS}	V _{DS} =-20 V, V _{GS} =0 V, T _J = 25°C			-1	μΑ
		V _{DS} =-16V, V _{GS} =0 V, T _J = 125°C			-10	μΑ
Gate leakage current, Forward	I _{GSSF}	V _{GS} =12 V, V _{DS} =0 V			100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-12 V, V _{DS} =0 V			-100	nA
Drain course on etate registeres	Б	V _{GS} =-4.5 V, I _D =-2 A		80	110	mΩ
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =-2.5 V, I _D =-1.5 A		100	130	mΩ
Forward transconductance	g fs	V_{DS} =-5 V , I_D =-2A		6		S
Dynamic characteristics						
Input capacitance	C _{iss}			406		
Output capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V},$ $F = 1 \text{MHz}$		51.7		pF
Reverse transfer capacitance	C _{rss}	- r = IIVIDZ		44.1		
Turn-on delay time	t _{d(on)}			10		
Rise time	t _r	$V_{DD} = -10V, V_{GS} = -4.5V, I_{D} = -2A$		5.3		ns
Turn-off delay time	t _{d(off)}	- VDD10V, VGS4.5V, 1D2A		32		
Fall time	t _f			8		
Gate resistance	R_g	V _{GS} =0V,V _{DS} =0V,f=1MHz		20		Ω
Gate charge characteristics						
Gate to source charge	Q_{gs}			1.5		
Gate to drain charge	Q_{gd}	V _{DS} =-10 V, I _D =-2A,		1.2		nC
Gate charge total	Q_g	- V _{GS} =-4.5 V		8.2		
Drain-Source diode characteristic	s and Maxi	mum Ratings		•	•	
Continuous Source Current	Is				-2	Α
Pulsed Source Current ²⁾	I _{SM}	1			-10	Α
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-2A, T _J =25℃			-1.2	V
	1	1		1		

Notes:

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2: Pulse Test: Pulse Width $\leq 300 \,\mu\,\text{s}$, Duty Cycle $\leq 2\%$.

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Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

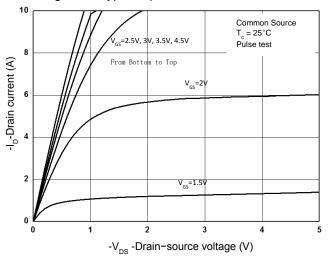


Figure 2. Transfer Characteristics

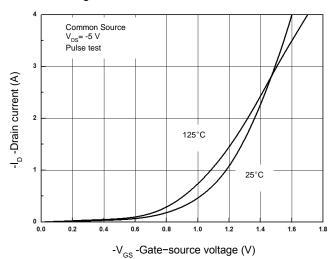


Figure 3. Capacitance Characteristics

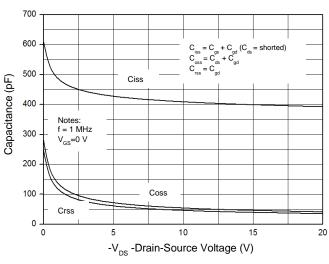


Figure 4. Gate Charge Waveform

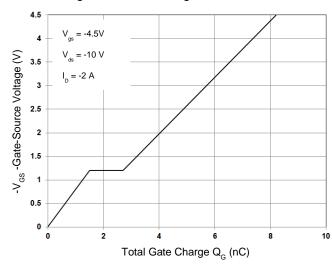


Figure 5. Body-Diode Characteristics

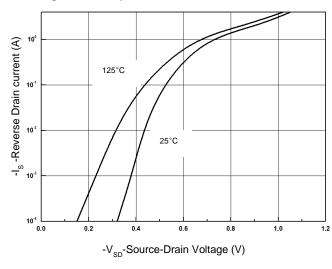


Figure 6. Rdson-Drain Current

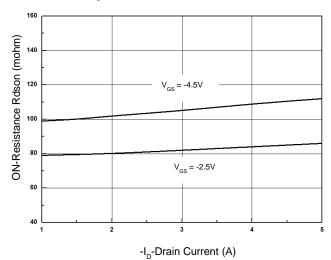




Figure 7. Rdson-Junction Temperature(°C)

Figure 8. Maximum Safe Operating Area

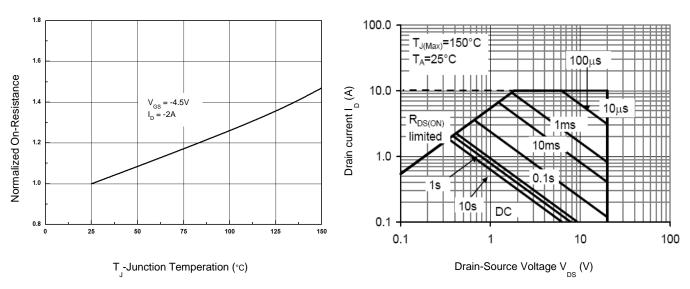
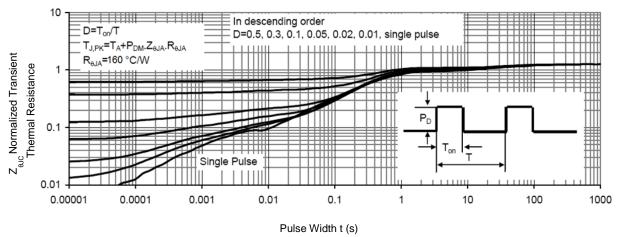


Figure 6. Normalized Maximum Transient Thermal Impedance (RthJA)

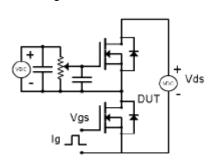


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Test Circuit & Waveform

Figure 8. Gate Charge Test Circuit & Waveform



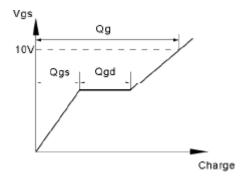
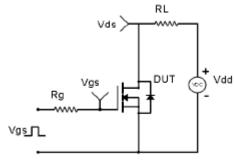


Figure 9. Resistive Switching Test Circuit & Waveforms



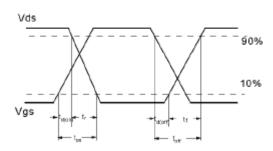
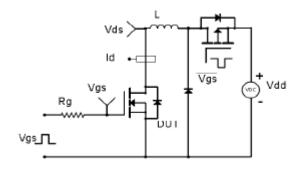


Figure 10. Unclamped Inductive Switching (UIS) Test Circuit & Waveform



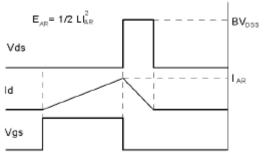
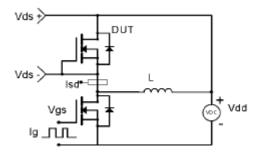
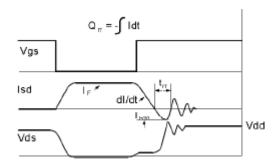


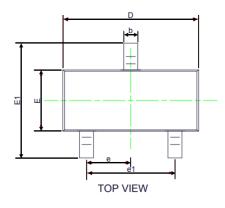
Figure 11. Diode Recovery Circuit & Waveform

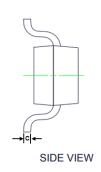




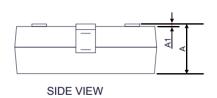


Mechanical Dimensions for SOT-23

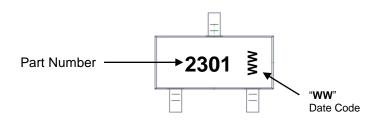




COMMON DIMENSIONS					
SYMBOL	MILLIMETERS		INCHS		
	MIN	MAX	MIN	MAX	
А	0.95	1.40	0.037	0.055	
A1	0.01	0.10	0.000	0.004	
b	0.35	0.50	0.014	0.020	
С	0.08	0.19	0.003	0.007	
D	2.70	3.10	0.106	0.122	
E	1.20	1.65	0.047	0.065	
E1	2.20	3.00	0.087	0.118	
е	0.95 TYP.		0.037 TYP.		
e1	1.78	2.04	0.070	0.080	



SOT-23 Part Marking Information





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