

# Lonten N-channel 600V, 100A, 0.030Ω LonFET™ Power MOSFET

#### **Description**

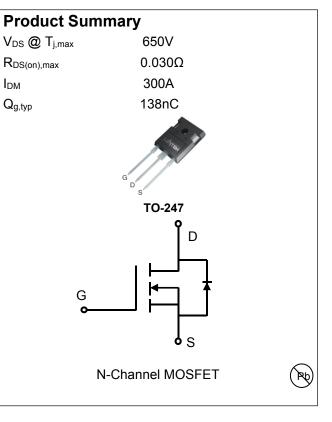
LonFET<sup>TM</sup> Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

#### **Features**

- ◆ Ultra low R<sub>DS(on)</sub>
- ◆ Ultra low gate charge (typ. Q<sub>g</sub> = 138nC)
- ◆ 100% UIS tested
- RoHS compliant

### **Applications**

- Power faction correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).



## **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	V
Continuous drain current ( T <sub>C</sub> = 25°C )	I <sub>D</sub>	100	А
( T <sub>C</sub> = 100°C )		65	Α
Pulsed drain current 1)	I <sub>DM</sub>	300	А
Gate-Source voltage	V <sub>GSS</sub>	±30	V
Avalanche energy, single pulse 2)	E <sub>AS</sub>	3200	mJ
Power Dissipation TO-247 ( T <sub>C</sub> = 25°C )	D	610	W
- Derate above 25°C	$P_{D}$	4.9	W/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C
Continuous diode forward current	Is	100	А
Diode pulse current	I <sub>S,pulse</sub>	300	Α

#### **Thermal Characteristics TO-247**

Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.2	°C/W	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	49	°C/W	
Soldering temperature, wavesoldering only allowed	т	260	°C	
at leads. (1.6mm from case for 10s)	I sold	260		



## **Package Marking and Ordering Information**

Device	Device Package	Marking	Units/Tube	Units/Reel
LSB60R030HT	TO-247	LSB60R030HT	30	

#### Electrical Characteristics T<sub>c</sub> = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics				,		
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =0.25 mA	600	-	-	V
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =0.25 mA	2.5	3.5	4.5	V
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> =600 V, V <sub>GS</sub> =0 V,				μΑ
		T <sub>j</sub> = 25°C	-	-	1	
		T <sub>j</sub> = 125°C	-	10	-	
Gate leakage current, Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =30 V, V <sub>DS</sub> =0 V	-	-	100	nA
Gate leakage current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-30 V, V <sub>DS</sub> =0 V	-	-	-100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =50 A	-			
		T <sub>j</sub> = 25°C	-	0.027	0.030	Ω
		T <sub>j</sub> = 150°C	-	0.07	-	
Dynamic characteristics	•					•
Input capacitance	Ciss	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V,	-	9030	-	
Output capacitance	Coss	f = 250 kHz	-	311	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	3.4	-	
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = 400 V, I <sub>D</sub> = 50 A	-	45	-	
Rise time	tr	$R_G = 10 \Omega, V_{GS} = 15 V$	-	107	-	ns
Turn-off delay time	t <sub>d(off)</sub>		-	304	-	
Fall time	t <sub>f</sub>		-	7.8	-	
Gate charge characteristics			'	•		
Gate to source charge	Q <sub>gs</sub>	V <sub>DD</sub> =400 V, I <sub>D</sub> =50 A,	-	41.4	-	
Gate to drain charge	Q <sub>gd</sub>	V <sub>GS</sub> =0 to 10 V	-	41.0	-	nC
Gate charge total	Qg	1	-	138	-	
Gate plateau voltage	V <sub>plateau</sub>	1	-	5.0	-	V
Reverse diode characteristics		•		,		
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0 V, I <sub>F</sub> =50 A	-	-	1.2	V
Reverse recovery time	t <sub>rr</sub>	V <sub>R</sub> =400 V, I <sub>F</sub> =30 A,	-	517	-	ns
Reverse recovery charge	Qrr	dI <sub>F</sub> /dt=100 A/µs	-	12	-	μC
Peak reverse recovery current	Irm		-	48	-	Α

#### Notes:

<sup>1.</sup> Limited by maximum junction temperature, maximum duty cycle is 0.75.

<sup>2.</sup>  $I_{AS}$  = 10A,  $V_{DD}$  =60V, Starting  $T_j$ = 25°C.



### **Electrical Characteristics Diagrams**

Figure 1. On-Region Characteristics

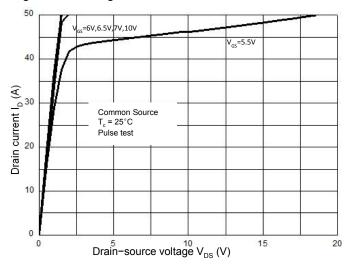


Figure 2. Transfer Characteristics

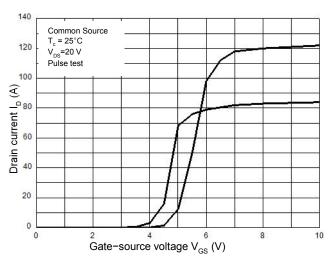


Figure 3. On-Resistance Variation vs. Drain Current

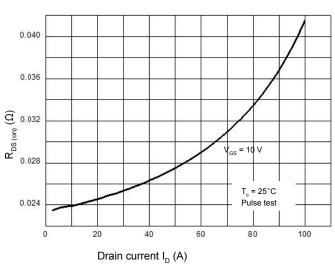


Figure 4. Threshold Voltage vs. Temperature

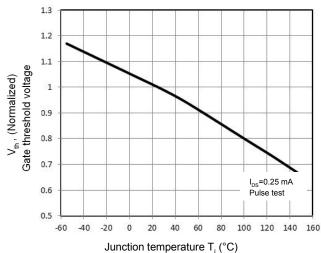


Figure 5. Breakdown Voltage vs. Temperature

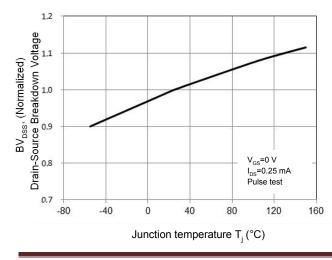


Figure 6. On-Resistance vs. Temperature

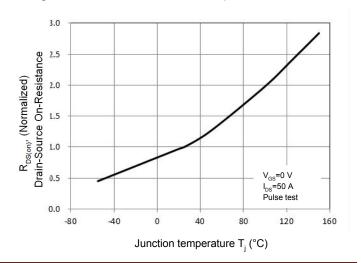




Figure 7. Capacitance Characteristics

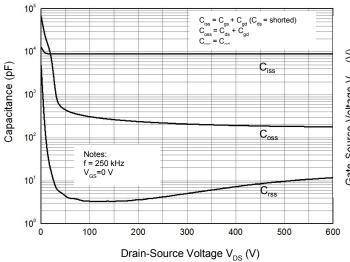


Figure 8. Gate Charge Characterist

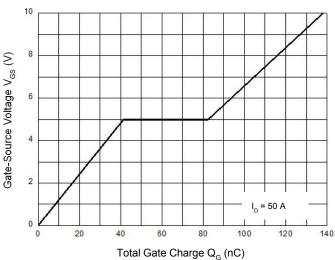


Figure 9 Maximum Safe Operating Area

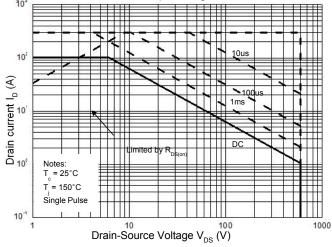
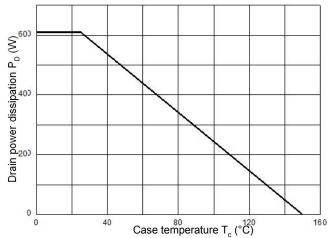
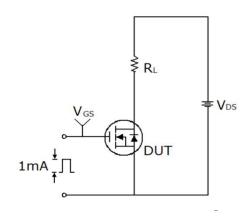


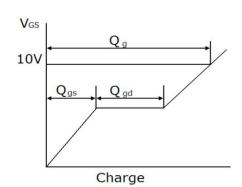
Figure 10 Power Dissipation vs. Temperature

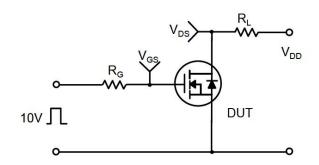


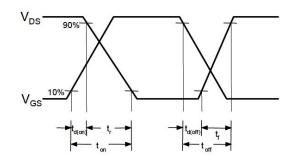


## **Gate Charge Test Circuit & Waveform**

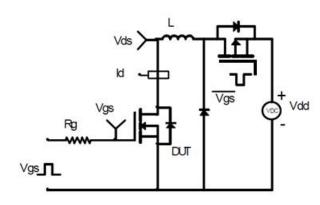


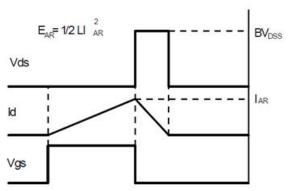






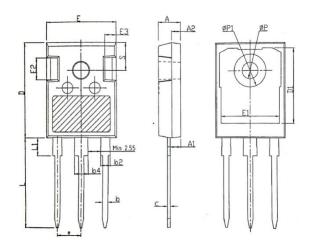
### **Unclamped Inductive Switching Test Circuit & Waveforms**





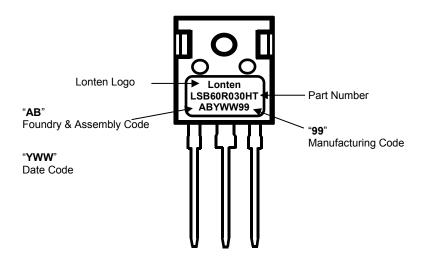


#### **Mechanical Dimensions for TO-247**



SYMBOL	mm			
BOL	MIN	NOM	MAX	
Α	4.80	5.00	5.20	
A1	2.21	2.41	2.59	
A2	1.85	2.00	2.15	
b	1.11	1.21	1.36	
b2	1.91	2.01	2.21	
b4	2.91	3.01	3.21	
С	0.51	0.61	0.75	
D	20.80	21.00	21.30	
D1	16.25	16.55	16.85	
Е	15.50	15.80	16.10	
E1	13.00	13.30	13.60	
E2	4.80	5.00	5.20	
E3	2.30 2.50		2.70	
е	5.44BSC			
L	19.82	19.92	20.22	
L1	_	_	4.30	
ØP	3.40	3.60	3.80	
ØP1	_	_	7.30	
S	6.15BSC			

## **TO-247 Part Marking Information**





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