

**₽**¢

# Lonten N-channel 600V, 54A, 0.066Ω LonFET<sup>™</sup> Power MOSFET

#### Description **Product Summary** LonFET<sup>™</sup> Power MOSFET is fabricated using V<sub>DS</sub> @ T<sub>i.max</sub> 650V advanced super junction technology. The resulting 0.066Ω R<sub>DS(on).max</sub> device has extremely low on resistance, making it 135A I<sub>DM</sub> especially suitable for applications which require 87nC Q<sub>g,typ</sub> superior power density and outstanding efficiency. **Features** TO-247 Ultra low R<sub>DS(on)</sub> ٠ Ultra low gate charge (typ. $Q_q = 87nC$ ) ٠ D 100% UIS tested ٠ **RoHS** compliant G

N-Channel MOSFET

## **Applications**

- Power faction correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).

# Absolute Maximum Ratings Parameter

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	V
Continuous drain current ( $T_c = 25^{\circ}C$ )	I <sub>D</sub>	54	А
( T <sub>C</sub> = 100°C )		30	А
Pulsed drain current <sup>1)</sup>	I <sub>DM</sub>	135	А
Gate-Source voltage	V <sub>GSS</sub>	±30	V
Avalanche energy, single pulse 2)	E <sub>AS</sub>	1200	mJ
Power Dissipation TO-247 ( $T_c = 25^{\circ}C$ )	P	290	W
- Derate above 25°C	P <sub>D</sub>	2.32	W/°C
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C
Continuous diode forward current	Is	54	A
Diode pulse current	I <sub>S,pulse</sub>	135	А

## Thermal Characteristics TO-247

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R <sub>θJC</sub>	0.43	°C/W
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	60	°C/W
Soldering temperature, wavesoldering only allowed	т	260	°C
at leads. (1.6mm from case for 10s)	I sold	200	U



## Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LSB60R066GF	TO-247	LSB60R066GF	30	

## **Electrical Characteristics** T<sub>c</sub> = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Static characteristics			·			
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =0.25 mA	600	-	-	V
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =0.25mA	2	3	4	V
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> =600 V, V <sub>GS</sub> =0 V,				μA
		$T_j = 25^{\circ}C$	-	-	1	
		T <sub>j</sub> = 125°C	-	10	-	
Gate leakage current, Forward	I <sub>GSSF</sub>	$V_{GS}$ =30 V, $V_{DS}$ =0 V	-	-	50	nA
Gate leakage current, Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-30 V, V <sub>DS</sub> =0 V	-	-	-50	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =27 A	-			
		T <sub>j</sub> = 25°C	-	0.060	0.0660	Ω
		$T_j = 150^{\circ}C$	-	0.13	-	
Dynamic characteristics		·				
Input capacitance	C <sub>iss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	-	4677	-	
Output capacitance	C <sub>oss</sub>	f = 1 MHz	-	2556	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	30	-	
Turn-on delay time	t <sub>d(on)</sub>	$V_{DD} = 400V, I_D = 27A$	-	29.0	-	
Rise time	tr	$R_G = 10\Omega, V_{GS}=10V$	-	12.8	-	ns
Turn-off delay time	t <sub>d(off)</sub>		-	191.6	-	
Fall time	t <sub>f</sub>		-	13.6	-	
Gate charge characteristics						
Gate to source charge	Q <sub>gs</sub>	V <sub>DD</sub> =480 V, I <sub>D</sub> =27A,	-	24	-	
Gate to drain charge	Q <sub>gd</sub>	V <sub>GS</sub> =0 to 10 V	-	31.24	-	nC
Gate charge total	Qg		-	87	-	
Gate plateau voltage	V <sub>plateau</sub>		-	5.5	-	V
Reverse diode characteristics	•	•				
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0 V, I <sub>F</sub> =27A	-	1.0	-	V
Reverse recovery time	t <sub>rr</sub>	V <sub>R</sub> =50 V, I <sub>F</sub> =47A,	-	234	-	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>F</sub> /dt=100 A/µs	-	1.65	-	μC
Peak reverse recovery current	Irrm	1	-	12.9	-	А

Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75.

2.  $I_{AS} = 8A$ ,  $V_{DD} = 60V$ , Starting  $T_j = 25^{\circ}C$ .



## **Electrical Characteristics Diagrams**

Figure 1. On-Region Characteristics

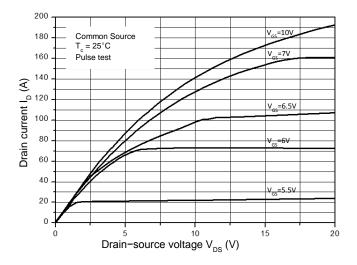


Figure 3. On-Resistance Variation vs. Drain Current

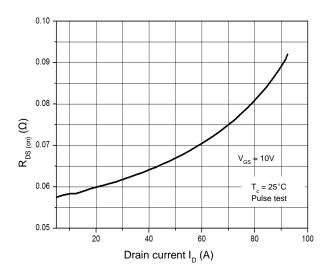


Figure 5. Breakdown Voltage vs. Temperature

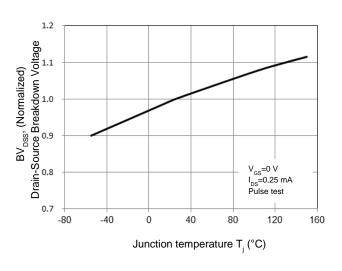


Figure 2. Transfer Characteristics

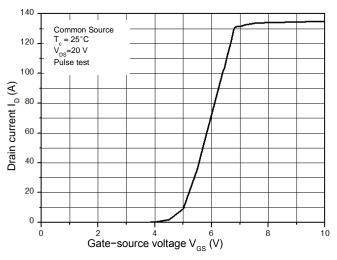
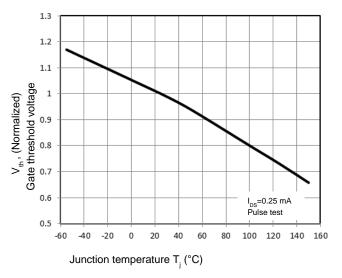
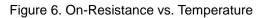
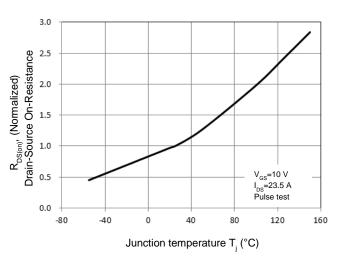


Figure 4. Threshold Voltage vs. Temperature







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#### Figure 7. Capacitance Characteristics

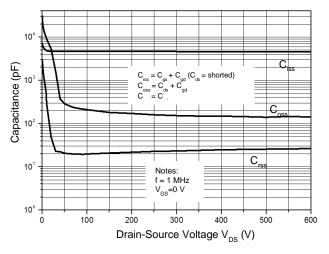


Figure 9. Maximum Safe Operating Area

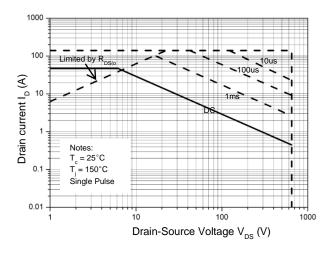


Figure 8. Gate Charge Characterist

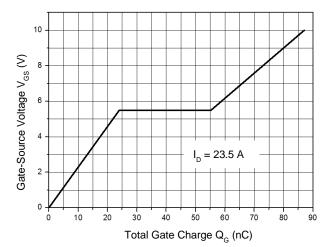
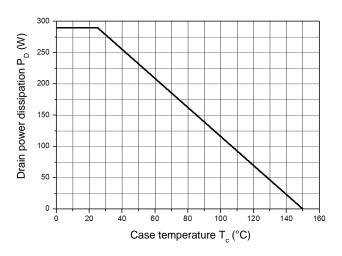
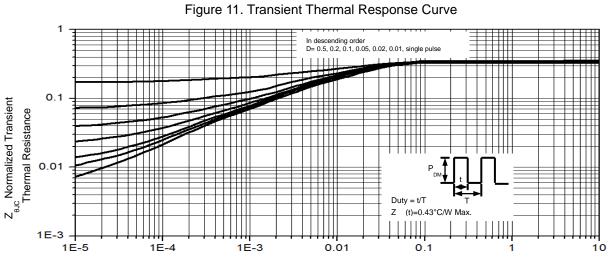


Figure 10. Power Dissipation vs. Temperature

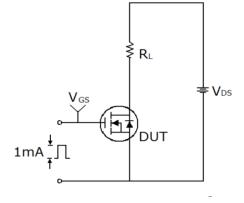


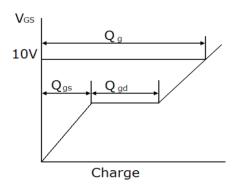


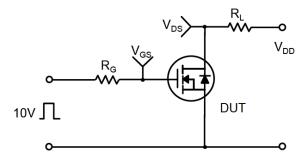
Pulse Width t (s)

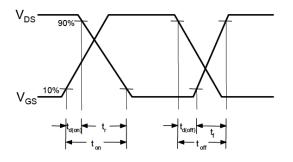


## Gate Charge Test Circuit & Waveform

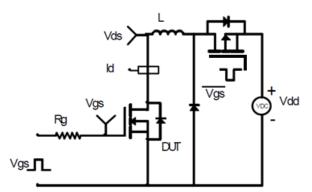


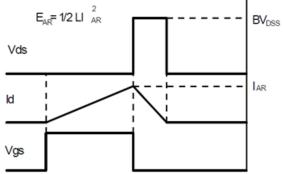






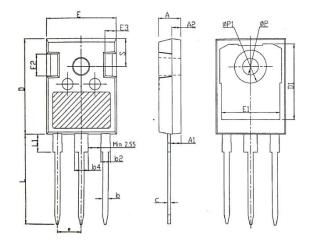
**Unclamped Inductive Switching Test Circuit & Waveforms** 





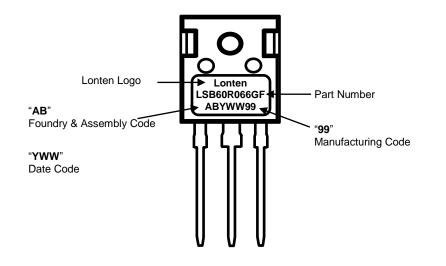


#### **Mechanical Dimensions for TO-247**



SYMBOL		mm	
STIVIBOL	MIN	NOM	MAX
А	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
с	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30 2.50		2.70
e	5.44BSC		
L	19.82	19.92	20.22
L1	_	_	4.30
ØP	3.40	3.60	3.80
ØP1	_	_	7.30
S		6.15BSC	

### **TO-247 Part Marking Information**





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