## Lonten N-channel 600V, 54A, $0.066 \Omega$ LonFET ${ }^{T M}$ Power MOSFET

## Description

LonFET ${ }^{\text {TM }}$ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

## Features

- Ultra low $\mathrm{R}_{\mathrm{DS}(o n)}$
- Ultra low gate charge (typ. $Q_{g}=87 n C$ )
- $100 \%$ UIS tested
- RoHS compliant


## Applications

- Power faction correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).

Product Summary

| $\mathrm{V}_{\mathrm{DS}} @ \mathrm{~T}_{\mathrm{j} \text {, max }}$ | 650 V |
| :--- | :--- |
| $\mathrm{R}_{\mathrm{DS}(\text { on }) \text { max }}$ | $0.066 \Omega$ |
| $\mathrm{I}_{\mathrm{DM}}$ | 135 A |
| $\mathrm{Q}_{\mathrm{g}, \mathrm{typ}}$ | 87 nC |
|  |  |

TO-247


N-Channel MOSFET

## Absolute Maximum Ratings

| Parameter | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Drain-Source Voltage | $\mathrm{V}_{\text {DSS }}$ | 600 | V |
| $\begin{array}{ll} & \left(\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right) \\ & \left(\mathrm{T}_{\mathrm{C}}=100^{\circ} \mathrm{C}\right)\end{array}$ | $\mathrm{I}_{\mathrm{D}}$ | $\begin{aligned} & 54 \\ & 30 \end{aligned}$ | A <br> A |
| Pulsed drain current ${ }^{1)}$ | $I_{\text {DM }}$ | 135 | A |
| Gate-Source voltage | $V_{\text {GSS }}$ | $\pm 30$ | V |
| Avalanche energy, single pulse ${ }^{2)}$ | $\mathrm{E}_{\text {AS }}$ | 1200 | mJ |
| Power Dissipation TO-247 ( $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ ) <br> - Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{D}}$ | $\begin{aligned} & \hline 290 \\ & 2.32 \end{aligned}$ | W <br> $\mathrm{W} /{ }^{\circ} \mathrm{C}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J},} \mathrm{T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Continuous diode forward current | Is | 54 | A |
| Diode pulse current | $\mathrm{I}_{\mathrm{S} \text { pulse }}$ | 135 | A |

Thermal Characteristics TO-247

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :---: | :---: |
| Thermal Resistance, Junction-to-Case | $\mathrm{R}_{\text {өJc }}$ | 0.43 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance, Junction-to-Ambient | $\mathrm{R}_{\text {өJA }}$ | 60 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Soldering temperature, wavesoldering only allowed <br> at leads. (1.6mm from case for 10s) | $\mathrm{T}_{\text {sold }}$ | 260 | ${ }^{\circ} \mathrm{C}$ |

## Package Marking and Ordering Information

| Device | Device Package | Marking | Units/Tube | Units/Real |
| :---: | :---: | :---: | :---: | :---: |
| LSB60R066GF | TO-247 | LSB60R066GF | 30 |  |

Electrical Characteristics $\mathrm{T}_{\mathrm{c}}=25^{\circ} \mathrm{C}$ unless otherwise noted

| Parameter | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Static characteristics |  |  |  |  |  |  |
| Drain-source breakdown voltage | $\mathrm{BV}_{\text {DSs }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=0.25 \mathrm{~mA}$ | 600 | - | - | V |
| Gate threshold voltage | $V_{G S(t h)}$ | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{Gs}}, \mathrm{I}_{\mathrm{D}}=0.25 \mathrm{~mA}$ | 2 | 3 | 4 | V |
| Drain cut-off current | $\mathrm{l}_{\text {DSS }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DS}}=600 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \\ & \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{j}}=125^{\circ} \mathrm{C} \end{aligned}$ |  | $10$ | $1$ | $\mu \mathrm{A}$ |
| Gate leakage current, Forward | IGSSF | $\mathrm{V}_{\mathrm{GS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | - | - | 50 | nA |
| Gate leakage current, Reverse | $\mathrm{I}_{\text {GSSR }}$ | $\mathrm{V}_{\mathrm{GS}}=-30 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | - | - | -50 | nA |
| Drain-source on-state resistance | $\mathrm{R}_{\text {DS(on) }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=27 \mathrm{~A} \\ & \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \\ & \mathrm{~T}_{\mathrm{j}}=150^{\circ} \mathrm{C} \end{aligned}$ |  | $\begin{gathered} 0.060 \\ 0.13 \end{gathered}$ | $0.0660$ | $\Omega$ |
| Dynamic characteristics |  |  |  |  |  |  |
| Input capacitance | $\mathrm{C}_{\text {iss }}$ | $\begin{aligned} & V_{D S}=25 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \\ & \mathrm{f}=1 \mathrm{MHz} \end{aligned}$ | - | 4677 | - | pF |
| Output capacitance | Coss |  | - | 2556 | - |  |
| Reverse transfer capacitance | $\mathrm{C}_{\text {rss }}$ |  | - | 30 | - |  |
| Turn-on delay time | $\mathrm{t}_{\mathrm{d}(0 n)}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=400 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=27 \mathrm{~A} \\ & \mathrm{R}_{\mathrm{G}}=10 \Omega, \mathrm{~V}_{\mathrm{G}}=10 \mathrm{~V} \end{aligned}$ | - | 29.0 | - | ns |
| Rise time | $\mathrm{tr}_{\mathrm{r}}$ |  | - | 12.8 | - |  |
| Turn-off delay time | $\mathrm{t}_{\text {doffif }}$ |  | - | 191.6 | - |  |
| Fall time | $\mathrm{t}_{\text {f }}$ |  | - | 13.6 | - |  |
| Gate charge characteristics |  |  |  |  |  |  |
| Gate to source charge | $\mathrm{Q}_{\mathrm{gs}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=480 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=27 \mathrm{~A}, \\ & \mathrm{~V}_{\mathrm{GS}}=0 \text { to } 10 \mathrm{~V} \end{aligned}$ | - | 24 | - | nC |
| Gate to drain charge | $\mathrm{Q}_{\mathrm{gd}}$ |  | - | 31.24 | - |  |
| Gate charge total | $\mathrm{Q}_{\mathrm{g}}$ |  | - | 87 | - |  |
| Gate plateau voltage | $V_{\text {plateau }}$ |  | - | 5.5 | - | V |

## Reverse diode characteristics

| Diode forward voltage | $\mathrm{V}_{\mathrm{SD}}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=27 \mathrm{~A}$ | - | 1.0 | - | V |
| :--- | :--- | :--- | :--- | :--- | :---: | :---: |
| Reverse recovery time | $\mathrm{t}_{\mathrm{rr}}$ | $\mathrm{V}_{\mathrm{R}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=47 \mathrm{~A}$, | - | 234 | - | ns |
| Reverse recovery charge | $\mathrm{Q}_{\mathrm{rr}}$ | $\mathrm{dl} / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{s}$ | - | 1.65 | - | $\mu \mathrm{C}$ |
| Peak reverse recovery current | $\mathrm{I}_{\mathrm{rrm}}$ |  | - | 12.9 | - | A |

## Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75 .
2. $\mathrm{I}_{\mathrm{AS}}=8 \mathrm{~A}, \mathrm{~V}_{\mathrm{DD}}=60 \mathrm{~V}$, Starting $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$

## Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics


Figure 3. On-Resistance Variation vs. Drain Current


Figure 5. Breakdown Voltage vs. Temperature


Figure 2. Transfer Characteristics


Figure 4. Threshold Voltage vs. Temperature


Figure 6. On-Resistance vs. Temperature


Figure 7. Capacitance Characteristics


Figure 9. Maximum Safe Operating Area


Figure 8. Gate Charge Characterist


Figure 10. Power Dissipation vs. Temperature


Figure 11. Transient Thermal Response Curve


Gate Charge Test Circuit \& Waveform


Unclamped Inductive Switching Test Circuit \& Waveforms


Mechanical Dimensions for TO-247


| SYMBOL | mm |  |  |
| :---: | :---: | :---: | :---: |
|  | MIN | NOM | MAX |
| A | 4.80 | 5.00 | 5.20 |
| A1 | 2.21 | 2.41 | 2.59 |
| A2 | 1.85 | 2.00 | 2.15 |
| b | 1.11 | 1.21 | 1.36 |
| b2 | 1.91 | 2.01 | 2.21 |
| b4 | 2.91 | 3.01 | 3.21 |
| c | 0.51 | 0.61 | 0.75 |
| D | 20.80 | 21.00 | 21.30 |
| D1 | 16.25 | 16.55 | 16.85 |
| E | 15.50 | 15.80 | 16.10 |
| E1 | 13.00 | 13.30 | 13.60 |
| E2 | 4.80 | 5.00 | 5.20 |
| E3 | 2.30 | 2.50 | 2.70 |
| e | 5.44 BSC |  |  |
| L | 19.82 | 19.92 | 20.22 |
| L1 | - | - | 4.30 |
| ØP | 3.40 | 3.60 | 3.80 |
| $\varnothing$ P1 | - | - | 7.30 |
| S | 6.15 BSC |  |  |

## TO-247 Part Marking Information



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