

Lonten N-channel 650V, 40A, 0.105Ω LonFET™ Power MOSFET

Description

LonFET[™] Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- ◆ Ultra low R_{DS(on)}
- ◆ Ultra low gate charge (typ. Q_g = 49.8nC)
- ◆ 100% UIS tested
- RoHS compliant

Applications

- Power factor correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).

Product Summary V_{DS} @ T_{j,max} 700V R_{DS(on),max} 0.105Ω I_{DM} 120A Q_{g,typ} 49.8nC TO-220F

N-Channel MOSFET

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	650	V
Continuous drain current (T _C = 25°C)	ID	40	A
(T _C = 100°C)		26	A
Pulsed drain current 1)	I _{DM}	120	A
Gate-Source voltage	V _{GSS}	±30	V
Avalanche energy, single pulse 2)	E _{AS}	1000	mJ
Power Dissipation (T _C = 25°C)	P _D	34.8	W
- Derate above 25°C		0.28	W/°C
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C
Continuous diode forward current	Is	40	A
Diode pulse current	I _{S,pulse}	120	A

Thermal Characteristics

Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.6	°C/W	
Thermal Resistance, Junction-to-Ambient	R ₀ JA	62.5	°C/W	
Soldering temperature, wavesoldering only allowed	т	260	°C	
at leads. (1.6mm from case for 10s)	I sold	260		



Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	
LSD65R105HF	TO-220F	LSD65R105HF	50	

Electrical Characteristics T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit	
Static characteristics							
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V	
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25 mA	3.0	3.5	4.5	V	
Drain cut-off current	I _{DSS}	V _{DS} =650V, V _{GS} =0 V,T _j = 25°C	-	-	5	μA	
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA	
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V		-	-100	nA	
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =20 A	-				
		T _j = 25°C	-	0.096	0.105	Ω	
		T _j = 150°C	-	0.25	-		
Gate resistance	R _G	f=1 MHz, open drain	-	2.0	-	Ω	
Dynamic characteristics							
Input capacitance	Ciss	V _{DS} = 100 V, V _{GS} = 0 V,	-	2870	-		
Output capacitance	Coss	f = 250 kHz -		104	-	pF	
Reverse transfer capacitance	Crss		-	2.2	-		
Turn-on delay time	t _{d(on)}	V _{DD} = 400 V, I _D = 20 A	-	29.8	-	ns	
Rise time	tr	R _G = 10 Ω, V _{GS} =10 V	-	59.2	-		
Turn-off delay time	t _{d(off)}		-	63.5	-		
Fall time	t _f		-	4.4	-		
Gate charge characteristics	<u>'</u>			1	'		
Gate to source charge	Q _{gs}	V _{DD} =520 V, I _D =20 A,	-	16.3	-		
Gate to drain charge	Q _{gd}	V _{GS} =0 to 10 V	-	11.1	-	nC	
Gate charge total	Qg		-	49.8	-		
Gate plateau voltage	V _{plateau}		-	5.0	-	V	
Reverse diode characteristics	•		•	•	·		
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =20 A	-	-	1.2	V	
Reverse recovery time	t _{rr}	V _R =400 V, I _F =20 A,	-	184.0	-	ns	
Reverse recovery charge	Qrr	dl _F /dt=100 A/µs	-	1.5	-	μC	
Peak reverse recovery current	I _{rrm}		-	16.7	-	А	

Notes

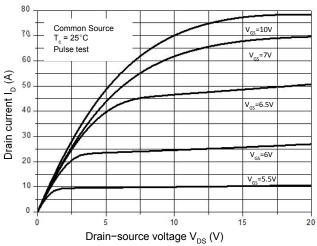
^{1.} Limited by maximum junction temperature, maximum duty cycle is 0.75.

^{2.} I_{AS} = 8A, V_{DD} =50V, RG=30 Ω , Starting T_{j} = 25 $^{\circ}C.$



Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics



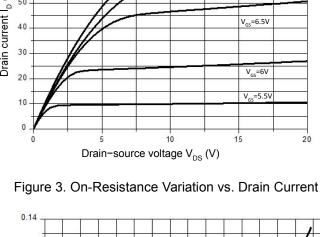


Figure 2. Transfer Characteristics

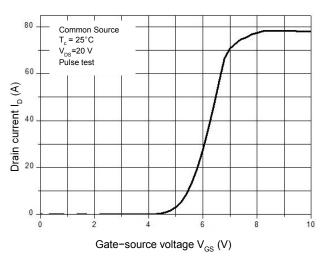


Figure 4. Threshold Voltage vs. Temperature

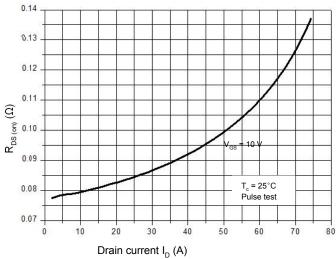
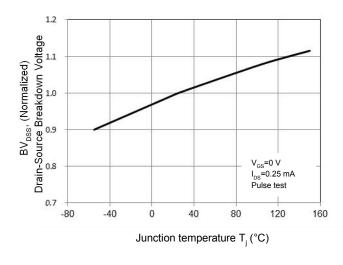


Figure 5. Breakdown Voltage vs. Temperature



1.3 1.2 1.1 Gate threshold voltage V_{th} , (Normalized) 1 0.9 0.8 0.7 =0.25 mA Pulse test 0.5 -40 100 120 140 160 Junction temperature T_i (°C)

Figure 6. On-Resistance vs. Temperature

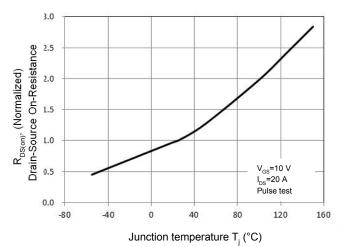




Figure 7. Capacitance Characteristics

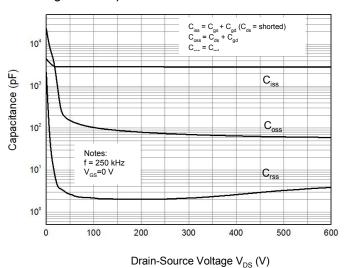


Figure 9. Power Dissipation vs. Temperature

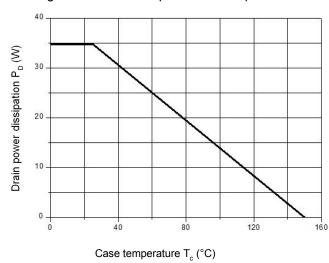


Figure 8. Gate Charge Characteristics

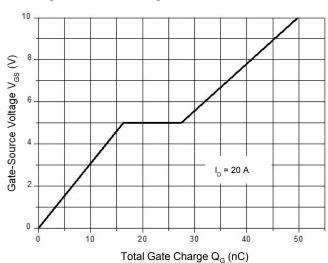


Figure 10.Drain Current Derating

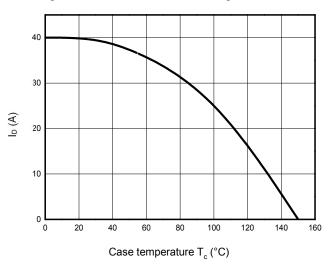
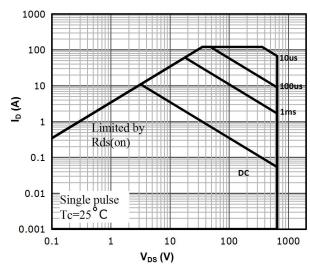


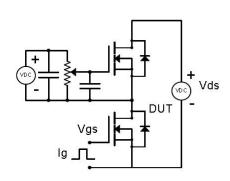
Figure 11: Safe Operating Area

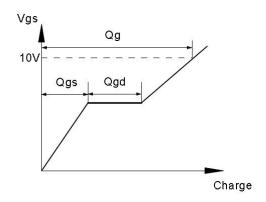




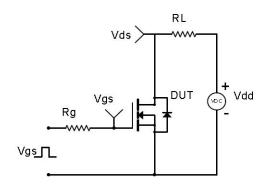
Test Circuit & Waveforms

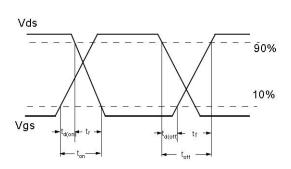
Gate Charge Test Circuit & Waveform



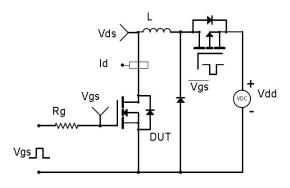


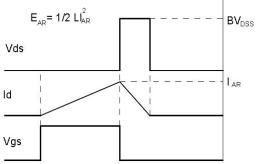
Switching Test Circuit & Waveform





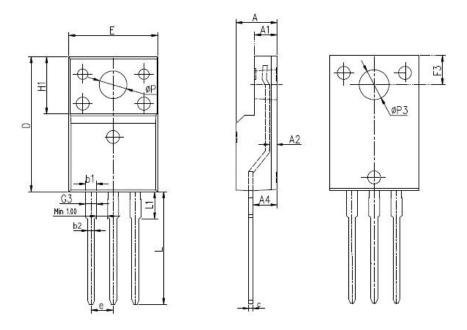
Unclamped Inductive Switching Test Circuit & Waveform







Mechanical Dimensions for TO-220F



DIMENSIONS IN MILLITMETERS DIMENSIONS IN INCHES					
SYMBOL	MIN	MAX	MIN	MAX	
A	4. 4	4. 9	0. 173	0. 193	
A1	2. 34	2. 74	0. 092	0. 108	
A2	0.3	0. 7	0. 012	0. 028	
A4	2. 5	2. 96	0. 098	0. 117	
С	0. 4	0. 7	0. 016	0. 028	
D	15. 57	16. 4	0. 613	0. 646	
Е	9. 96	10. 4	0. 392	0. 409	
H1	6. 48	6. 95	0. 255	0. 274	
е	2. 54H	BSC	0. 1BSC		
L	12.68	14. 2	0. 499	0. 559	
L1	2.88	3. 6	0. 113	0. 142	
ФР	3	3. 38	0. 118	0. 133	
ФР3	3. 15	3. 65	0. 124	0. 144	
F3	3. 15	3. 45	0. 124	0. 136	
G3	1. 15	1. 58	0. 045	0.062	
b1	1. 18	1. 43	0.046	0. 056	
b2	0. 7	1	0. 028	0.039	



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