

Lonten N-channel 800V, 2A, 2.8Ω LonFET[™] Power MOSFET

Description

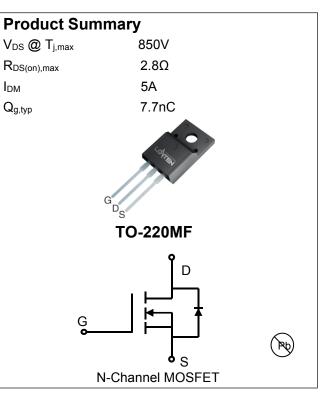
LonFETTM Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- ♦ Ultra low R_{DS(on)}
- ◆ Ultra low gate charge (typ. Qg = 7.7nC)
- ◆ 100% UIS tested
- RoHS compliant

Applications

- Power factor correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	800	V
Continuous drain current (Tc = 25°C)	ID	2	A
(T _C = 100°C)		1.2	A
Pulsed drain current 1)	I _{DM}	5	Α
Gate-Source voltage	V _{GSS}	±30	V
Avalanche energy, single pulse 2)	E _{AS}	25	mJ
Avalanche energy, repetitive 3)	Ear	0.07	mJ
Avalanche current, repetitive 3)	I _{AR}	2	Α
Power Dissipation (T _C = 25°C)	P _D	34.2	W
- Derate above 25°C		0.27	W/°C
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C
Continuous diode forward current	Is	2	Α
Diode pulse current	I _{S,pulse}	5	Α

Thermal Characteristics

Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	3.7	°C/W	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	75	°C/W	
Soldering temperature, wavesoldering only allowed	· T	260	°C	
at leads. (1.6mm from case for 10s)	I sold	200		



Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LSD80R2K8GT	TO-220MF	LSD80R2K8GT	50	

Electrical Characteristics T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition Min.		Тур.	Max.	Unit
Static characteristics						•
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	800	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25mA 2.5 3.5 4.5		4.5	V	
Drain cut-off current	I _{DSS}	V _{DS} =800 V, V _{GS} =0 V,				μA
		T _j = 25°C	-	-	1	
		T _j = 125°C	-	10	-	
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =1 A	-			
		T _j = 25°C	-	2.2	2.8	Ω
		T _j = 150°C	-	5.7		
Gate resistance	R _G	f=1 MHz, open drain	-	11	-	Ω
Dynamic characteristics						•
Input capacitance	C _{iss}	V _{DS} = 100 V, V _{GS} = 0 V,	-	238	-	
Output capacitance	Coss	f = 250 kHz	-	8.78	-	pF
Reverse transfer capacitance	C _{rss}		-	1.47	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 400V, I _D = 1A	-	19.8	-	
Rise time	t _r	$R_G = 10\Omega$, $V_{GS}=10V$	-	33.7	-	ns
Turn-off delay time	t _{d(off)}	1	-	43.8	-	
Fall time	t _f	1	-	59.8	-	
Gate charge characteristics	I					I
Gate to source charge	Q _{gs}	V _{DD} =400 V, I _D =1A,	-	1.2	-	
Gate to drain charge	Q _{gd}	V _{GS} =0 to 10 V	-	4.3	-	nC
Gate charge total	Qg		-	7.7	-	
Gate plateau voltage	V _{plateau}	1	-	5.2	-	V
Reverse diode characteristics	·	1		1	ı	1
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =1A	-	1.0	-	V
Reverse recovery time	t _{rr}	V _R =50 V, I _F =2A,	-	195	-	ns
Reverse recovery charge	Qrr	dI _F /dt=100 A/µs	-	0.975	-	μC
Peak reverse recovery current	Irrm	1	-	10	-	Α

Notes:

- 1. Limited by maximum junction temperature, maximum duty cycle is 0.75.
- 2. I_{AS} = 1A, V_{DD} = 60V, Starting T_j = 25°C.
- 3. Repetitive Rating: Pulse width limited by maximum junction temperature.



Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

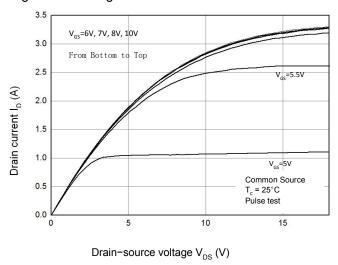


Figure 3. On-Resistance Variation vs. Drain Current

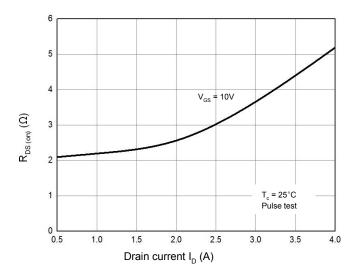


Figure 5. Breakdown Voltage vs. Temperature

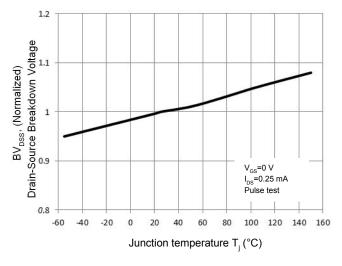


Figure 2. Transfer Characteristics

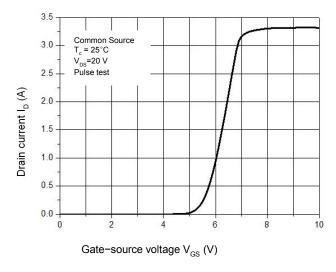


Figure 4. Threshold Voltage vs. Temperature

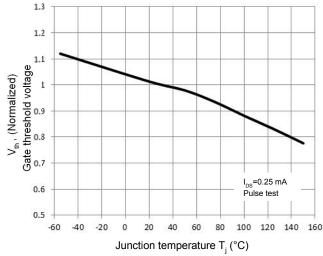


Figure 6. On-Resistance vs. Temperature

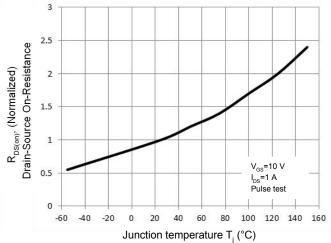




Figure 7. Capacitance Characteristics

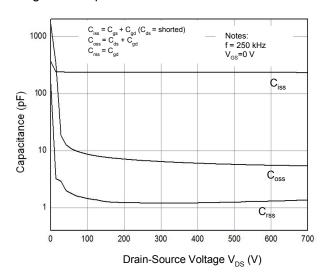


Figure 8. Gate Charge Characterist

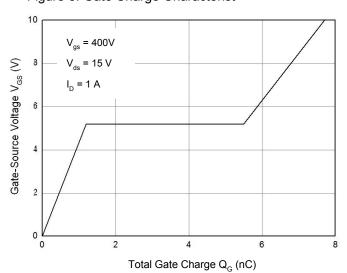


Figure 9. Maximum Safe Operating Area

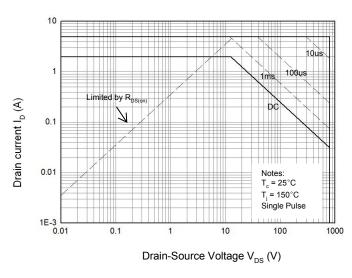


Figure 10. Power Dissipation vs. Temperature

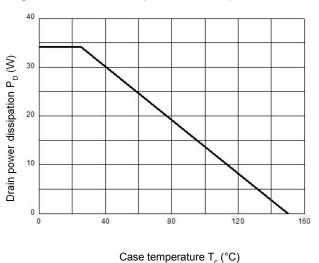
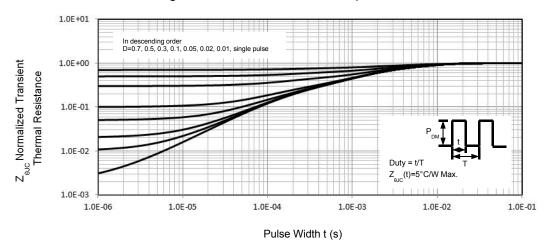


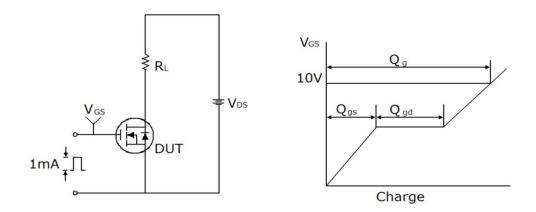
Figure 11. Transient Thermal Response Curve



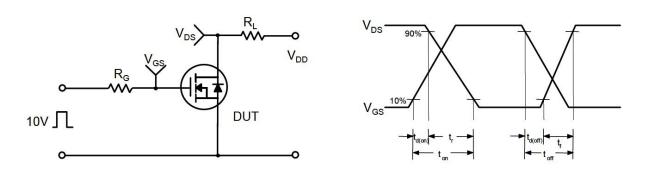
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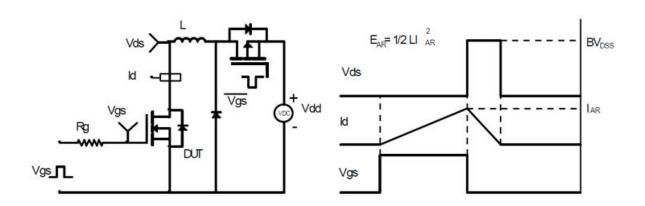
Gate Charge Test Circuit & Waveform



Switching Test Circuit & Waveforms

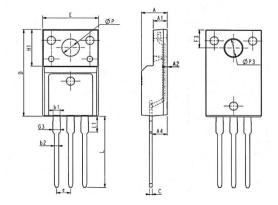


Unclamped Inductive Switching Test Circuit & Waveforms



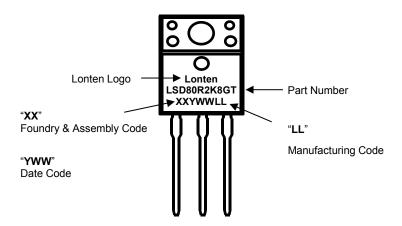


Mechanical Dimensions for TO-220MF



COMMON DIMENSIONS						
SYMBOL	MM			INCH		
OTMBOL	MIN	NOM	MAX	MIN	NOM	MAX
E	9.96	10.16	10.36	0.392	0.400	0.408
А	4.50	4.70	4.90	0.177	0.185	0.193
A1	2.34	2.54	2.74	0.092	0.100	0.108
A2	0.30	0.45	0.60	0.012	0.002	0.024
A4	2.65	2.76	2.96	0.104	0.109	0.117
С	0.40	0.50	0.65	0.016	0.020	0.026
D	15.57	15.87	16.17	0.613	0.625	0.637
H1	6.70REF		0.264REF			
е		2.54BSC		0.1BSC		
ØP	3.03	3.18	3.38	0.119	0.125	0.133
L	12.68	12.98	13.28	0.499	0.511	0.523
L1	2.88	3.03	3.18	0.113	0.119	0.125
ØP3	3.15REF		0.124REF			
F3	3.15	3.30	3.45	0.124	0.130	0.136
G3	1.25	1.35	1.55	0.049	0.053	0.061
b1	1.18	1.28	1.43	0.046	0.050	0.056
b2	0.70	0.80	0.95	0.028	0.031	0.037

TO-220MF Part Marking Information





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