# Lonten N-channel 650V, 11A, 0.38Ω LonFET™ Power MOSFET

Description

LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- Ultra low $R_{DS(on)}$
- Ultra low gate charge (typ. Q_g = 22.8nC)
- 100% UIS tested
- RoHS compliant

Applications

- Power factor correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).

Product Summary

V_{DS} @ $T_{j,max}$	700V
$R_{DS(on),max}$	0.38Ω
I_{DM}	30A
$Q_{g,typ}$	22.8nC
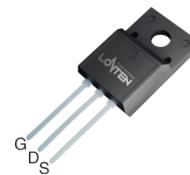
TO-220NF
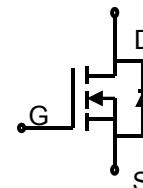
N-Channel MOSFET

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	650	V
Continuous drain current (T_C = 25°C)	I_D	11	A
(T_C = 100°C)		7	A
Pulsed drain current [1]	I_{DM}	30	A
Gate-Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse [2]	E_{AS}	270	mJ
Power Dissipation (T_C = 25°C)	P_D	33	W
- Derate above 25°C		0.26	W/°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Continuous diode forward current	I_S	11	A
Diode pulse current	$I_{S,pulse}$	30	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	3.8	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	80	°C/W
Soldering temperature, wavesoldering only allowed at leads. (1.6mm from case for 10s)	T_{sold}	260	°C

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube
LSDN65R380GT	TO-220NF	LSDN65R380GT	50

Electrical Characteristics T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0 V, I_D=0.25 mA	650	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=0.25mA	2.5	3.5	4.5	V
Drain cut-off current	I_{DSS}	V_{DS}=650 V, V_{GS}=0 V,T_j = 25°C	-	-	1	μA
Gate leakage current, Forward	I_{GSSF}	V_{GS}=30 V, V_{DS}=0 V	-	-	100	nA
Gate leakage current, Reverse	I_{GSSR}	V_{GS}=-30 V, V_{DS}=0 V	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D=5.5 A	-			
		T_j = 25°C	-	0.34	0.38	Ω
		T_j = 150°C	-	0.86	-	
Gate resistance	R_G	f=1 MHz, open drain	-	5.55	-	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V_{DS} = 100 V, V_{GS} = 0 V,	-	1068	-	
Output capacitance	C_{oss}	f = 250 kHz	-	39	-	pF
Reverse transfer capacitance	C_{rss}		-	1.8	-	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 400V, I_D = 5.5A	-	15	-	
Rise time	t_r	R_G = 4.7Ω, V_{GS}=10V	-	27	-	ns
Turn-off delay time	$t_{d(off)}$		-	69	-	
Fall time	t_f		-	11	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	V_{DD}=520V, I_D=5.5A,	-	6.2	-	
Gate to drain charge	Q_{gd}	V_{GS}=0 to 10 V	-	8.5	-	nC
Gate charge total	Q_g		-	22.8	-	
	$V_{plateau}$		-	5.5	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=5.5A	-	1.0	-	V
Reverse recovery time	t_{rr}	V_R=400V, I_F=11A,	-	345	-	ns
Reverse recovery charge	Q_{rr}	dI$_F$/dt=100 A/μs	-	3.8	-	μC
Peak reverse recovery current	I_{rrm}		-	22	-	A

Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75.

2. I_{AS} = 3A, V_{DD} = 60V, Starting T_j= 25°C.

Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics
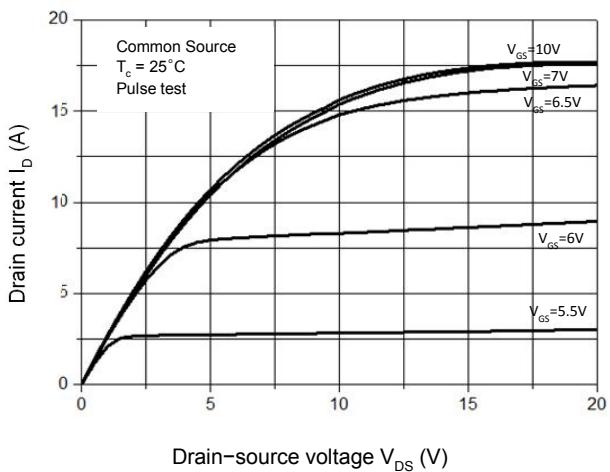
Figure 2. Transfer Characteristics
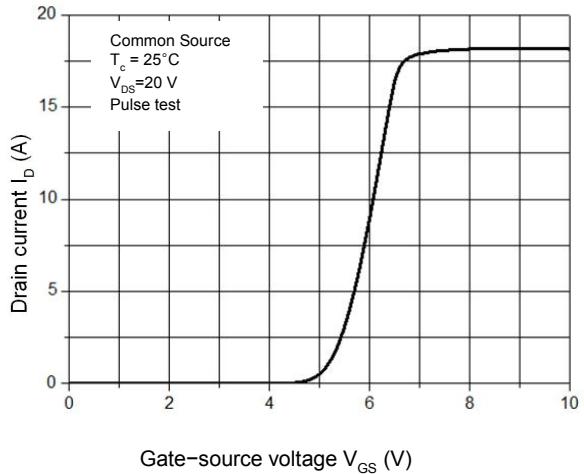
Figure 3. On-Resistance Variation vs. Drain Current
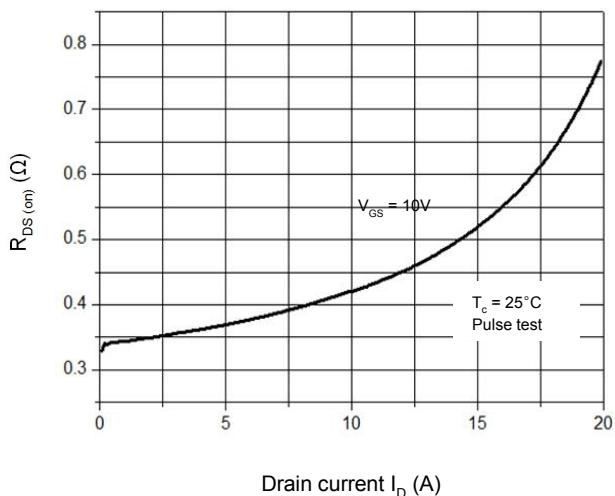

Figure 4. Threshold Voltage vs. Temperature
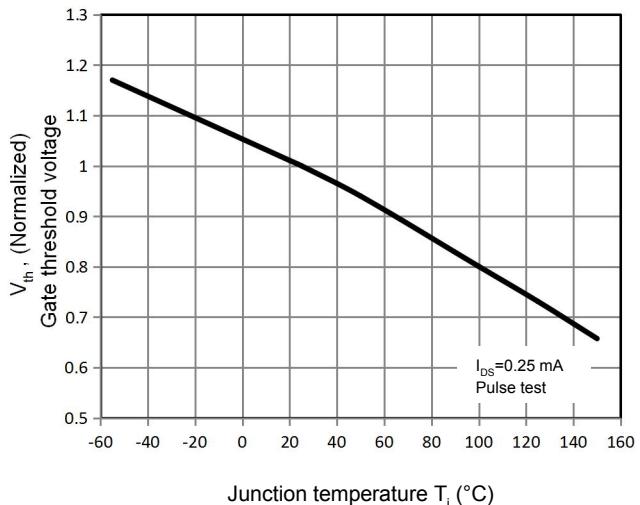

Figure 5. Breakdown Voltage vs. Temperature
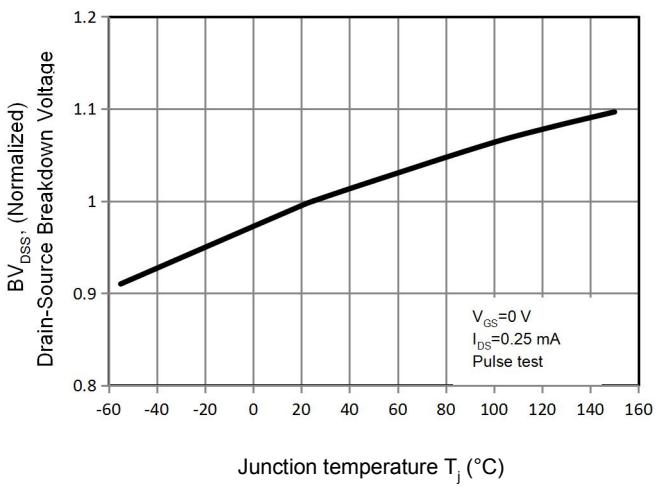
Figure 6. On-Resistance vs. Temperature
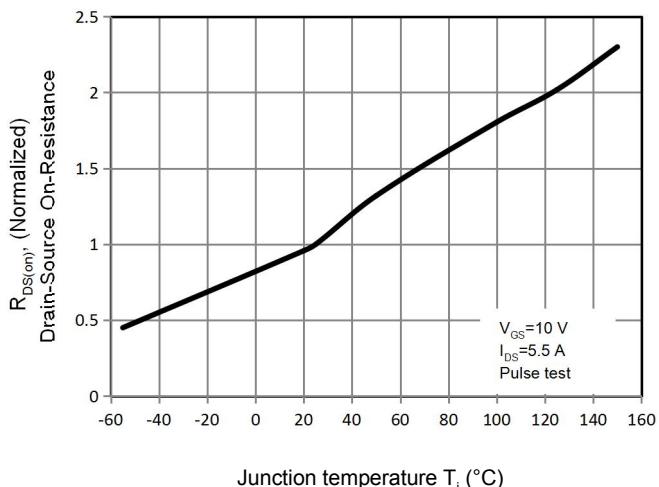
Figure 7. Capacitance Characteristics

$C_{iss} = C_{gs} + C_{gd}$ (C_{ds} = shorted)
$C_{oss} = C_{ds} + C_{gd}$
$C_{rss} = C_{gd}$

Notes:
f = 250 kHz
V_{GS}=0 V

Drain-Source Voltage V_{DS} (V)
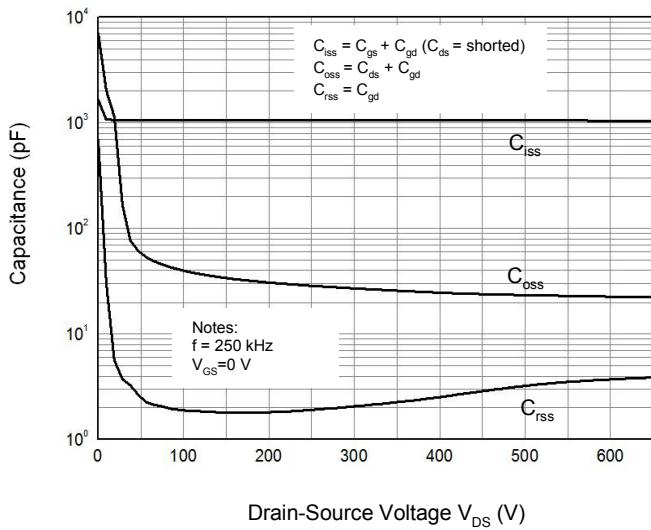
Figure 8. Gate Charge Characteristics

I_D = 5.5A

Total Gate Charge Q_G (nC)
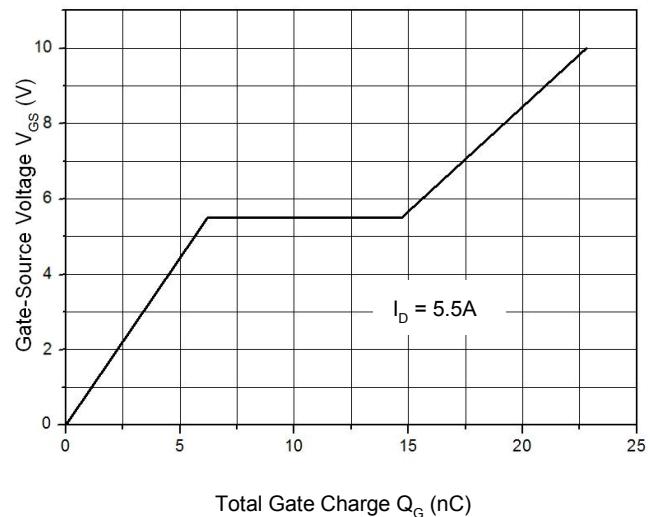
Figure 9. Power Dissipation vs. Temperature

Case temperature T_c (°C)
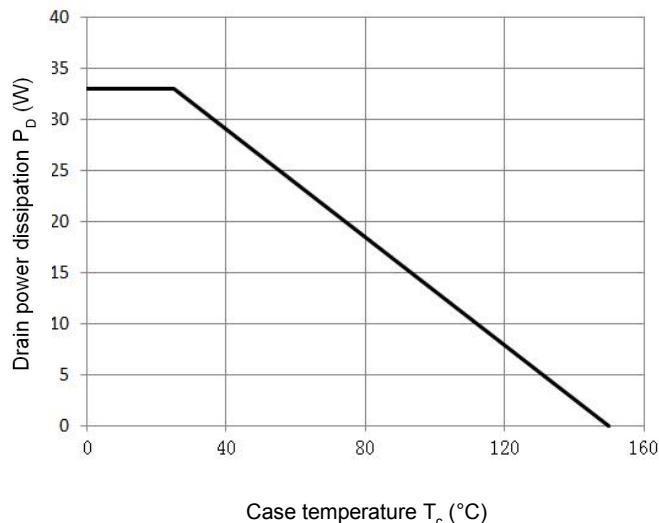
Figure 10.Drain Current Derating

Case temperature T_c (°C)
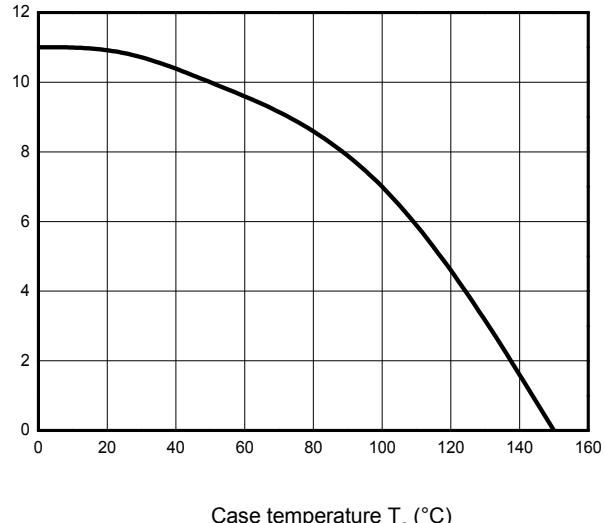
Figure 10: Safe Operating Area

Limited by Rds(on)

100us
1ms
DC
10ms
100ms

Single pulse
Tc=25°C

V_{DS} (V)
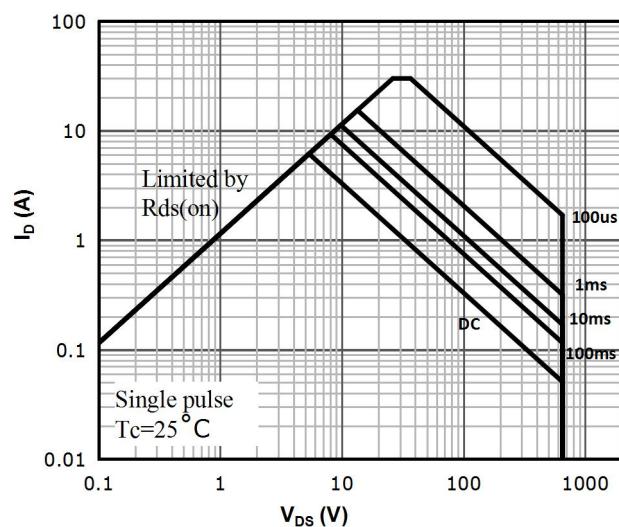
Figure 12. Transient Thermal Response Curve
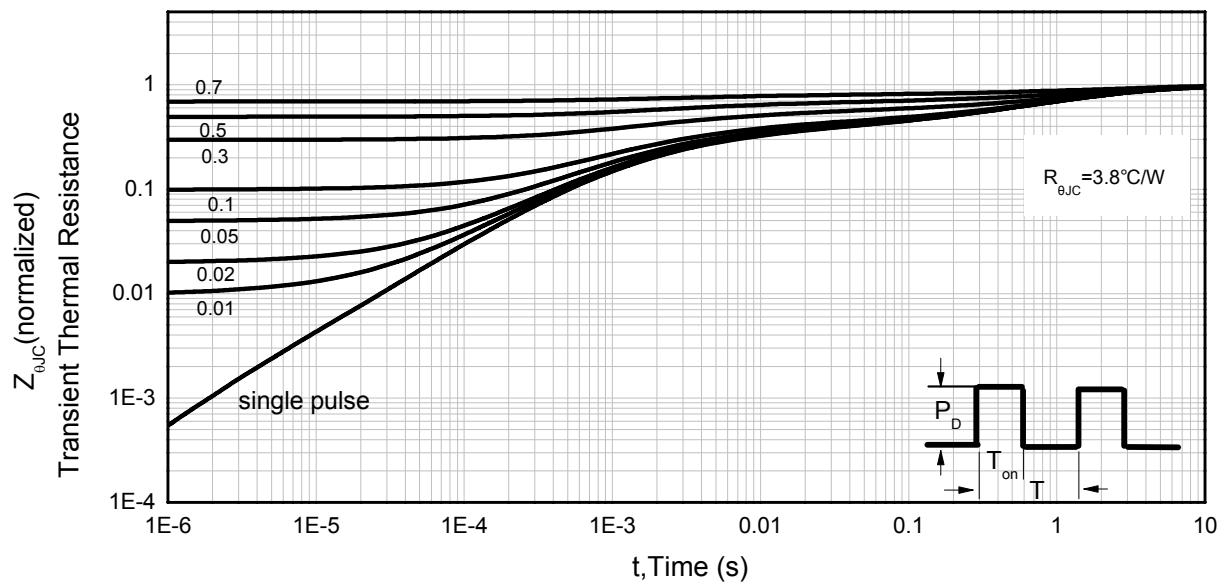
## Test Circuit & Waveforms

Gate Charge Test Circuit & Waveform
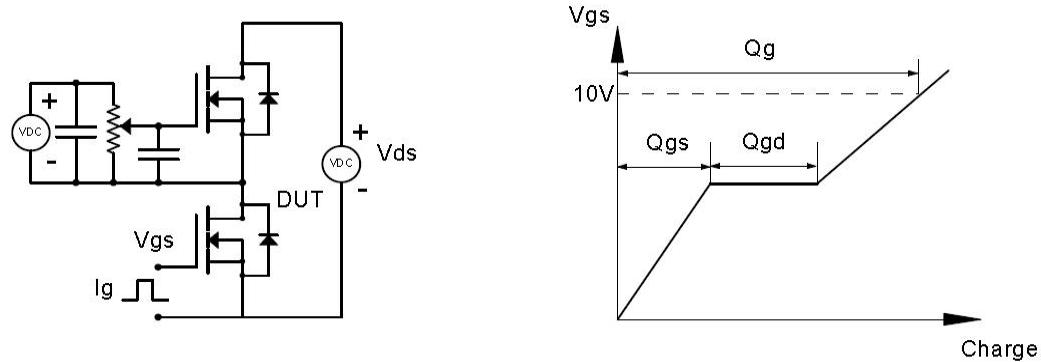
Resistive Switching Test Circuit & Waveforms
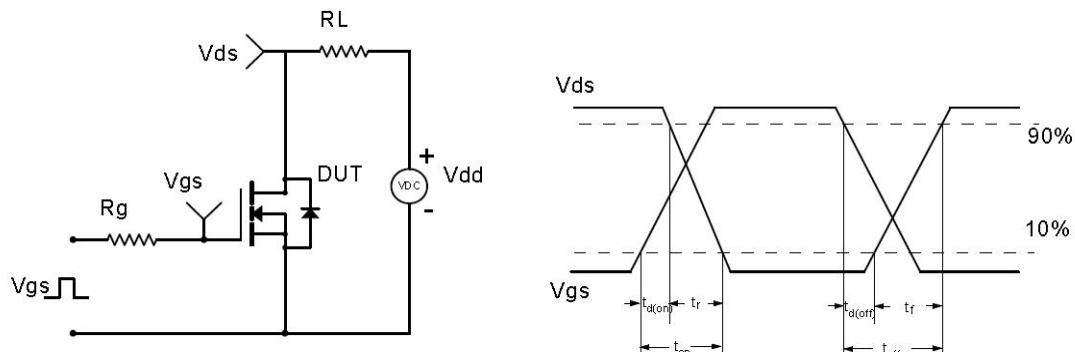
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

$$E_{AR} = 1/2 \, LI_{AR}^2$$
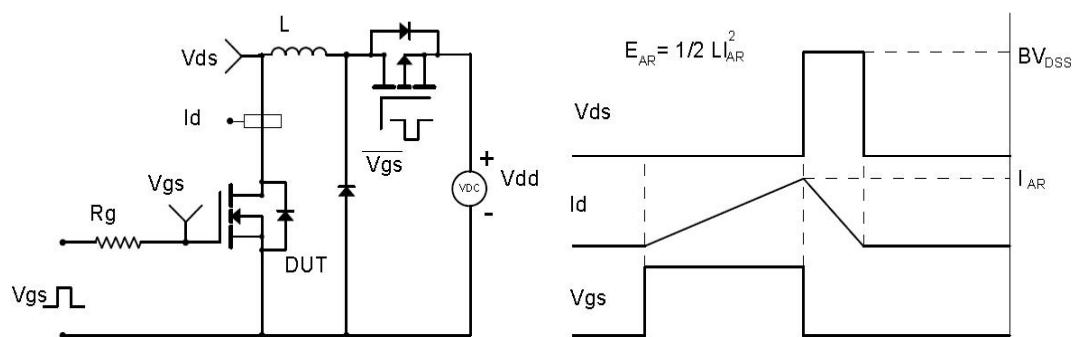
Mechanical Dimensions for TO-220NF
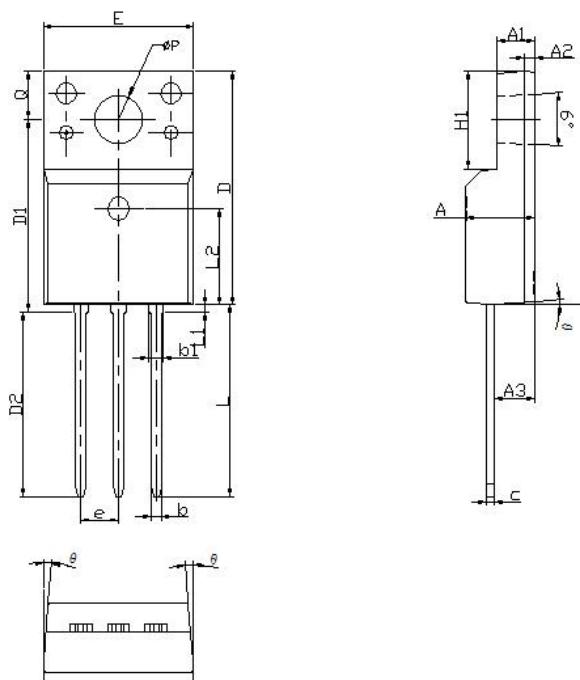
DIMENSIONS IN MILLITMETERS			DIMENSIONS IN INCHES	
SYMBOL	MIN	MAX	MIN	MAX
A	4.3	4.83	0.169	0.190
A1	2.34	2.9	0.092	0.114
A2	0.70REF		0.028REF	
A3	2.56	2.93	0.101	0.115
b	0.59	0.8	0.023	0.031
b1	–	1.1	–	0.043
c	0.45	0.79	0.018	0.031
D	14.7	16.07	0.579	0.633
D1	12.87	13.27	0.507	0.522
D2	12.28	12.68	0.483	0.499
E	9.7	10.36	0.382	0.408
e	2.54BSC		0.1BSC	
H1	6.48	7.1	0.255	0.280
L	12.68	13.35	0.499	0.526
L1	–	0.85	–	0.033
L2	6.50REF		0.256REF	
ΦP	3.05	3.4	0.120	0.134
Q	2.7	3.4	0.106	0.134
θ	1°	5°	1°	5°

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