

Lonten N-channel 650V, 2A, 2.5Ω LonFET™ Power MOSFET

Description

LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

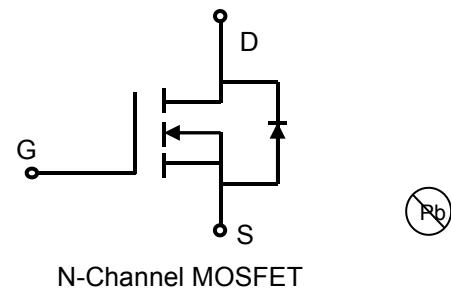
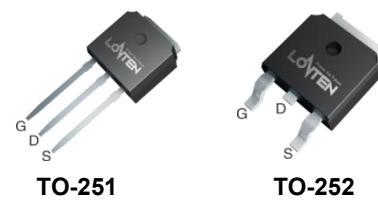
- ◆ Ultra low $R_{DS(on)}$
- ◆ Ultra low gate charge (typ. $Q_g = 9.5\text{nC}$)
- ◆ 100% UIS tested
- ◆ RoHS compliant

Applications

- ◆ Power factor correction (PFC).
- ◆ Switched mode power supplies (SMPS).
- ◆ Uninterruptible power supply (UPS).

Product Summary

$V_{DS} @ T_{j,max}$	700V
$R_{DS(on),max}$	2.5Ω
I_{DM}	5A
$Q_{g,typ}$	9.5nC



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	650	V
Continuous drain current ($T_c = 25^\circ\text{C}$)	I_D	2	A
($T_c = 100^\circ\text{C}$)		1.2	A
Pulsed drain current ¹⁾	I_{DM}	5	A
Gate-Source voltage	V_{GSS}	± 30	V
Avalanche energy, single pulse ²⁾	E_{AS}	25	mJ
Avalanche energy, repetitive ³⁾	E_{AR}	0.07	mJ
Avalanche current, repetitive ³⁾	I_{AR}	2	A
Power Dissipation TO-251/TO-252 ($T_c = 25^\circ\text{C}$)	P_D	25	W
- Derate above 25°C		0.2	W/ $^\circ\text{C}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$
Continuous diode forward current	I_S	2	A
Diode pulse current	$I_{S,pulse}$	5	A

Thermal Characteristics TO-251/TO-252

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	75	$^\circ\text{C}/\text{W}$
Soldering temperature, wavesoldering only allowed at leads. (1.6mm from case for 10s)	T_{sold}	260	$^\circ\text{C}$

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Reel
LSG65R2K5GT	TO-252	LSG65R2K5GT		2500
LSH65R2K5GT	TO-251	LSH65R2K5GT	72	

Electrical Characteristics

T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25mA	2.5	3.5	4.5	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V, T _j = 25°C T _j = 125°C	-	-	1	μA
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	50	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-50	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =1 A T _j = 25°C T _j = 150°C	-	2.2	2.5	Ω
Gate resistance	R _G	f=1 MHz, open drain	-	9	-	Ω
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} = 100 V, V _{GS} = 0 V, f = 250 kHz	-	127	-	pF
Output capacitance	C _{oss}		-	6.8	-	
Reverse transfer capacitance	C _{rss}		-	1.2	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 300V, I _D = 1A R _G = 25Ω, V _{GS} =10V	-	16	-	ns
Rise time	t _r		-	22	-	
Turn-off delay time	t _{d(off)}		-	32	-	
Fall time	t _f		-	17	-	
Gate charge characteristics						
Gate to source charge	Q _{gs}	V _{DD} =480 V, I _D =1A, V _{GS} =0 to 10 V	-	2.7	-	nC
Gate to drain charge	Q _{gd}		-	4.8	-	
Gate charge total	Q _g		-	9.5	-	
Gate plateau voltage	V _{plateau}		-	6	-	V
Reverse diode characteristics						
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =1A	-	1.0	-	V
Reverse recovery time	t _{rr}	V _R =50 V, I _F =2A, dI _F /dt=100 A/μs	-	178	-	ns
Reverse recovery charge	Q _{rr}		-	0.7	-	
Peak reverse recovery current	I _{rrm}		-	8	-	A

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- I_{AS} = 1A, V_{DD} = 60V, Starting T_j= 25°C.
- Repetitive Rating: Pulse width limited by maximum junction temperature.

Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

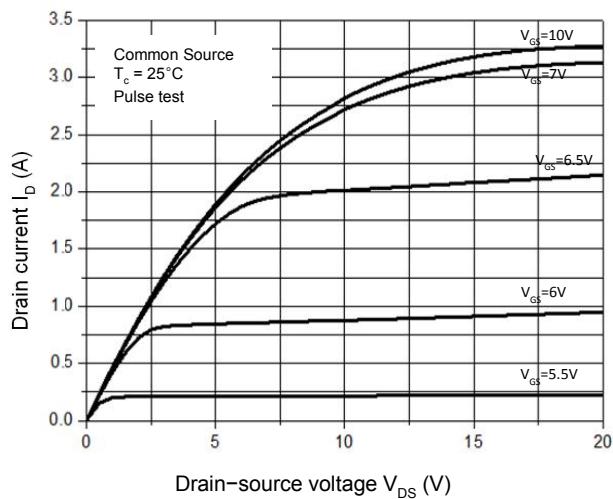


Figure 2. Transfer Characteristics

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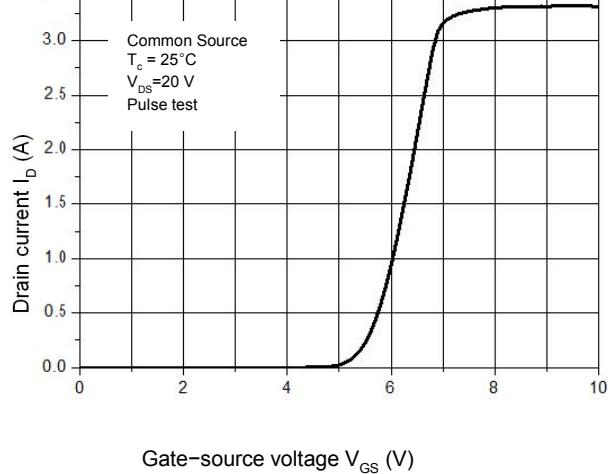


Figure 3. On-Resistance Variation vs. Drain Current

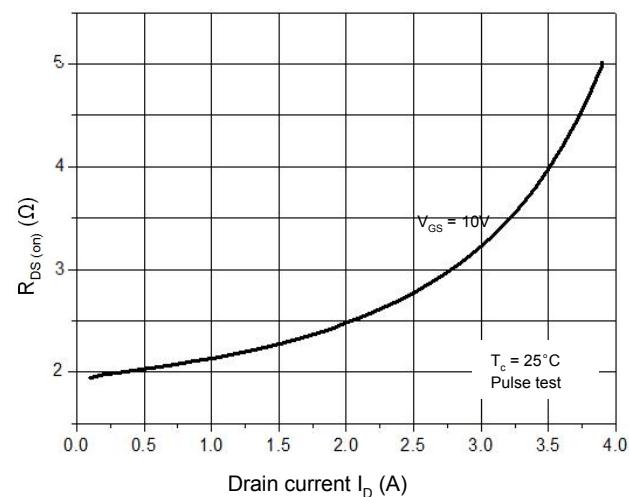


Figure 4. Threshold Voltage vs. Temperature

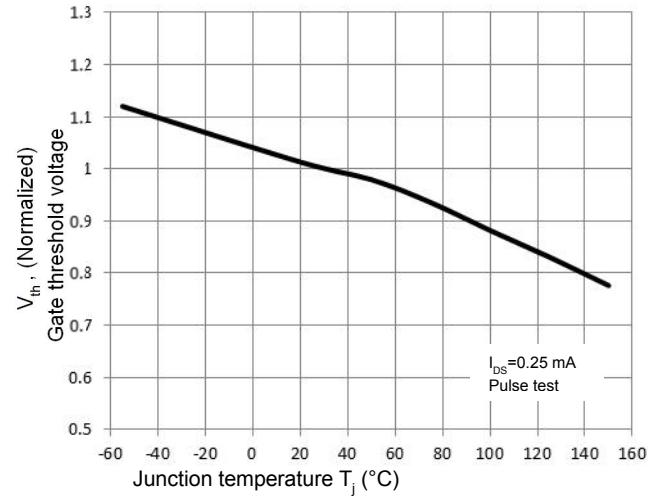


Figure 5. Breakdown Voltage vs. Temperature

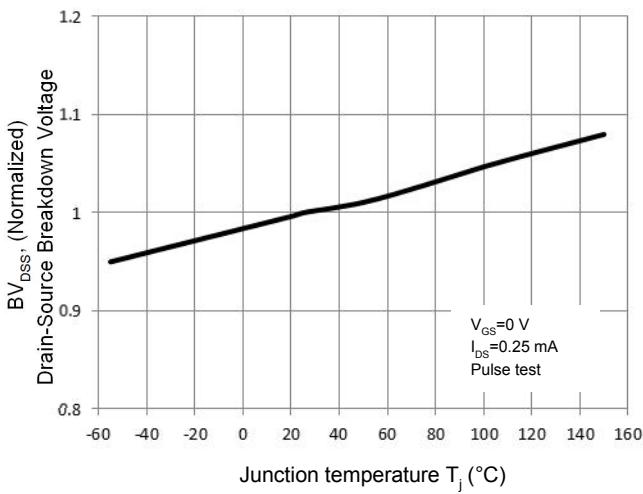


Figure 6. On-Resistance vs. Temperature

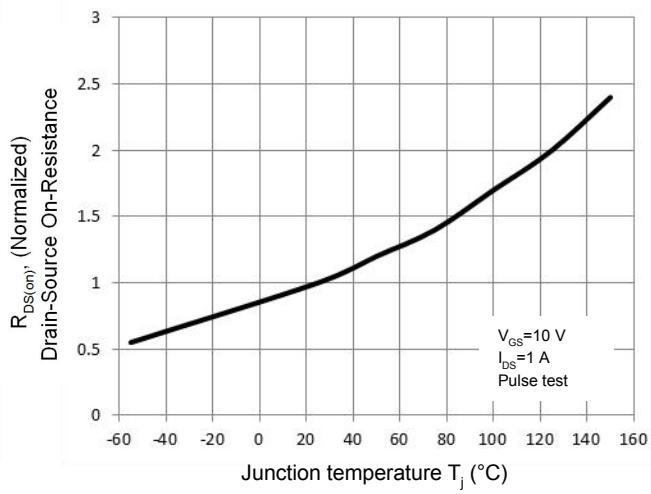


Figure 7. Capacitance Characteristics

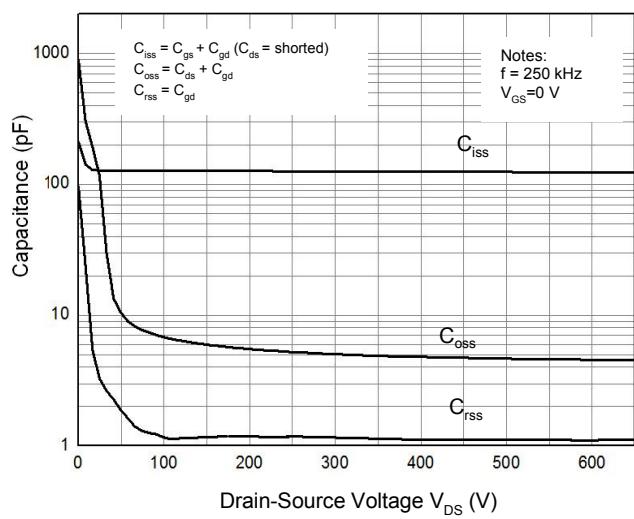


Figure 8. Gate Charge Characterist

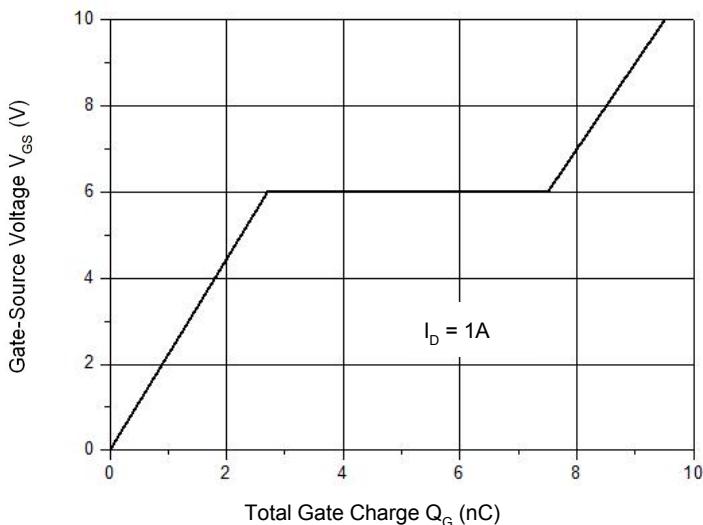


Figure 9. Maximum Safe Operating Area

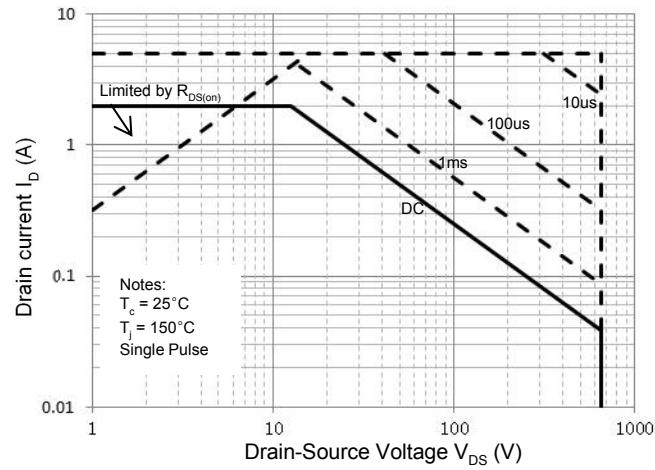


Figure 10. Power Dissipation vs. Temperature

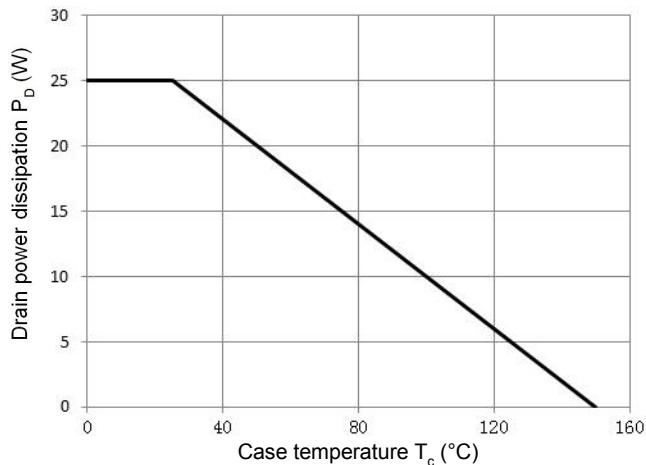
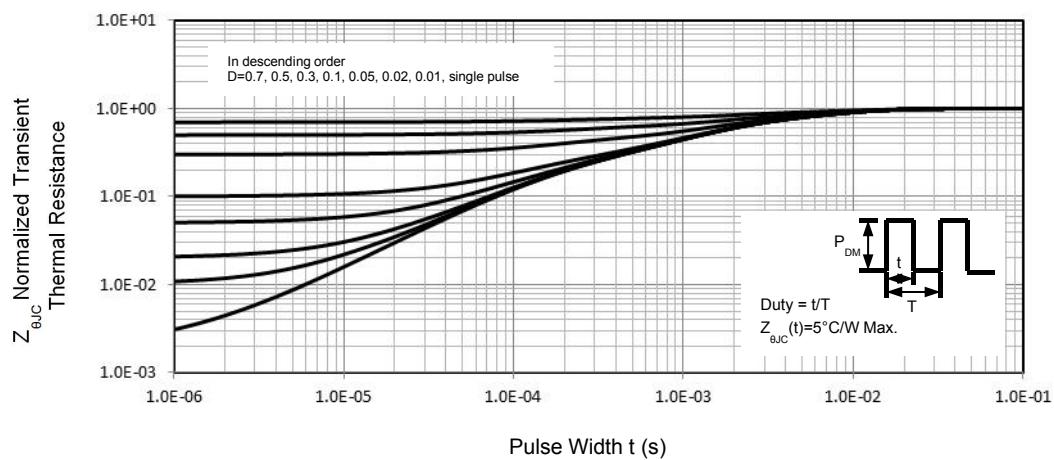
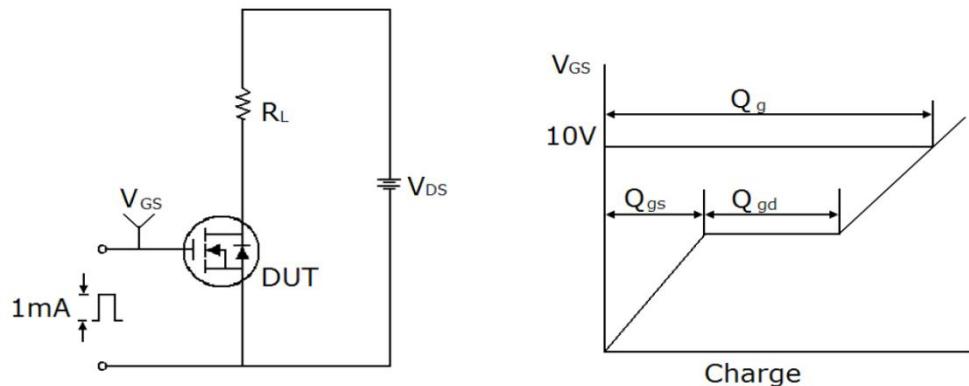


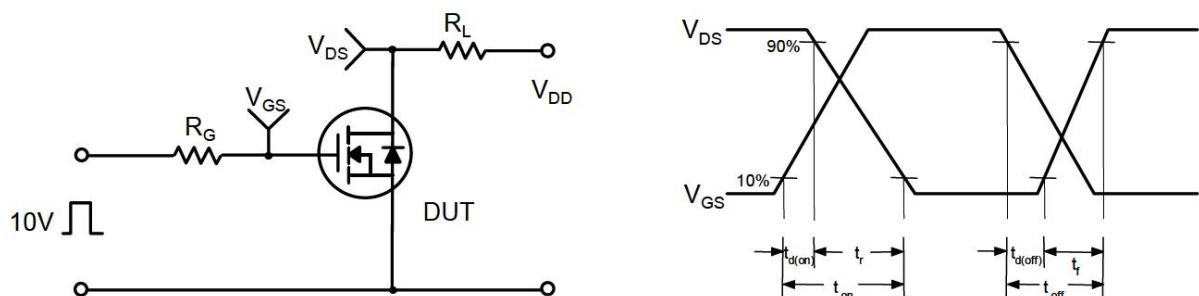
Figure 11. Transient Thermal Response Curve



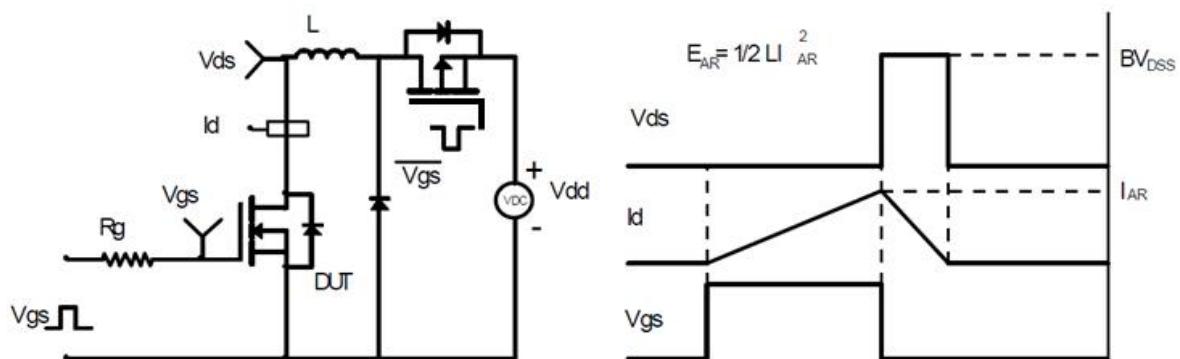
Gate Charge Test Circuit & Waveform



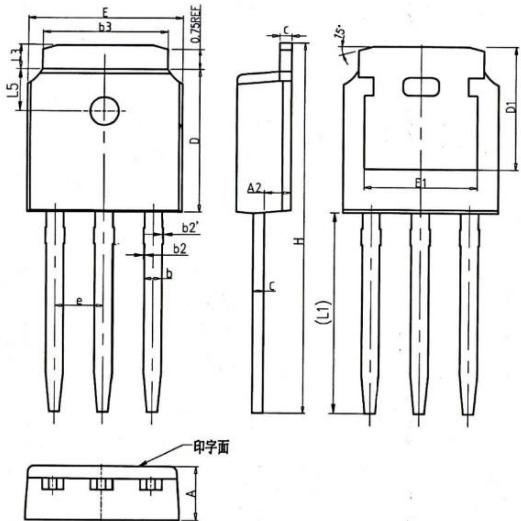
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

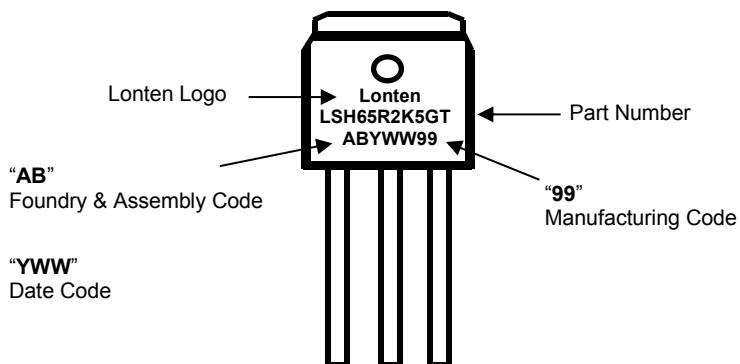


Mechanical Dimensions for TO-251

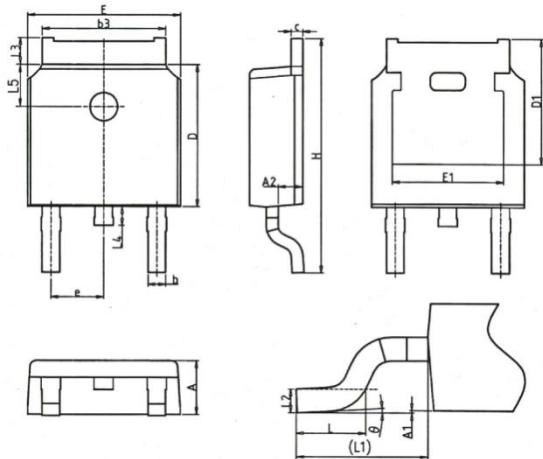


SYMBOL	COMMON DIMENSIONS		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	—	—
e	2.286BSC		
H	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

TO-251 Part Marking Information

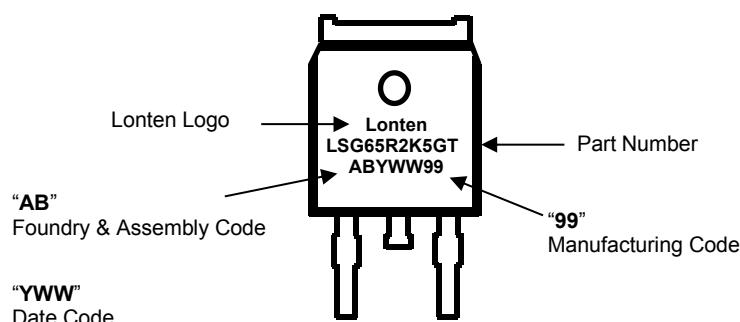


Mechanical Dimensions for TO-252



SYMBOL	COMMON DIMENSIONS			
	mm	MIN	NOM	MAX
A	2.30	2.20	2.30	2.38
A1	—	0.00	—	0.20
A2	1.17	0.97	1.07	—
b	0.90	0.68	0.78	—
b3	5.46	5.20	5.33	5.46
c	0.61	0.43	0.53	—
D	6.22	5.98	6.10	—
D1	5.30REF			
E	6.73	6.40	6.60	—
E1	—	4.63	—	—
e	2.286BSC			
H	10.50	9.40	10.10	—
L	1.75	1.38	1.50	—
L1	2.90REF			
L2	0.51BSC			
L3	1.28	0.88	—	—
L4	1.00	0.50	—	—
L5	1.95	1.65	1.80	—
θ	8°	0°	—	—

TO-252 Part Marking Information



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