Lonten N-channel 800V, 2A, 2.8Ω LonFET™ Power MOSFET

Description

LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

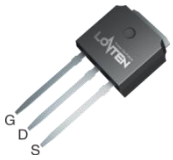
- Ultra low $R_{DS(on)}$
- Ultra low gate charge (typ. Qg = 7.7nC)
- 100% UIS tested
- RoHS compliant

Applications

- Power faction correction (PFC).
- Switched mode power supplies (SMPS).
- Uninterruptible power supply (UPS).

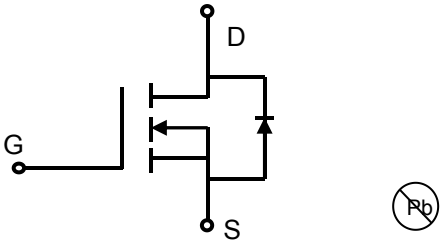
Product Summary

V_{DS} @ $T_{j,max}$	850V
$R_{DS(on),max}$	2.8Ω
I_{DM}	5A
$Q_{g,typ}$	7.7nC



TO-251

TO-252



N-Channel MOSFET

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	800	V
Continuous drain current (T_C = 25°C)	I_D	2	A
(T_C = 100°C)		1.2	A
Pulsed drain current [1]	I_{DM}	5	A
Gate-Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse [2]	E_{AS}	25	mJ
Avalanche energy, repetitive [3]	E_{AR}	0.07	mJ
Avalanche current, repetitive [3]	I_{AR}	2	A
Power Dissipation TO-251/TO-252 (T_C = 25°C)	P_D	25	W
- Derate above 25°C		0.2	W/°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Continuous diode forward current	I_S	2	A
Diode pulse current	$I_{S,pulse}$	5	A

Thermal Characteristics TO-251/TO-252

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	75	°C/W
Soldering temperature, wavesoldering only allowed at leads. (1.6mm from case for 10s)	T_{sold}	260	°C

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Real
LSG80R2K8GT	TO-252	LSG80R2K8GT		2500
LSH80R2K8GT	TO-251	LSH80R2K8GT	72	

Electrical Characteristics

T_c = 25°C unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0 V, I_D=0.25 mA	800	-	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=0.25mA	2.5	3.5	4.5	V
Drain cut-off current	I_{DSS}	V_{DS}=800 V, V_{GS}=0 V, T_j = 25°C T_j = 125°C	 - -	 - 10	 1 -	µA
Gate leakage current, Forward	I_{GSSF}	V_{GS}=30 V, V_{DS}=0 V	-	-	100	nA
Gate leakage current, Reverse	I_{GSSR}	V_{GS}=-30 V, V_{DS}=0 V	-	-	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS}=10 V, I_D=1 A T_j = 25°C T_j = 150°C	- - -	 2.2 5.7	 2.8	Ω
Gate resistance	R_G	f=1 MHz, open drain	-	11	-	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	V_{DS} = 100 V, V_{GS} = 0 V,	-	238	-	pF
Output capacitance	C_{oss}	f = 250 kHz	-	8.78	-	
Reverse transfer capacitance	C_{rss}		-	1.47	-	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 400V, I_D = 1A	-	19.8	-	ns
Rise time	t_r	R_G = 10Ω, V_{GS}=10V	-	33.7	-	
Turn-off delay time	$t_{d(off)}$		-	43.8	-	
Fall time	t_f		-	59.8	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	V_{DD}=400 V, I_D=1A,	-	1.2	-	nC
Gate to drain charge	Q_{gd}	V_{GS}=0 to 10 V	-	4.3	-	
Gate charge total	Q_g		-	7.7	-	
Gate plateau voltage	$V_{plateau}$		-	5.2	-	V
Reverse diode characteristics						
Diode forward voltage	V_{SD}	V_{GS}=0 V, I_F=1A	-	1.0	-	V
Reverse recovery time	t_{rr}	V_R=50 V, I_F=2A,	-	195	-	ns
Reverse recovery charge	Q_{rr}	dI_F/dt=100 A/µs	-	0.975	-	µC
Peak reverse recovery current	I_{rrm}		-	10	-	A

Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75.
2. I_{AS} = 1A, V_{DD} = 60V, Starting T_j= 25°C.
3. Repetitive Rating: Pulse width limited by maximum junction temperature.

Electrical Characteristics Diagrams

Figure 1. On-Region Characteristics

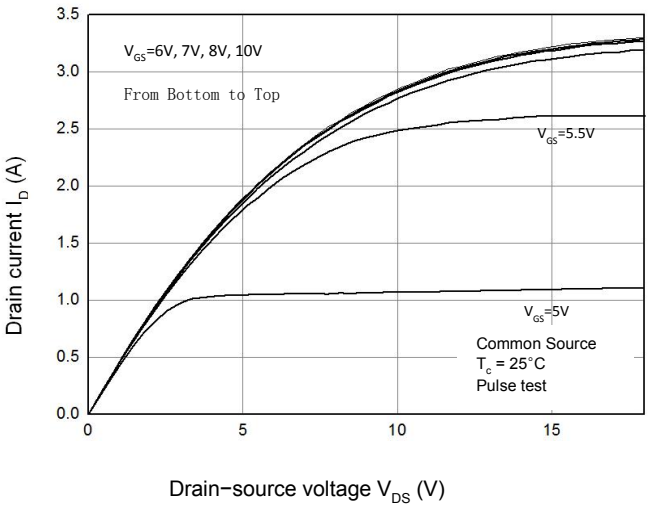


Figure 2. Transfer Characteristics

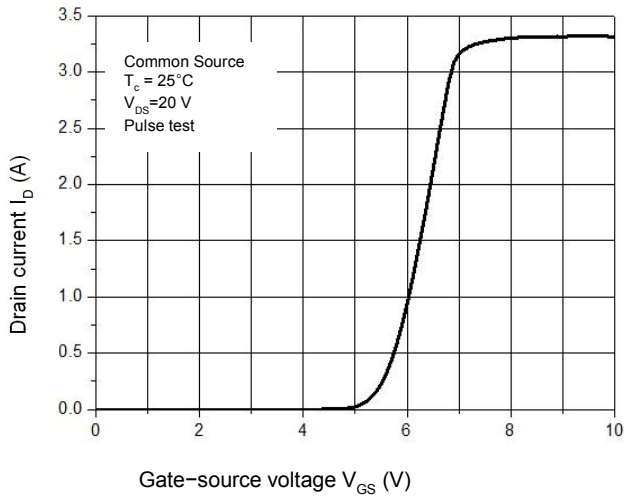


Figure 3. On-Resistance Variation vs. Drain Current

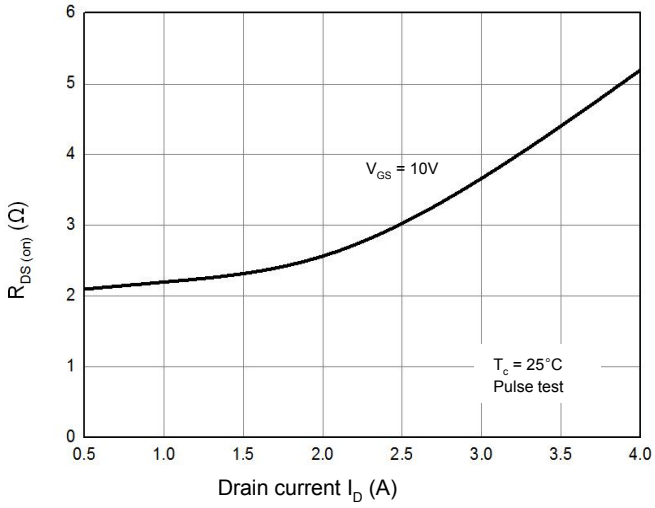


Figure 4. Threshold Voltage vs. Temperature

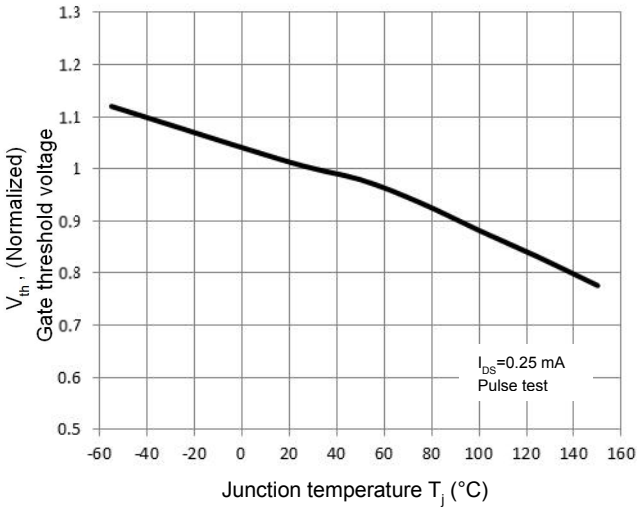


Figure 5. Breakdown Voltage vs. Temperature

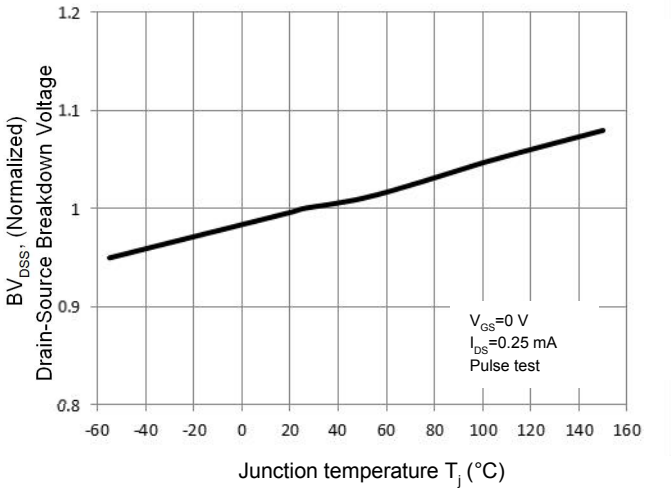


Figure 6. On-Resistance vs. Temperature

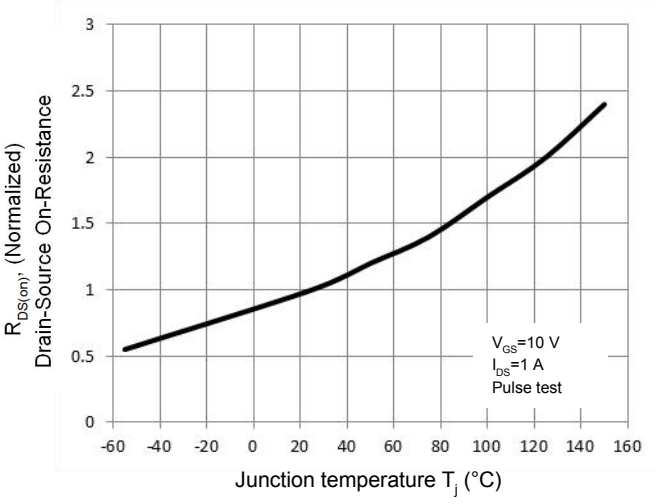


Figure 7. Capacitance Characteristics

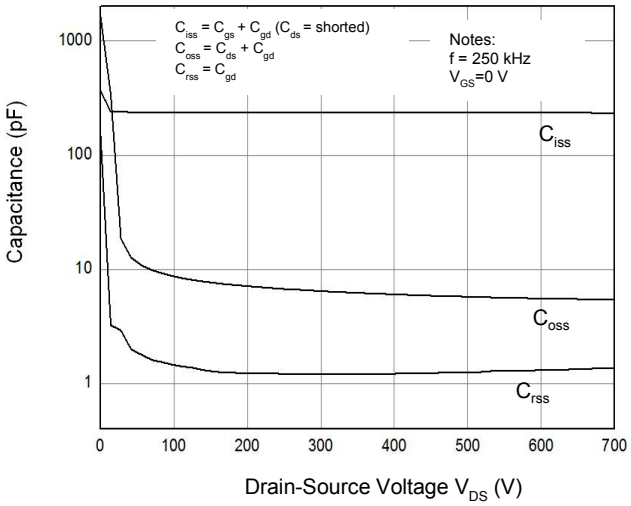


Figure 8. Gate Charge Characterist

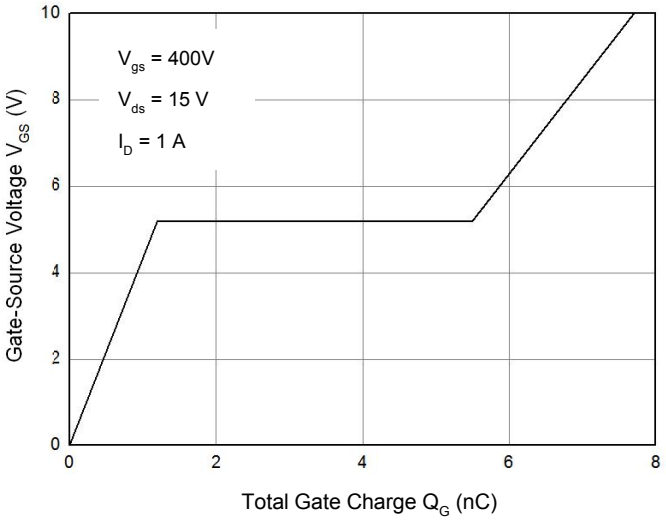


Figure 9. Maximum Safe Operating Area

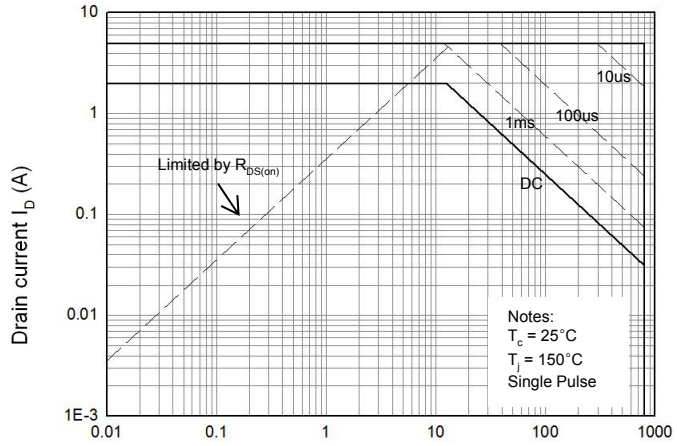


Figure 10. Power Dissipation vs. Temperature

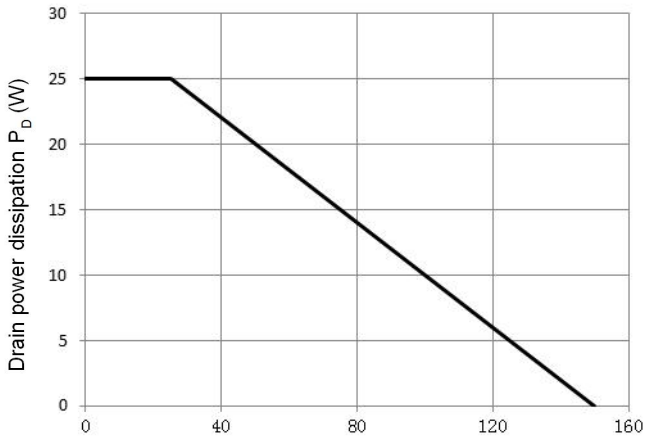
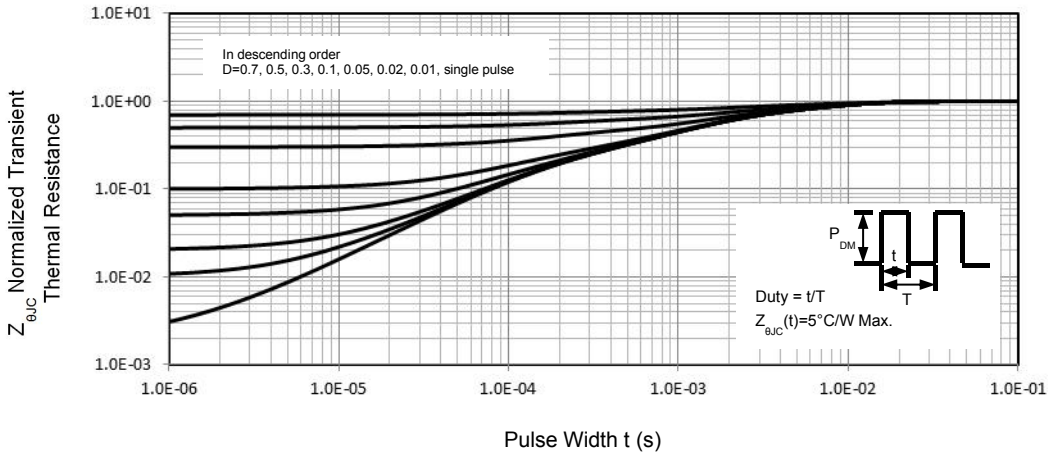
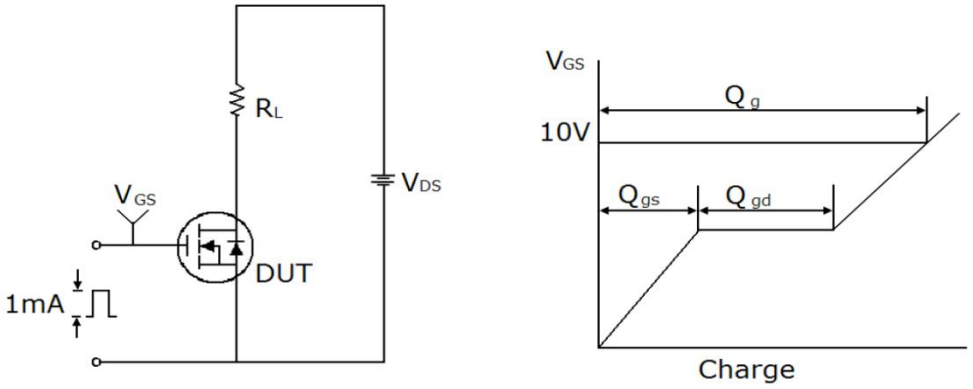


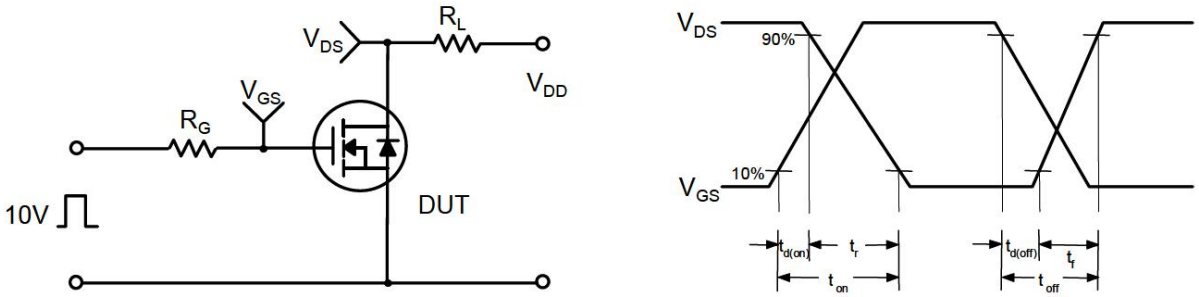
Figure 11. Transient Thermal Response Curve



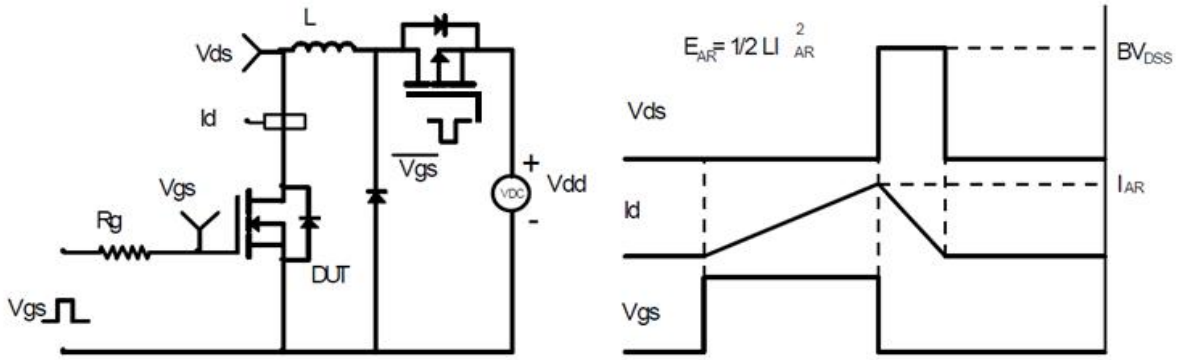
Gate Charge Test Circuit & Waveform



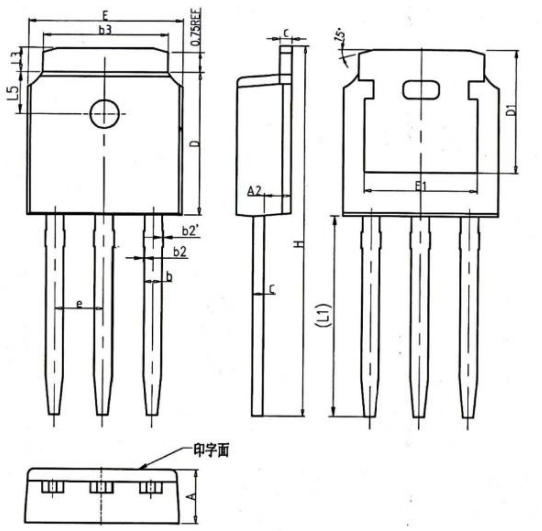
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

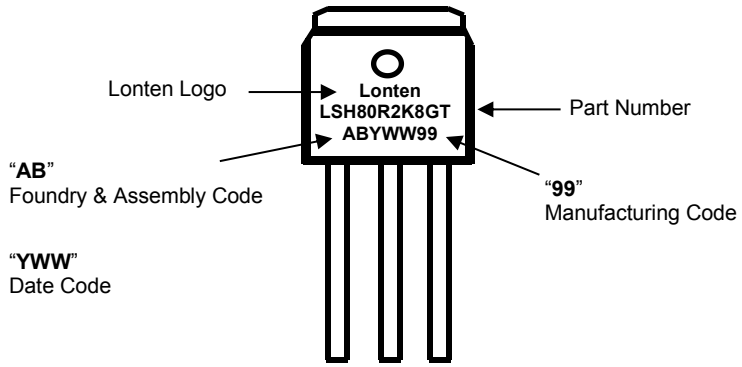


Mechanical Dimensions for TO-251



COMMON DIMENSIONS			
SYMBOL	MM		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.73
E1	4.63	—	—
e		2.286BSC	
H	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

TO-251 Part Marking Information



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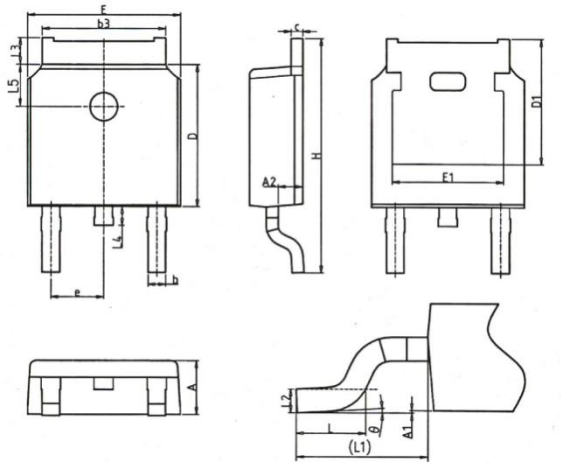

COMMON DIMENSIONS			
SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	—	0.20
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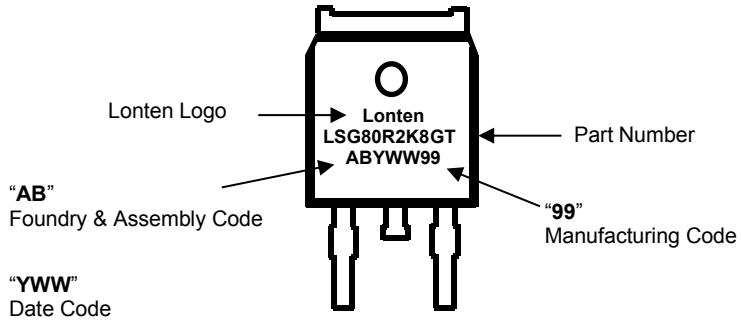
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	—	—
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	—	1.28
L4	0.50	—	1.00
L5	1.65	1.80	1.95
θ	0°	—	8°

TO-252 Part Marking Information



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