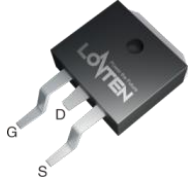
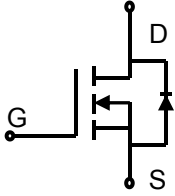



## Lonten N-channel 100V, 145A, 4.35mΩ Power MOSFET

<p><b>Description</b></p> <p>These N-Channel enhancement mode power field effect transistors are using <b>shielded gate trench DMOS</b> technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and with stand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.</p> <p><b>Features</b></p> <ul style="list-style-type: none"> <li>◆ 100V, 145A, <math>R_{DS(on),max}=4.35m\Omega@V_{GS} = 10V</math></li> <li>◆ Improved dv/dt capability</li> <li>◆ Fast switching</li> <li>◆ 100% EAS Guaranteed</li> <li>◆ Green device available</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>◆ Motor Drives</li> <li>◆ UPS</li> <li>◆ DC-DC Converter</li> </ul>	<p><b>Product Summary</b></p> <table style="width: 100%; border: none;"> <tr> <td style="padding: 2px;"><math>V_{DSS}</math></td> <td style="padding: 2px;">100V</td> </tr> <tr> <td style="padding: 2px;"><math>R_{DS(on),max}@ V_{GS}=10V</math></td> <td style="padding: 2px;">4.35mΩ</td> </tr> <tr> <td style="padding: 2px;"><math>I_D</math></td> <td style="padding: 2px;">145A</td> </tr> </table> <p><b>Pin Configuration</b></p> <div style="text-align: center;">  <p><b>TO-263</b></p>  <p>N-Channel MOSFET</p> </div> <div style="text-align: right; margin-top: 10px;">  </div>	$V_{DSS}$	100V	$R_{DS(on),max}@ V_{GS}=10V$	4.35mΩ	$I_D$	145A
$V_{DSS}$	100V						
$R_{DS(on),max}@ V_{GS}=10V$	4.35mΩ						
$I_D$	145A						

### Absolute Maximum Ratings $T_C = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	100	V
Continuous drain current ( $T_C = 25^\circ C$ ) ( $T_C = 100^\circ C$ )	$I_D$	145	A
		92	A
Pulsed drain current <sup>1)</sup>	$I_{DM}$	480	A
Gate-Source voltage	$V_{GSS}$	$\pm 20$	V
Avalanche energy <sup>2)</sup>	$E_{AS}$	272	mJ
Power Dissipation	$P_D$	156	W
Storage Temperature Range	$T_{STG}$	-55 to +150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to +150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.8	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient <sup>3)</sup>	$R_{\theta JA}$	75	$^\circ C/W$

### Package Marking and Ordering Information

Device	Device Package	Marking	Units/Reel
LSGE10R042	TO-263	LSGE10R042	800

**Electrical Characteristics**
 $T_J = 25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static characteristics</b>						
Drain-source breakdown voltage	$BV_{DSS}$	$V_{GS}=0\text{ V}, I_D=250\mu\text{A}$	100	---	---	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	---	4.0	V
Drain-source leakage current	$I_{DSS}$	$V_{DS}=100\text{ V}, V_{GS}=0\text{ V}, T_J = 25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=100\text{ V}, V_{GS}=0\text{ V}, T_J = 150^\circ\text{C}$	---	---	100	$\mu\text{A}$
Gate leakage current, Forward	$I_{GSSF}$	$V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$	---	---	100	nA
Gate leakage current, Reverse	$I_{GSSR}$	$V_{GS}=-20\text{ V}, V_{DS}=0\text{ V}$	---	---	-100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{ V}, I_D=40\text{ A}, T_J = 25^\circ\text{C}$	---	4.0	4.35	m $\Omega$
		$T_J = 150^\circ\text{C}$	---	7.2	---	
Forward transconductance	$g_{fs}$	$V_{DS} = 20\text{ V}, I_D=40\text{ A}$	---	120	---	S
<b>Dynamic characteristics</b>						
Input capacitance	$C_{iss}$	$V_{DS} = 50\text{ V}, V_{GS} = 0\text{ V}, f = 250\text{ kHz}$	---	3838	---	pF
Output capacitance	$C_{oss}$		---	1252	---	
Reverse transfer capacitance	$C_{rss}$		---	13.4	---	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 40\text{ V}, V_{GS}=15\text{ V}, I_D = 60\text{ A}$	---	29.4	---	ns
Rise time	$t_r$		---	29.2	---	
Turn-off delay time	$t_{d(off)}$		---	80.2	---	
Fall time	$t_f$		---	30.8	---	
Gate resistance	$R_g$	$V_{GS}=0\text{ V}, V_{DS}=0\text{ V}, f=1\text{ MHz}$	---	2.0	---	$\Omega$
<b>Gate charge characteristics</b>						
Gate to source charge	$Q_{gs}$	$V_{DS}=80\text{ V}, I_D=80\text{ A}, V_{GS}= 10\text{ V}$	---	20.5	---	nC
Gate to drain charge	$Q_{gd}$		---	16	---	
Gate charge total	$Q_g$		---	65	---	
Gate plateau voltage	$V_{plateau}$		---	5.5	---	V
Output Charge	$Q_{oss}$	$V_{DS}=80\text{ V}, V_{GS}= 0\text{ V}$	---	138	---	nC
<b>Drain-Source diode characteristics and Maximum Ratings</b>						
Continuous Source Current	$I_S$		---	---	111	A
Pulsed Source Current	$I_{SM}$		---	---	444	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0\text{ V}, I_S=80\text{ A}, T_J=25^\circ\text{C}$	---	---	1.4	V
Reverse Recovery Time	$t_{rr}$	$I_S=80\text{ A}, di/dt=100\text{ A}/\mu\text{s}, T_J=25^\circ\text{C}$	---	55.6	---	ns
Reverse Recovery Charge	$Q_{rr}$		---	233	---	nC

**Notes:**

- 1: Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2:  $V_{DD}=50\text{ V}, V_{GS}=10\text{ V}, L=0.5\text{ mH}, I_{AS}=33\text{ A}, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
- 3: The value of  $R_{thJA}$  is measured by placing the device in a still air box which is one cubic foot.

**Electrical Characteristics Diagrams**

Figure 1. Typ. Output Characteristics

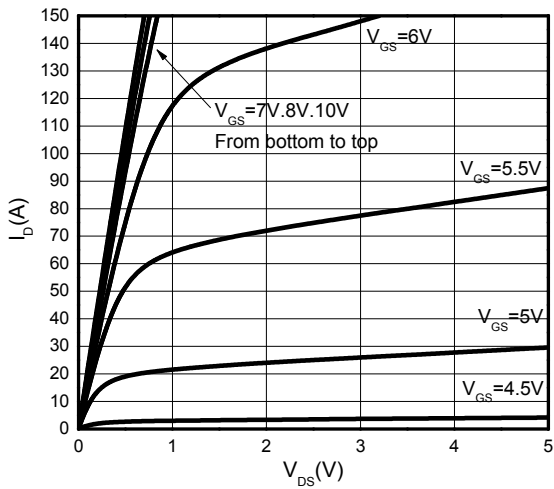


Figure 2. Transfer Characteristics

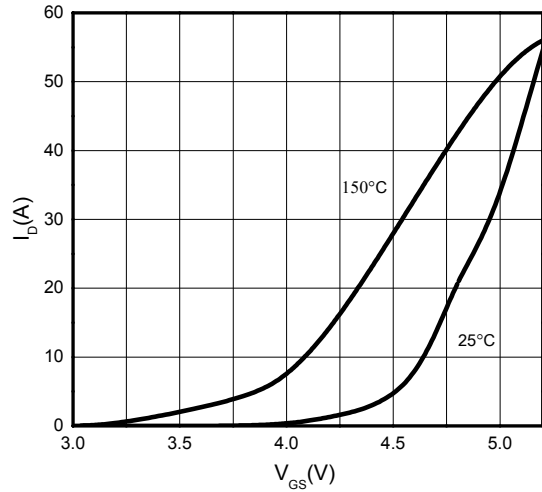


Figure 3. On-Resistance vs. Drain Current

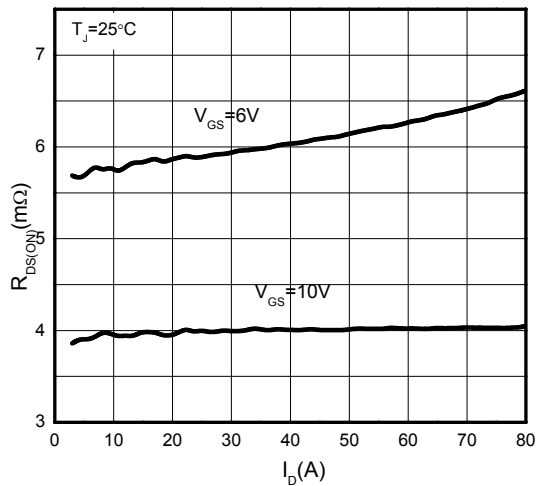


Figure 4. On-Resistance vs. Temperature

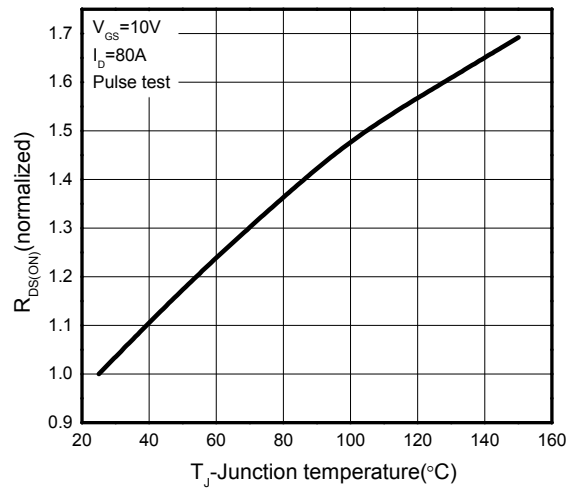


Figure 5. Breakdown Voltage vs. Temperature

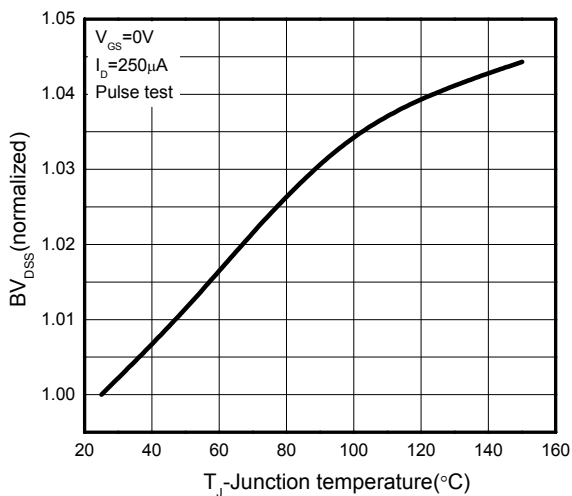


Figure 6. Threshold Voltage vs. Temperature

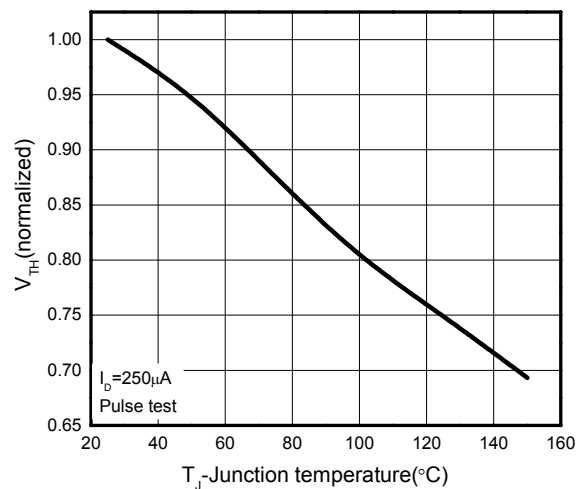


Figure 7. Rds(on) vs. Gate Voltage

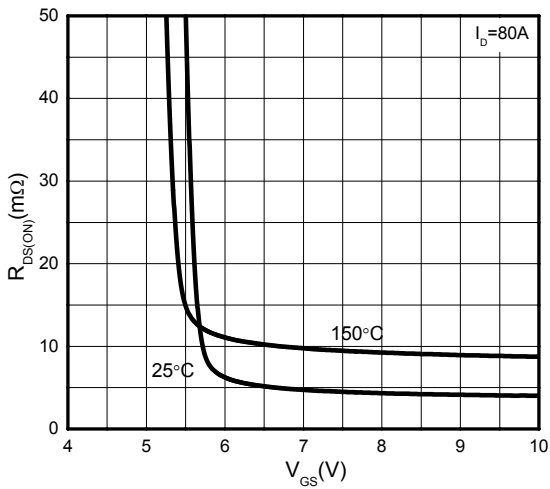


Figure 8. Body-Diode Characteristics

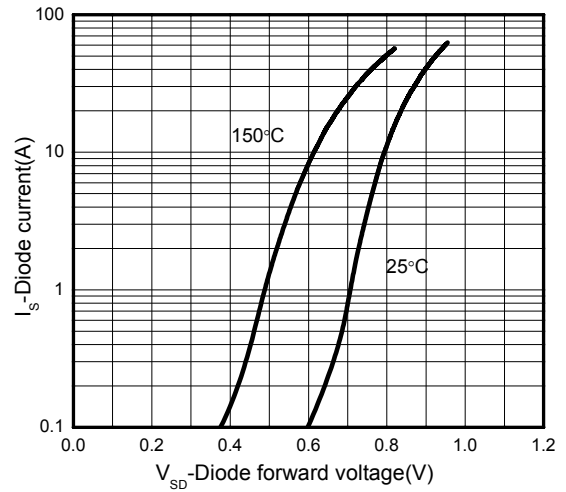


Figure 9. Capacitance Characteristics

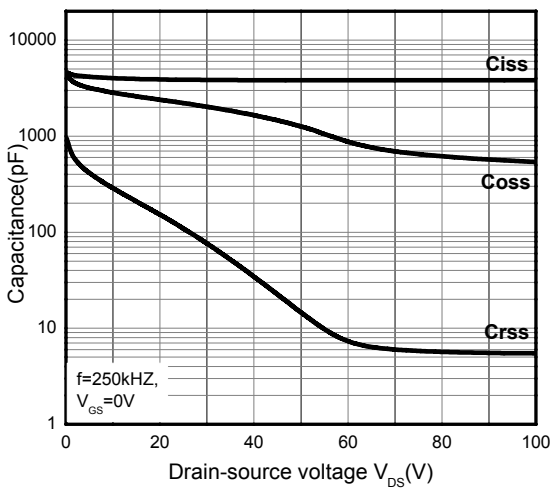


Figure 10. Gate Charge Characteristics

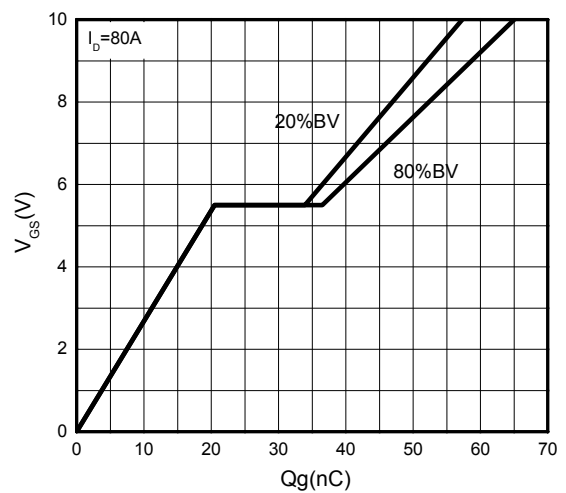


Figure 11. Drain Current Derating

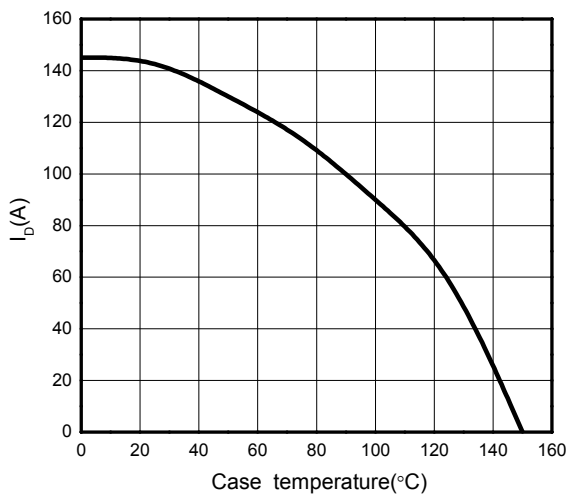


Figure 12. Power Dissipation vs. Temperature

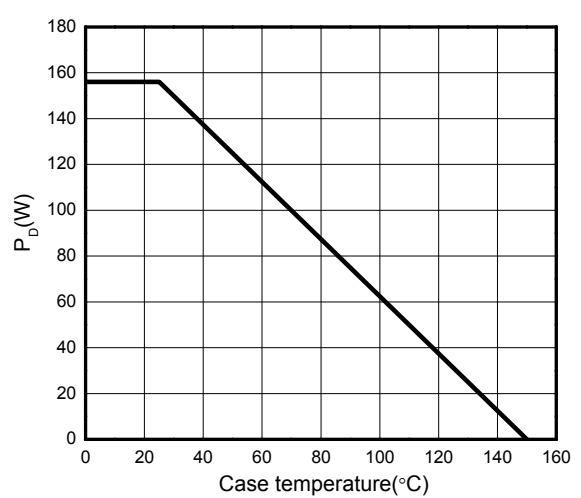


Figure 13: Safe Operating Area

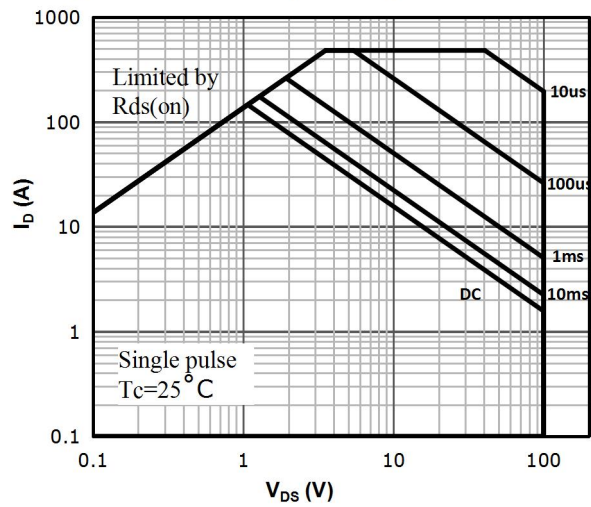
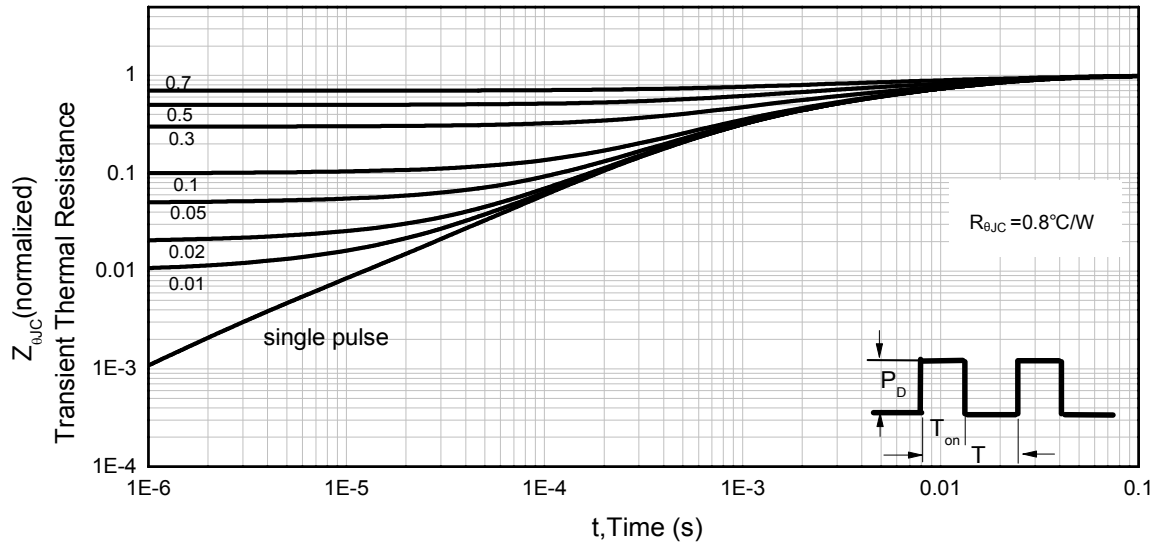
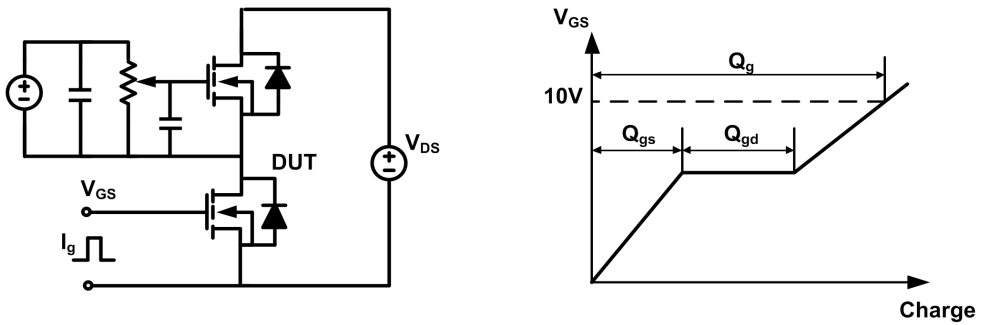


Figure 14. Normalized Maximum Transient Thermal Impedance (R<sub>thJC</sub>)

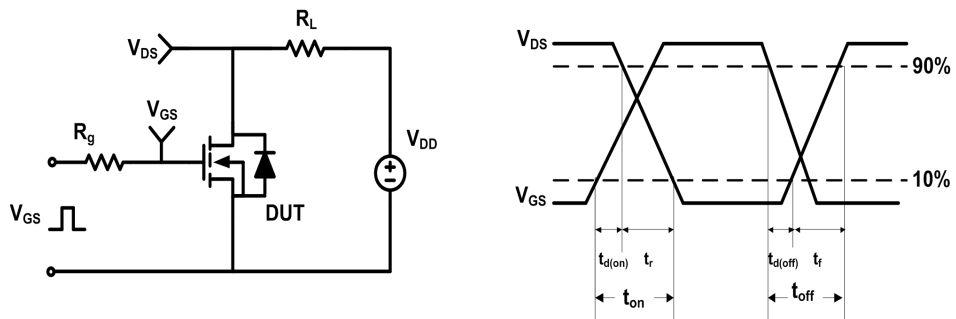


**Test Circuit & Waveforms**

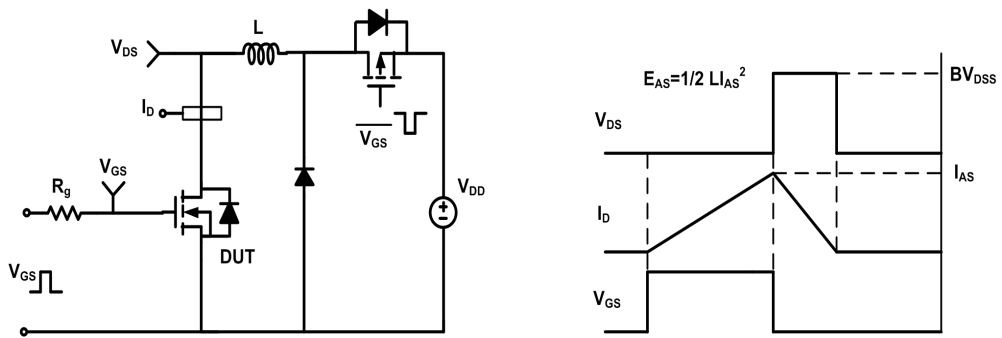
Gate Charge Test Circuit & Waveform



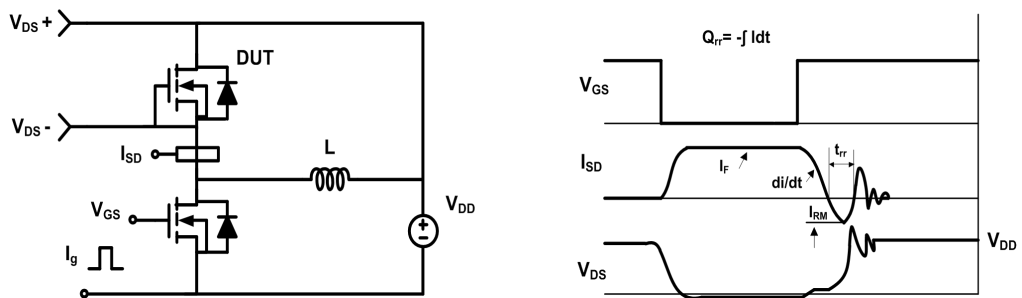
Resistive Switching Test Circuit & Waveform



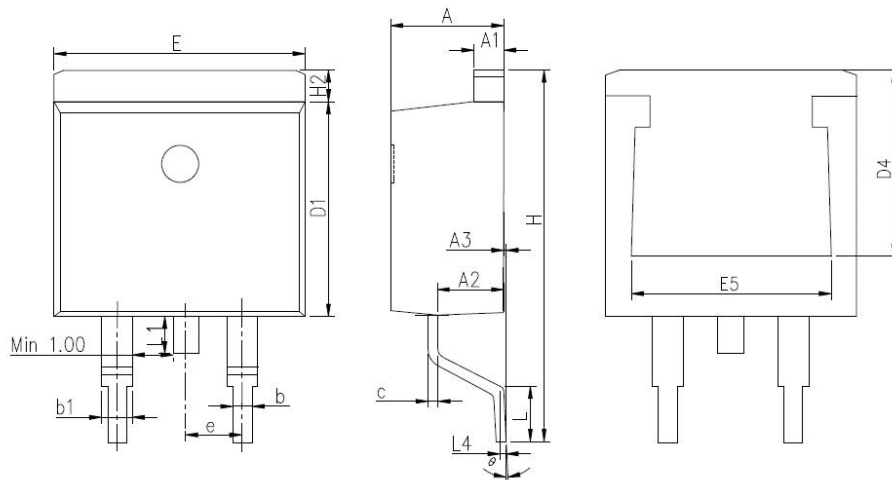
Unclamped Inductive Switching (UIS) Test Circuit & Waveform



Diode Recovery Test Circuit & Waveform



**Mechanical Dimensions for TO-263**



DIMENSIONS IN MILLITMETERS			DIMENSIONS IN INCHES	
SYMBOL	MIN	MAX	MIN	MAX
A	4.36	4.8	0.172	0.189
A1	1.19	1.42	0.047	0.056
A2	2.2	2.96	0.087	0.117
A3	0	0.25	0	0.010
b	0.7	0.96	0.028	0.038
b1	1.17	1.47	0.046	0.058
c	0.3	0.69	0.012	0.027
D1	8.5	9.5	0.335	0.374
D4	6.6	-	0.260	-
E	9.8	10.55	0.386	0.415
E5	7.06	8.7	0.278	0.343
e	2.54BSC		0.1BSC	
H	14.7	15.7	0.579	0.618
H2	0.95	1.65	0.037	0.065
L	1.9	2.8	0.075	0.110
L1	-	1.78	-	0.070
L4	0.25BSC		0.01BSC	
θ	0°	9°	0°	9°

## Revision History

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LSGE10R042

Revision:2020-12-30 ,Rev 1.1

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