



30V N-Channel MOSFET

General Description

The LPM9040A uses advanced trench technology to provide excellent $R_{DS(ON)}$ with low gate charge. This is an all purpose device that is suitable for use in a wide range of power conversion applications.

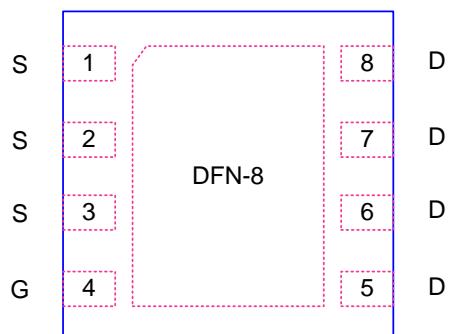
Order Information

LPM9040A □ □ □
 └── F: Pb-Free
 └── Package Type
 QV : DFN-8

Applications

- ✧ Driver for Relay, Solenoid, Motor, LED etc.
- ✧ DC-DC converter circuit
- ✧ Power Switch
- ✧ Load Switch
- ✧ Charging

Pin Configurations



Features

- ◆ 100% EAS Guaranteed
- ◆ Green Device Available
- ◆ Super Low Gate Charge
- ◆ Excellent CdV/dt effect decline
- ◆ Advanced high cell density Trench technology

Pin Description

Pin Number	Pin Description
1	
2	Source
3	
4	Gate
5	
6	
7	
8	Drain

Marking Information

Part	Marking	Package	Shipping
LPM9040AQVF	LPS 9040A YWX	DFN-8	5K/REEL
Marking indication:			
Y:Production year W:Production week X:Production batch.			



Absolute Maximum Ratings

Parameter		Symbol	10 Sec	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	±20		
Continuous Drain Current	TA=25°C	I _D	13.5	10	A
	TA=70°C		10.8	8	
Pulsed Drain Current		I _{DM}	120		
Avalanche Current		I _{AR}	23		
Repetitive avalanche energy L=0.3mH		E _{AR}	79		mJ
Power Dissipation	TA=25°C	P _D	3.1	1.7	W
	TA=70°C		2.0	1.1	
Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150		°C

Thermal resistance ratings

Parameter		Symbol	TYP	MAX	Unit
Junction-to-Case Thermal Resistance	t ≤ 10s	R _{θJA}	31	40	°C/W
Junction-to-Case Thermal Resistance	Steady State		59	75	°C/W
Maximum Junction-to-Lead	Steady State	R _{θJL}	16	24	°C/W



Electrical Characteristics

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1	μA
					5	
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm20\text{V}$			±100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=25\mu\text{A}$	1.7	2.2	3	V
$I_{\text{D(ON)}}$	On state drain current	$V_{GS}=10\text{V}, V_{DS}=5\text{V}$	120			A
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=10\text{A}$ $T_J=125^\circ\text{C}$		8.2	10	$\text{m}\Omega$
				12.5	16	
		$V_{GS}=4.5\text{V}, I_D=8\text{A}$		10	12.5	
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}, I_D=10\text{A}$		75		S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.72	1	V
I_S	Maximum Body-Diode Continuous Current				2.5	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}, V_{DS}=20\text{V}, f=1\text{MHz}$		1500	1950	pF
C_{oss}	Output Capacitance			215		pF
C_{rss}	Reverse Transfer Capacitance			135		pF
R_g	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	2	3.5	5	Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, I_D=10\text{A}$		27.2	37	nC
$Q_g(4.5\text{V})$	Total Gate Charge			13.6	18	nC
Q_{gs}	Gate Source Charge			4.5		nC
Q_{gd}	Gate Drain Charge			6.4		nC
$t_{\text{D(on)}}$	Turn-On Delay Time	$V_{GS}=10\text{V}, V_{DS}=20\text{V}, R_L=2\Omega, R_{\text{GEN}}=3\Omega$		6.4		ns
t_r	Turn-On Rise Time			17.2		ns
$t_{\text{D(off)}}$	Turn-Off Delay Time			29.6		ns
t_f	Turn-Off Fall Time			16.8		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=10\text{A}, dI/dt=100\text{A}/\mu\text{s}$		30	40	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=10\text{A}, dI/dt=100\text{A}/\mu\text{s}$		19		nC



Typical Characteristics

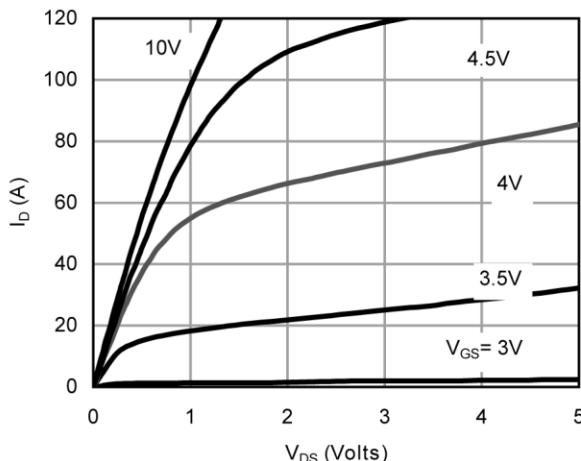


Figure 1: On-Region Characteristics

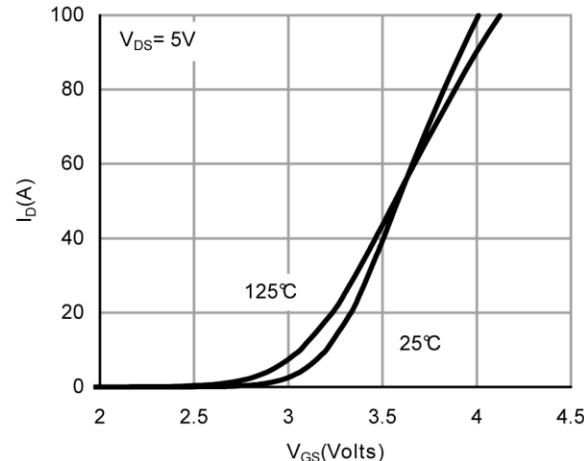


Figure 2: Transfer Characteristics

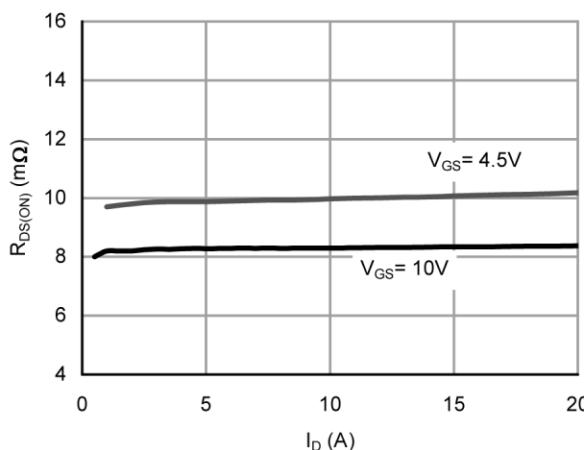


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

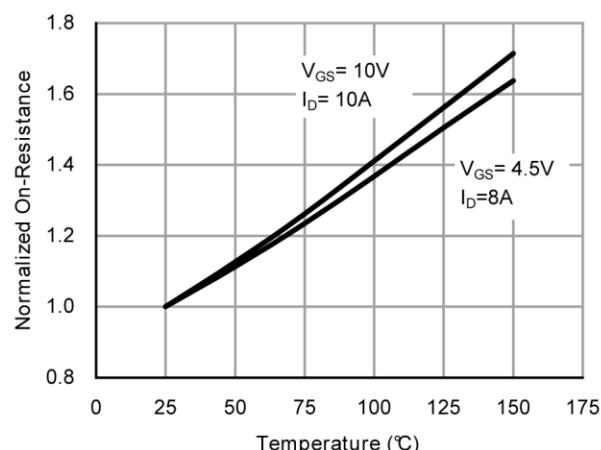


Figure 4: On-Resistance vs. Junction Temperature

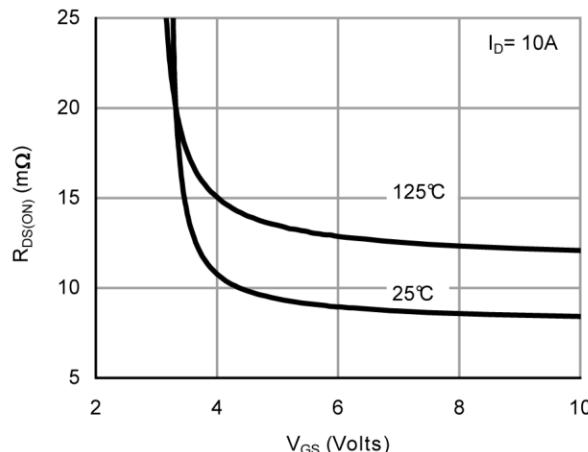


Figure 5: On-Resistance vs. Gate-Source Voltage

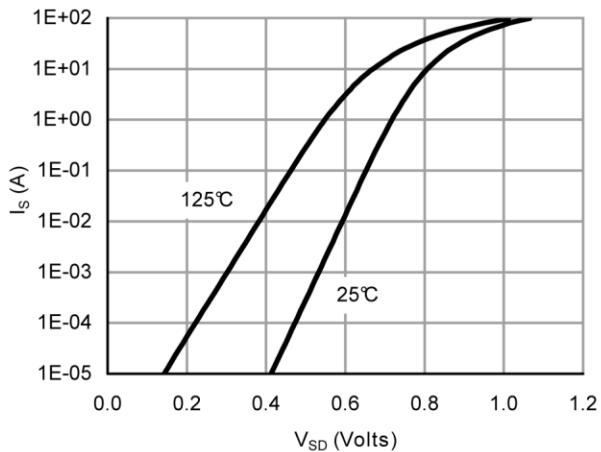


Figure 6: Body-Diode Characteristics

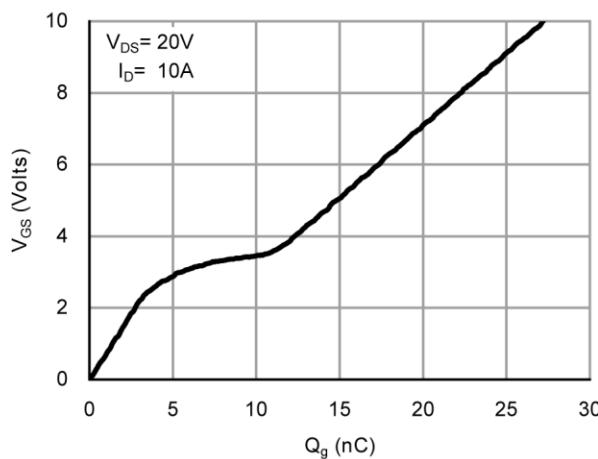


Figure 7: Gate-Charge Characteristics

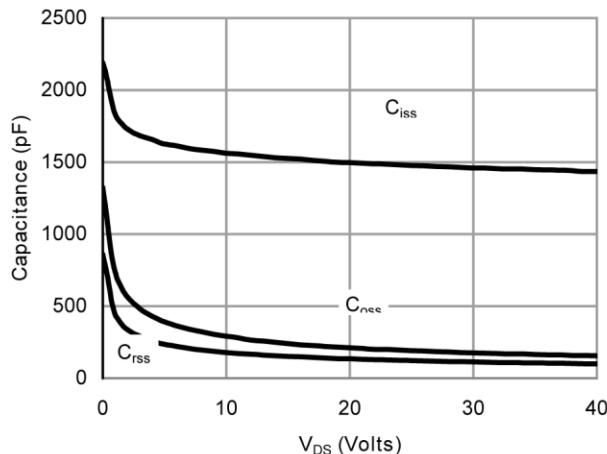


Figure 8: Capacitance Characteristics

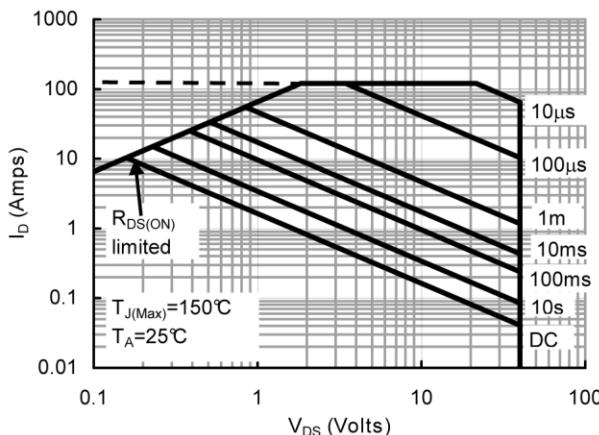


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

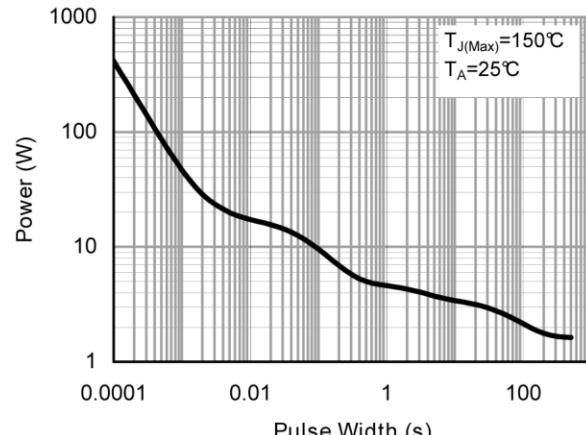


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

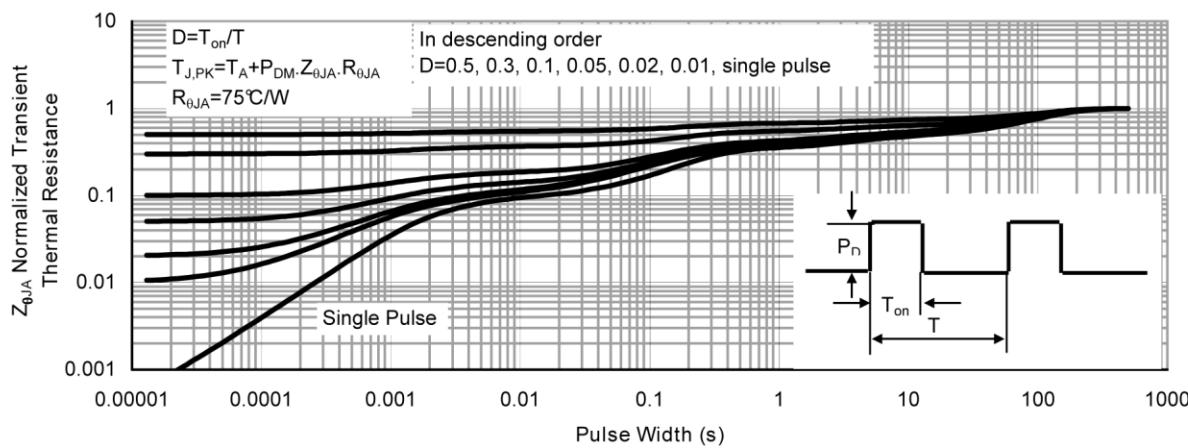
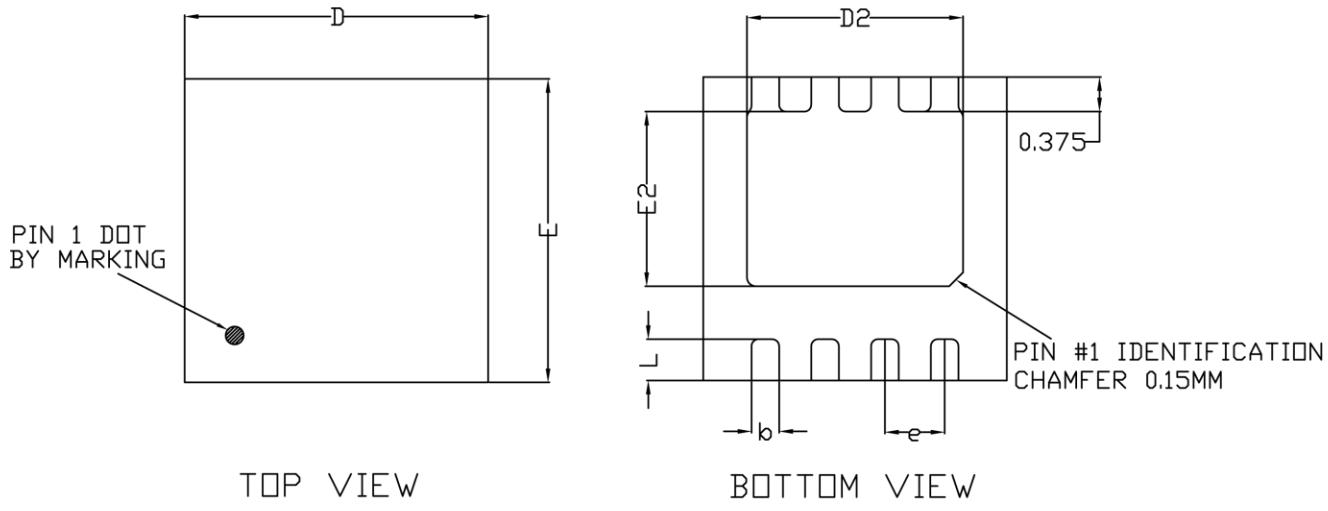


Figure 11: Normalized Maximum Transient Thermal Impedance (Note E)



Packaging Information

DFN-8



COMMON DIMENSIONS(MM)			
PKG.	UT:ULTRA THIN		
REF.	MIN.	NOM.	MAX
A	0.70	0.75	0.80
A1	0.00	-	0.05
A3	0.20 REF.		
D	3.25	3.30	3.35
E	3.25	3.30	3.35
D2	2.30	2.35	2.40
E2	1.85	1.90	1.95
b	0.25	0.30	0.35
L	0.35	0.45	0.55
e	0.65 BSC		

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