## ΜΑζΟΜ

## RF Power MOSFET Transistor 120 W, 2 - 175 MHz, 28 V

#### Features

- N-Channel enhancement mode device
- DMOS structure
- Lower capacitances for broadband operation
- High saturated output power
- Lower noise figure than bipolar devices
- RoHS Compliant

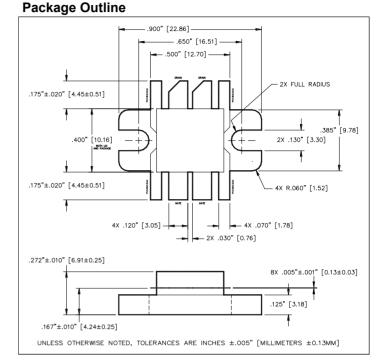
Symbol	Rating	Units				
V <sub>DS</sub>	65	V				
V <sub>GS</sub>	20	V				
I <sub>DS</sub>	12	А				
PD	250	W				
TJ	200	°C				
T <sub>STG</sub>	-55 to +150	°C				
θ <sub>JC</sub>	0.7	°C/W				
	Symbol V <sub>DS</sub> V <sub>GS</sub> I <sub>DS</sub> P <sub>D</sub> T <sub>J</sub> T <sub>STG</sub>	Symbol Rating   V <sub>DS</sub> 65   V <sub>GS</sub> 20   I <sub>DS</sub> 12   P <sub>D</sub> 250   T <sub>J</sub> 200   T <sub>STG</sub> -55 to +150				

### ABSOLUTE MAXIMUM RATINGS AT 25° C

### TYPICAL DEVICE IMPEDANCE

F (MHz)	Z <sub>IN</sub> (Ω)	Z <sub>LOAD</sub> (Ω)			
30	3.0 - j12.5	8.0 + j6.0			
50	1.5 - j8.5	7.0 +j6.5			
100	1.0 - j6.0	6.5 + j5.0			
$V_{DD}$ = 28V, $I_{DQ}$ = 600mA, $P_{OUT}$ = 120 W					

#### **ELECTRICAL CHARACTERISTICS AT 25°C**



 $Z_{\mbox{\scriptsize IN}}$  is the series equivalent input impedance of the device from gate to source.

 $Z_{\text{LOAD}}$  is the optimum series equivalent load impedance as measured from drain to ground.

Parameter	Symbol	Min	Мах	Units	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	65	-	V	V <sub>GS</sub> = 0.0 V , I <sub>DS</sub> = 3.0 mA
Drain-Source Leakage Current	I <sub>DSS</sub>	-	6.0	mA	$V_{\rm GS}$ = 28.0 V , $V_{\rm GS}$ = 0.0 V
Gate-Source Leakage Current	I <sub>GSS</sub>	-	6.0	μA	$V_{GS}$ = 20.0 V , $V_{DS}$ = 0.0 V
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	6.0	V	V <sub>DS</sub> = 10.0 V , I <sub>DS</sub> = 600.0 mA
Forward Transconductance	G <sub>M</sub>	3.0	-	S	$V_{\text{DS}}$ = 10.0 V , $I_{\text{DS}}$ = 6000.0 mA , $\Delta$ $V_{\text{GS}}$ = 1.0V, 80 $\mu s$ Pulse
Input Capacitance	CISS	-	270	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Output Capacitance	Coss	-	240	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Reverse Capacitance	C <sub>RSS</sub>	-	48	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Power Gain	G <sub>P</sub>	13	-	dB	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Drain Efficiency	ŋ₀	60	-	%	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Return Loss	RL	10	-	%	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Load Mismatch Tolerance	VSWR-T	-	30:1	-	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz

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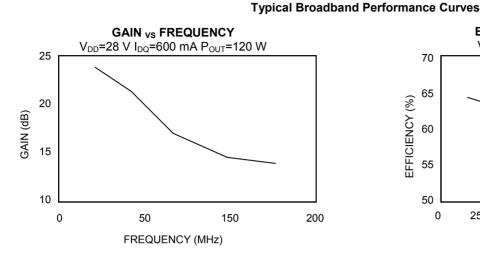
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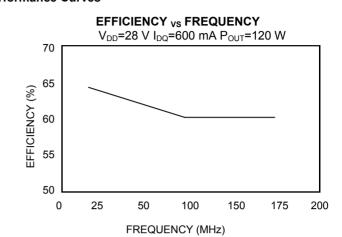




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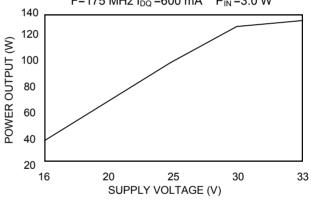
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POWER OUTPUT vs POWER INPUT V<sub>DD</sub> =28 V I<sub>DQ</sub> =600 mA 140 30MHz 175MHz 100MHz 20 0.1 0.2 0.3 1 2 3 4 5 6 7 8 9 POWER INPUT (W)

POWER OUTPUT vs SUPPLY VOLTAGE F=175 MHz I<sub>DO</sub>=600 mA P<sub>IN</sub>=3.0 W

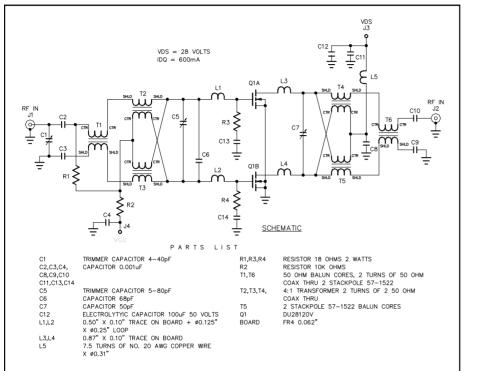


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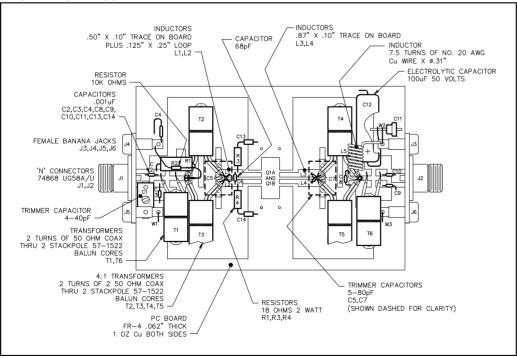
# DU28120V

## RF Power MOSFET Transistor 120 W, 2 - 175 MHz, 28 V

### TEST FIXTURE SCHEMATIC



#### **TEST FIXTURE ASSEMBLY**



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