## Features

- Ultra Broad Bandwidth: 50 MHz to 50 GHz
- Functional Bandwidth: 50 MHz to 70 GHz
- Insertion Loss: 0.7 dB
- Isolation: 33 dB @ 50 GHz
- Low Current Consumption:
- 10 mA for Low Loss State
+10 mA for Isolation State
- MACOMs Unique AIGaAs Hetero-Junction Anode Technology
- Silicon Nitride Passivation
- Polymer Scratch Protection
- RoHS Compliant


## Applications

- Aerospace \& Defense
- ISM


## Description

The MA4AGSW2 is an Aluminum-Gallium-Arsenide, single pole, double throw (SPDT), PIN diode switch. The switch features enhanced AIGaAs anodes which are formed using MACOM's hetero-junction technology. AIGaAs technology produces a switch with less loss than a device fabricated using conventional GaAs processes. As much as a 0.3 dB reduction in insertion loss can be realized at 50 GHz . This device is fabricated on an OMCVD epitaxial wafer using a process designed for high device uniformity and extremely low parasitics. The diodes within the chip exhibit low series resistance, low capacitance, and fast switching speed. They are fully passivated with silicon nitride and have an additional polymer layer for scratch protection. The protective coating prevents damage during handling and assembly to the diode junction and the chip anode air-bridges. Off chip bias circuitry is required.

The high electron mobility of AIGaAs and the low capacitance of the PIN diodes used makes this switch ideal for fast response, high frequency, multi-throw switch designs. AIGaAs PIN diode switches are an ideal choice for switching arrays in radar systems, radiometers, test equipment and other multi-assembly components.


Yellow areas indicate bond pads


Ordering Information

| Part Number | Package |
| :---: | :---: |
| MA4AGSW2 | waffle pack |
| MASW-000552-13210G | Gel Pack |

## Electrical Specifications:

$\mathrm{T}_{\mathrm{A}}=+\mathbf{2 5}{ }^{\circ} \mathrm{C}, \pm 10 \mathrm{~mA}$ Bias Current ${ }^{1}, \pm 5 \mathrm{~V}$ (on-wafer measurements)

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | $\begin{gathered} 0.05-18 \mathrm{GHz} \\ 18-50 \mathrm{GHz} \end{gathered}$ | dB | - | $\begin{aligned} & 0.5 \\ & 0.7 \end{aligned}$ | $\begin{aligned} & 0.6 \\ & 0.9 \end{aligned}$ |
| Isolation | $\begin{gathered} 0.05-18 \mathrm{GHz} \\ 18-50 \mathrm{GHz} \end{gathered}$ | dB | $\begin{aligned} & 45 \\ & 28 \end{aligned}$ | $\begin{aligned} & 47 \\ & 33 \end{aligned}$ | - |
| Input Return Loss | $\begin{gathered} 0.05-18 \mathrm{GHz} \\ 18-50 \mathrm{GHz} \end{gathered}$ | dB | - | $\begin{aligned} & 22 \\ & 21 \end{aligned}$ | - |
| Output Return Loss | $\begin{gathered} 0.05-18 \mathrm{GHz} \\ 18-50 \mathrm{GHz} \end{gathered}$ | dB | - | $\begin{aligned} & 25 \\ & 22 \end{aligned}$ | - |
| Switching Speed ${ }^{2}$ (10-90\% RF Voltage) | $\pm 5$ V TTL Compatible PIN Diode Driver, $10 \mathrm{GHz}$ | ns | - | 20 | - |

1. Bias current of $\pm 10 \mathrm{~mA}$ is recommended for optimal performance.
2. Typical switching speed is measured from $10 \%$ to $90 \%$ of the detected RF voltage driven by a $\pm 5 \mathrm{~V}$ TTL compatible driver. Driver output parallel RC network uses a capacitor between $390-560 \mathrm{pF}$ and a resistor between $150-220 \Omega$ to achieve 15 ns rise and fall times.

## Absolute Maximum Ratings ${ }^{3,4,5} @ \mathrm{~T}_{\mathrm{A}}=+\mathbf{2 5}^{\circ} \mathrm{C}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Incident C.W. RF Power | 23 dBm |
| DC Reverse Voltage | 25 V |
| Bias Current | $\pm 25 \mathrm{~mA}$ |
| Junction Temperature | $+175^{\circ} \mathrm{C}$ |
| Operating Temperature | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage Temperature | $-55^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Assembly Temperature | $+300^{\circ} \mathrm{C}<10 \mathrm{sec}$. |

3. Maximum combined operating conditions for RF Power, DC bias, and temperature: 23 dBm C.W. @ 10 mA (per diode) @ $+85^{\circ} \mathrm{C}$.
4. Exceeding any one or combination of these limits may cause permanent damage to this device.
5. MACOM does not recommend sustained operation near these survivability limits.

## Typical RF Performance Curves @ $\mathbf{+ 2 5 ^ { \circ }} \mathbf{C}$ (probed on wafer)

## Insertion Loss @-10 mA



Input Return Loss @ -10 mA


Isolation @ +10 mA


Output Return Loss @-10mA


## Operation of the MA4AGSW2 Switch

The simultaneous application of a negative DC current to the low loss port and positive DC current to the remaining isolated switching port is required for the operation of the MA4AGSW2, AIGaAs, PIN switch. The backside area of the die is the RF and DC return ground plane. The DC return is connected to the common port J 1 . The forward bias voltage at J 2 \& J 3 will not exceed $\pm 1.6 \mathrm{~V}$ and is typically $\pm 1.4 \mathrm{~V}$ with supply current of $\pm 10 \mathrm{~mA}$. In the low loss state, the series diode must be forward biased and the shunt diode reverse biased. While for the Isolated port, the shunt diode is forward biased and the series diode is reverse biased. The bias network design shown below should yield $>30 \mathrm{~dB}$ RF to DC Isolation.

The lowest insertion loss, P1dB, IP3, and switching speed is achieved by using a voltage pull-up resistor in the DC return path, (J1). A minimum value of $|-2 \mathrm{~V}|$ is recommended at this return node, which is achievable with a standard, $\pm 5 \mathrm{~V}$ TTL Controlled PIN Diode Driver.

## MA4AGSW2 Schematic with a Typical External 2-18 GHz Bias Network



## Typical Driver Connections

| Control Level (DC Current) |  | RF Output State |  |
| :---: | :---: | :---: | :---: |
| $\mathbf{J 2}$ | $\mathbf{J 3}$ | $\mathbf{J 2}-\mathbf{J 1}$ | J3 - J1 |
| -10 mA | +10 mA | Low Loss | Isolation |
| +10 mA | -10 mA | Isolation | Low Loss |

## MA4AGSW2 with MADR-011022 Driver Application Schematic 2-18 GHz



## Parts List ${ }^{6}$

| Part | Value |
| :---: | :---: |
| C1, C3 | $0.1 \mu \mathrm{~F}$ |
| C2 | 47 pF |
| C4, C5 | 470 pF |
| R1, R2 | $360 \Omega$ |
| R3 | $500 \Omega$ |

6. Resistor values calculated to provide $\sim 10 \mathrm{~mA}$ of bias current given $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$, $\mathrm{V}_{\text {EEB }}=-10 \mathrm{~V}$, voltage drop at driver output 0.4 V and $\mathrm{V}_{\mathrm{F}}$ of switch diodes 1 V .

## Chip Dimensions and Bonding Pad Locations (In Yellow)



| Dimensions | mils |  |  | mm |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 48.0 | 49.0 | 50.0 | 1.219 | 1.245 | 1.270 |
| B | 29.7 | 30.7 | 31.7 | 0.754 | 0.780 | 0.805 |
| C | 17.3 | 17.8 | 18.3 | 0.439 | 0.452 | 0.465 |
| D | 13.7 | 13.9 | 14.1 | 0.348 | 0.358 | 0.358 |
| E | 20.3 | 20.5 | 20.7 | 0.516 | 0.521 | 0.526 |
| F | 3.5 | 4.0 | 4.5 | 0.094 | 0.099 | 0.109 |
| Pads X-Y | 3.5 | 4.0 | 4.5 | 0.089 | 0.102 | 0.114 |

## Assembly Instructions

## Cleanliness

The chip should be handled in a clean environment.
Static Sensitivity
This device is considered ESD Class 1A, HBM. Proper ESD techniques should be used during handling.

## General Handling

The protective polymer coating on the active areas of the die provides scratch and impact protection, particularly for the metal air bridge, which contacts the diode's anode. Die should primarily be handled with vacuum pickup tools, or alternatively with plastic tweezers.

## Assembly Techniques

The MA4AGSW2, AIGaAs switch is designed to be mounted with electrically conductive silver epoxy or with a low temperature solder perform, which does not have a rich tin content.

## Solder DIE Attach

Only solders which do not scavenge gold, such as 80Au/20Sn or Indalloy \#2 is recommended. Do not expose die to temperatures $>300^{\circ} \mathrm{C}$ for more than 10 seconds.

Conductive Epoxy DIE Attach
Use a controlled thickness of approximately 2 mils for best electrical conductivity and lowest thermal resistance. Cure epoxy per manufacturer's schedule. Typically $150^{\circ} \mathrm{C}$ for 1 hour.

## Ribbon/Wire Bonding

Thermo-compression wedge or ball bonding may be used to attach ribbons or wire to the gold bonding pads. A $1 / 4 \times 3$ mil gold ribbon is recommended on all RF ports and should be kept as short as possible for the lowest inductance and best microwave performance. For more detailed handling and assembly instructions, see Application Note M541, "Bonding and Handling Procedures for Chip Diode Devices" at www.macom.com.

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