## Features

- Broad Bandwidth
- Specified from 50 MHz to 20 GHz
- Usable from 50 MHz to 26.5 GHz
- Lower Insertion Loss / Higher Isolation
- Fully Monolithic, Glass Encapsulated Chip
- Up to 33 dBm CW Power Handling @ $+25^{\circ} \mathrm{C}$
- RoHS* Compliant


## Applications

- Aerospace \& Defense
- ISM


## Description

The MA4SW110 is a series-shunt, broadband, PIN diode switch made with MACOM's HMIC ${ }^{\text {TM }}$ (Heterolithic Microwave Integrated Circuit) process. This process allows the silicon pedestals which form the series - shunt diode and vias to be embedded into low loss, low dispersion glass. By also incorporating small spacing between circuit elements, the result is an HMIC chip with low insertion loss and high isolation at frequencies up to 26.5 GHz .

They are designed for use as moderate power, high performance switches and provide superior performance when compared to similar designs that use discrete components.

The top side of the chip is protected by a polymer coating for manual or automatic handling and large gold bond pads help facilitate connection of low inductance ribbons. The gold metallization on the backside of the chip allows for attachment via 80/20, gold/tin solder or conductive silver epoxy.

## Functional Diagrams



Ordering Information

| Part Number | Package |
| :---: | :---: |
| MA4SW110 | Gel Pack |

[^0]Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=\boldsymbol{+ 2 5}{ }^{\circ} \mathrm{C}, \mathbf{2 0} \mathbf{~ m A}$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | 6 GHz 13 GHz 20 GHz | dB | - | $\begin{aligned} & 0.4 \\ & 0.5 \\ & 0.7 \end{aligned}$ | $\begin{aligned} & 0.7 \\ & 0.9 \\ & 1.2 \end{aligned}$ |
| Isolation | $\begin{aligned} & 6 \mathrm{GHz} \\ & 13 \mathrm{GHz} \\ & 20 \mathrm{GHz} \end{aligned}$ | dB | $\begin{aligned} & 46 \\ & 39 \\ & 34 \end{aligned}$ | $\begin{aligned} & 55 \\ & 47 \\ & 42 \end{aligned}$ | - |
| Input Return Loss | 6 GHz 13 GHz 20 GHz | dB | $\begin{aligned} & 22 \\ & 15 \\ & 14 \end{aligned}$ | $\begin{aligned} & 31 \\ & 33 \\ & 27 \end{aligned}$ | - |
| Switching Speed ${ }^{1}$ | - | ns | - | 50 | - |
| Voltage Rating ${ }^{2}$ | - | V | - | - | 50 |
| Signal Compression | $500 \mathrm{~mW}, 1 \mathrm{GHz}$ | dB | - | 0.2 | - |

1. Typical switching speed is measured from ( $10 \%$ to $90 \%$ and $90 \%$ to $10 \%$ of detected RF voltage), driven by TTL compatible drivers. In the modulating state, (the switching port is modulating, all other ports are in steady state isolation.) The switching speed is measured using an $R C$ network using the following values: $R=50-200 \Omega, C=390-1000 \mathrm{pF}$. Driver spike current, $\mathrm{I}_{\mathrm{C}}=\mathrm{C} \mathrm{dv} / \mathrm{dt}$, ratio of spike current to steady state current, is typically 10:1.
2. Maximum reverse leakage current in either the shunt or series PIN diodes shall be $10 \mu \mathrm{~A}$ maximum at -50 volts.

## Absolute Maximum Ratings ${ }^{3,4,5}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| RF CW Incident Power | $33 \mathrm{dBm} @+85^{\circ} \mathrm{C}$ |
| DC Reverse Voltage | 50 V |
| Bias Current per Port | $\pm 50 \mathrm{~mA} \mathrm{@}+25^{\circ} \mathrm{C}$ <br> $\pm 20 \mathrm{~mA} @+85^{\circ} \mathrm{C}$ |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Junction Temperature | $+175^{\circ} \mathrm{C}$ |

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. MACOM does not recommend sustained operation near these survivability limits.
5. Maximum operating conditions for a combination of RF power, DC bias and temperature: 33 dBm CW @ 15 mA (per diode) $+85^{\circ} \mathrm{C}$.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) and Class C1 (CDM) devices.

## Typical Performance Curves @ 10 GHz:

## Insertion Loss vs. Bias Current



Input Return Loss vs. Bias Current


Isolation vs. Bias Current


Output Return Loss vs. Bias Current


Rev. V12

## Typical Performance Curves @ $\mathrm{T}_{\mathrm{A}}=+\mathbf{+ 2} 5^{\circ} \mathrm{C}, 20 \mathrm{~mA}$ Bias Current

Return Loss vs. Frequency


Insertion Loss vs. Frequency


## Isolation vs. Frequency



## Operation of MA4SW110 Switch

The simultaneous application of a negative DC current to the low loss port and positive DC current to the isolated port is required for proper operation of the PIN switches. The backside area of the die is the RF and DC ground return and the DC return is through the common Port J1. A constant current source should be used to supply the DC control currents. The control voltages at these points will not exceed $\pm 1.5$ volts for supply currents up to $\pm 50 \mathrm{~mA}$. In the low loss state, the series diode must be forward biased and the shunt diode reverse biased. On all isolated ports, the shunt diode is forward biased and the series diode is reverse biased.

## Application Schematic with MADR-011022



## Parts List ${ }^{7}$

| Part | Value |
| :---: | :---: |
| C1, C3 | $0.1 \mu \mathrm{~F}$ |
| C2 | 47 pF |
| C4 | 470 pF |
| R1 | $270 \Omega$ |
| R2 | $390 \Omega$ |
| R3 | $560 \Omega$ |
| D1 | 1 N 4148 WS |

7. Resistor values calculated to provide $\sim 20 \mathrm{~mA}$ of bias current and $\sim-12 \mathrm{~V}$ reverse bias voltage at the anode of the shunt diode given $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{~V}_{\text {EEB }}=-20 \mathrm{~V}$, voltage drop at driver output $0.4 \mathrm{~V}, \mathrm{~V}_{\mathrm{F}}$ of D 10.7 V and $\mathrm{V}_{\mathrm{F}}$ of switch diodes $\sim 1 \mathrm{~V}$.

## Driver Connections

| DC Control Current (mA) | RF Output State |
| :---: | :---: |
| J2 | J1-J2 |
| -20 | low loss |
| +20 | Isolation |

## Bias Connections ${ }^{6}$


6. RLC values are for an operation frequency of $2-18 \mathrm{GHz}$ and bias current of $\pm 20 \mathrm{~mA}$ per port..

Minimum Reverse Bias Voltage ${ }^{8}$

| Frequency (GHz) | DC Voltage (V) |
| :---: | :---: |
| 0.05 | 17 |
| 2 | 12 |
| 5 | 7 |
| 10 | 4 |
| 15 | 3 |
| 18 | 2 |

8. Minimum DC bias voltage to maintain low loss under 2 W of power with 1.5:1 VSWR.

## Cleanliness

The chips should be handled in a clean environment free of dust and organic contamination.

## Wire / Ribbon Bonding

Thermo compression wedge bonding using $0.003^{\prime \prime} \mathrm{x}$ $0.00025^{\prime \prime}$ ribbon or $0.001^{\prime \prime}$ diameter gold wire is recommended. A work stage temperature of $150^{\circ} \mathrm{C}-200^{\circ} \mathrm{C}$, tool tip temperature of $120^{\circ} \mathrm{C}-150^{\circ}$ and a downward force of 18 to 22 grams should be used. If ultrasonic energy is necessary, it should be adjusted to the minimum level required to achieve a good bond. Excessive power or force will fracture the silicon beneath the bond pad causing it to lift. RF bond wires and ribbons should be kept as short as possible for optimum RF performance.

## Chip Outline Drawings ${ }^{9,10}$


9. Topside and backside metallization is gold, $2.5 \mu \mathrm{~m}$ thick typical.
10. Yellow areas indicate ribbon/wire bonding pads

## Chip Mounting

HMIC switches have Ti-Pt-Au backside metallization and can be mounted using a gold-tin eutectic solder or conductive epoxy. Mounting surface must be free of contamination and flat.

## Eutectic Die Attachment

80/20, gold-tin, solder is recommended. A re-flow oven or hot gas die bonder with a temperature setting of $290^{\circ} \mathrm{C}$ is normally used to melt the solder. The chip should not be exposed to temperatures greater than $320^{\circ} \mathrm{C}$ for more than 20 seconds. Typically no more than three seconds at peak temperature is required for attachment. RoHS compliant solders may also be used but solders rich in tin should be avoided as they will scavenge the backside gold and/or cause gold embrittlement.

## Epoxy Die Attachment

A minimum amount of epoxy, 1-2 mils thick, should be used to attach chip. A thin epoxy fillet should be visible around the outer perimeter of the chip after placement. Epoxy cure time is typically 1 hour at $150^{\circ} \mathrm{C}$.

| Dim. | Inches |  | mm |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min. | Max. | Min. | Max. |  |  |
| A | 0.014 | 0.018 | 0.35 | 0.45 |  |  |
| B | 0.025 | 0.029 | 0.64 | 0.74 |  |  |
| C | 0.008 Ref. |  | 0.20 Ref. |  |  |  |
| D | 0.004 |  | 0.006 | 0.10 |  | 0.15 |
| E | 0.004 Ref. |  | 0.10 Ref. |  |  |  |
| F | 0.003 Ref. |  | 0.08 Ref. |  |  |  |
| G | 0.003 Ref. |  | 0.08 Ref. |  |  |  |
| H | 0.020 Ref. |  | 0.52 Ref. |  |  |  |

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[^0]:    * Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

