## Features

- Broad Bandwidth Specified 2-18 GHz
- Usable up to 26 GHz
- Integrated Bias Network
- Lower Insertion Loss / Higher Isolation
- Fully Monolithic, Glass Encapsulated Chip
- Up to +33 dBm CW Power Handling @ $+25^{\circ} \mathrm{C}$
- RoHS* Compliant


## Description

The MA4SW210B-1 (SP2T) and MA4SW310B-1 (SP3T) broadband switches with an integrated bias networks utilizing MACOM's $\mathrm{HMIC}^{\text {TM }}$ (Heterolithic Microwave Integrated Circuit) process, US Patent $5,268,310$. This process allows the incorporation of silicon pedestals that form series and shunt diodes or vias by imbedding them in low loss, low dispersion glass. By using small spacing between circuit elements, this combination of silicon and glass gives HMIC devices low loss and high isolation performance with exceptional repeatability through low millimeter frequencies.

The top side of the chip is protected by a polymer coating for manual or automatic handling and large gold bond pads help facilitate connection of low inductance ribbons. The gold metallization on the backside of the chip allows for attachment via 80/20 (gold/tin) solder or conductive silver epoxy.

## Ordering Information

| Part Number | Package |
| :---: | :--- |
| MA4SW210B-1 | Gel Pack |
| MA4SW310B-1 | Gel Pack |

## Functional Diagrams ${ }^{1}$

MA4SW210B-1 (SP2T)


MA4SW310B-1 (SP3T)


1. Yellow areas indicate ribbon/wire bonding pads
[^0]Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}^{\boldsymbol{\circ}} \mathrm{C}, \mathbf{2 0} \mathbf{~ m A}$
MA4SW210B-1 (SPDT)

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 2 GHz |  |  | 1.5 | 1.8 |
| Insertion Loss | 6 GHz | dB | - | 0.7 | 1.0 |
|  | 12 GHz |  |  | 0.9 | 1.2 |
|  | 18 GHz |  | 5 | 1.8 |  |
| Isolation | 2 GHz | dB | 47 | 60 |  |
|  | 6 GHz | 40 | 45 | - |  |
|  | 12 GHz |  | 36 | 40 |  |
| Input Return Loss | 18 GHz | dB | - | 14 | 15 |
|  | 2 GHz |  |  | 15 | - |
| Switching Speed ${ }^{2}$ | 6 GHz | ns | - | 50 | - |

## MA4SW310B-1 (SP3T)

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | 2 GHz |  |  | 1.6 | 2.0 |
| Insertion Loss | 6 GHz | dB | - | 0.8 | 1.1 |
|  | 12 GHz |  |  | 1.0 | 1.3 |
|  | 18 GHz |  | 5 | 1.9 |  |
| Isolation | 2 GHz | dB | 47 | 59 |  |
|  | 6 GHz | 40 | 45 | - |  |
|  | 12 GHz |  | 36 | 40 |  |
| Switching Speed ${ }^{2}$ | 2 GHz | dB | - | 14 |  |
|  | 6 GHz |  |  | 16 | - |
|  | 12 GHz | ns | - | 50 | - |

1. Typical switching speed is measured from ( $10 \%$ to $90 \%$ and $90 \%$ to $10 \%$ of detected RF voltage), driven by TTL compatible drivers. In the modulating state, (the switching port is modulating, all other ports are in steady state isolation.) The switching speed is measured using an $R C$ network using the following values: $R=50-200 \Omega, C=390-1000 \mathrm{pF}$. Driver spike current, $\mathrm{I}_{\mathrm{C}}=\mathrm{C}$ dv/dt, ratio of spike current to steady state current, is typically 10:1.

## Absolute Maximum Ratings ${ }^{3,4,5}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| RF CW Incident Power | +33 dBm |
| Reverse Voltage | -50 V |
| Bias Current per Port | $\pm 50 \mathrm{~mA} @+25^{\circ} \mathrm{C}$ |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Junction Temperature | $+175^{\circ} \mathrm{C}$ |

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. MACOM does not recommend sustained operation near these survivability limits.
5. Maximum operating conditions for a combination of RF power, DC bias and temperature: +33 dBm CW @ 15 mA (per diode) @ $+85^{\circ} \mathrm{C}$.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) and Class C1 (CDM).devices.

## Cleanliness

The chips should be handled in a clean environment free of dust and organic contamination.

## Wire / Ribbon Bonding

Thermo compression wedge bonding using 0.003 " x 0.00025 " ribbon or 0.001 " diameter gold wire is recommended. A work stage temperature of $150^{\circ} \mathrm{C}-200^{\circ} \mathrm{C}$, tool tip temperature of $120^{\circ} \mathrm{C}-150^{\circ}$ and a downward force of 18 to 22 grams should be used. If ultrasonic energy is necessary, it should be adjusted to the minimum level required to achieve a good bond. Excessive power or force will fracture the silicon beneath the bond pad causing it to lift. RF bond wires and ribbons should be kept as short as possible for optimum RF performance.

## Chip Mounting

HMIC switches have Ti-Pt-Au backside metallization and can be mounted using a gold-tin eutectic solder or conductive epoxy. Mounting surface must be free of contamination and flat.

## Eutectic Die Attachment

An 80/20, gold-tin, eutectic solder is recommended. Adjust the work surface temperature to $255^{\circ} \mathrm{C}$ and the tool tip temperature to $265^{\circ} \mathrm{C}$. After placing the chip onto the circuit board re-flow the solder by applying hot forming gas ( $95 / 5 \mathrm{Ni} / \mathrm{H}$ ) to the top surface of the chip. Temperature should be approximately $290^{\circ} \mathrm{C}$ and not exceed $320^{\circ} \mathrm{C}$ for more than 20 seconds. Typically no more than three seconds is necessary for attachment. Solders rich in tin should be avoided

## Epoxy Die Attachment

A minimum amount of epoxy, 1-2 mils thick, should be used to attach chip. A thin epoxy fillet should be visible around the outer perimeter of the chip after placement. Epoxy cure time is typically 1 hour at $150^{\circ} \mathrm{C}$.

## Typical RF Performance at $\mathrm{T}_{\mathrm{AMB}}=\boldsymbol{+ 2 5 ^ { \circ }} \mathbf{C}, 20 \mathrm{~mA}$ Bias Current

ISOLATION vs FREQUENCY MA4SW210B-1


INSERTION LOSS vs FREQUENCY MA4SW210-B1


ISOLATION vs FREQUENCY MA4SW310B-1


FREQUENCY, GHz

## INSERTION LOSS vs FREQUENCY

 MA4SW310B-1

FREQUENCY, GHz

RETURN LOSS vs FREQUENCY
MA4SW310B-1


MACOM.

HMIC $^{\text {TM }}$ Silicon PIN Diode Switches with Integrated Bias Network


## Operation of MA4SWx10B-1 Series

Operation of the MA4SWx10B-1 series PIN diode switches is achieved by simultaneous application of DC currents to the bias pads. The required levels for the different states are shown in the tables below. The control currents should be supplied by constant current sources. The nominal 40-60 $\Omega$ pull-up resistor voltage @ J4 and J 5 is usually -1 V for -20 mA and +20 mA for +1 V .

## Driver / Bias Connections

## MA4SW210B-1 (SP2T)

| DC Control Current (mA) |  | RF Output States |  |
| :---: | :---: | :---: | :---: |
| J4 | J5 | J1-J2 | J1-J3 |
| -20 | +20 | low loss | Isolation |
| +20 | -20 | Isolation | low loss |



MA4SW310B-1 (SP3T)

| DC Control Current (mA) |  |  |  | RF Output States |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| J5 | J6 | J7 | J1-J2 | J1-J3 | J1-J4 |  |
| -20 | +20 | +20 | low loss | Isolation | Isolation |  |
| +20 | -20 | +20 | Isolation | low loss | Isolation |  |
| +20 | +20 | -20 | Isolation | Isolation | low loss |  |



## Chip Outline Drawings ${ }^{7,8}$

MA4SW210B-1


| DIM | INCHES |  | MM |  |
| :---: | :---: | :---: | :---: | :---: |
|  | MIN. | MAX. | MIN. | MAX. |
| A | 0.066 | 0.070 | 1.680 | 1.780 |
| B | 0.048 | 0.052 | 1.230 | 1.330 |
| C | 0.004 | 0.006 | 0.100 | 0.150 |
| D | 0.004 | 0.006 | 0.090 | 0.140 |
| E | 0.012 | 0.013 | 0.292 | 0.317 |
| F | 0.029 | 0.030 | 0.735 | 0.760 |
| G | 0.030 | 0.031 | 0.766 | 0.791 |
| H | 0.029 | 0.030 | 0.732 | 0.757 |
| J | 0.005 | REF. | 0.129 | REF. |
| K | 0.005 | REF. | 0.129 | REF. |

MA4SW310B-1

7. Topside and backside metallization is gold, $2.5 \mu \mathrm{~m}$ thick typical.
8. Yellow areas indicate ribbon/wire bonding pads

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[^0]:    *Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

