Ultra small Broadband General Purpose Amplifier 4 - 20 GHz

### Features

- Gain: 16 dB
- Flatness: ± 2 dB
- 50 Ω match in and out
- P1dB: +18 dBm @ 14 GHz
- Single DC supply, +5 V to +12 V, 45 mA
- Lead-Free 1.5 x 1.2 mm 6-Lead TDFN package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

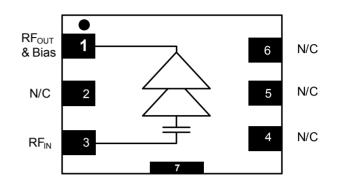
### Description

The MAAM-011101 operates from 4 to 20 GHz and features 16 dB typical gain and +18 dBm of output power. The input and output are fully matched to 50  $\Omega$  with a typical return loss better than 12 dB. Small signal linearity is typically +30 dBm and reverse isolation better than 28 dB. This device requires a minimum of +5V, typically +8V, and maximum +10V for standard operation. Typical current is 45 mA.

Typical usage is a system buffer amplifier, gain block, mixer LO driver, power amplifier driver requiring small size and high performance. Typical applications are for WiFi, WiMAX, Point-to-Point radios, IMS, EW, and Aerospace and Defense.

The MAAM-011101 is housed in a leadless  $1.5 \times 1.2$  mm package that is small yet can be handled and placed with standard pick and place assembly equipment. It is fabricated using a GaAs process which features full passivation for increased performance and reliability.

### **Functional Schematic**



### **Pin Configuration**

Pin No.	Pin Name	Description	
1	RF <sub>OUT</sub>	RF Output & Bias (Vd)	
2	N/C	No Connection	
3	RF <sub>IN</sub>	RF Input	
4	N/C	No Connection	
5	N/C	No Connection	
6	N/C	No Connection	
7 <sup>3</sup>	Paddle	GND	

3. The exposed pad centered on the package bottom must be connected to RF and DC ground.

#### **Ordering Information**<sup>1,2</sup>

Part Number	Package		
MAAM-011101-TR1000	1000 Piece Reel		
MAAM-011101-001SMB	Sample Test Board		

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

\*Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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#### Electrical Specifications: $T_A = +25^{\circ}C$ , $V_D = +8$ Volts, $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	4 GHz 8 GHz 12 GHz 16 GHz 20 GHz	GHz dB GHz dB		13 19 16 15 15	
Noise Figure	4 - 20 GHz	dB		4	_
Input Return Loss	6 - 18 GHz	dB		12	—
Output Return Loss	6 - 18 GHz	dB		14	—
Isolation	4 - 20 GHz	dB		30	—
P1dB	4 GHz 8 GHz 12 GHz 16 GHz 20 GHz	dBm	 +16  	+15 +17 +19 +19 +18	
I <sub>DD</sub>	+8 Volts	mA	35	45	55

#### Absolute Maximum Ratings<sup>4,5,6</sup>

Parameter	Absolute Maximum		
RF Input Power	+23 dBm		
Voltage	+12 volts		
Operating Temperature	-40°C to +85°C		
Junction Temperature <sup>7</sup>	+150°C		
Storage Temperature	-65°C to +150°C		

- 4. Exceeding any one or combination of these limits may cause permanent damage to this device.
- 5. MACOM does not recommend sustained operation near these survivability limits.
- 6. Operating at nominal conditions with  $T_J \le +150^{\circ}C$  will ensure MTTF > 1 x 10<sup>6</sup> hours.
- 7. Junction Temperature  $(T_J) = T_C + \Theta_{JC} * ((V * I) (P_{OUT} P_{IN}))$ Typical thermal resistance  $(\Theta_{JC}) = 40^{\circ}C/W$ 
  - a) For  $T_c = 25^{\circ}C$ ,
  - $T_J$  = +43°C @ +10 V, 45 mA, P<sub>OUT</sub> = -4 dBm, P<sub>IN</sub> = -20 dBm b) For T<sub>C</sub> = 85°C,
  - T<sub>J</sub> = +103°C @ +10 V, 45 mA, P<sub>OUT</sub> = -3 dBm, P<sub>IN</sub> = -20 dBm

#### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A devices.

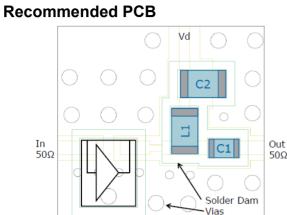
<sup>2</sup> 

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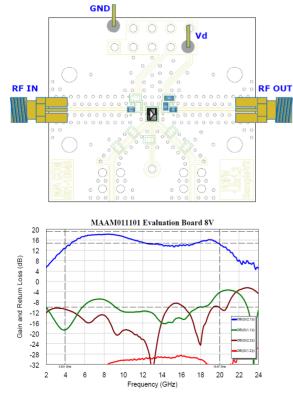
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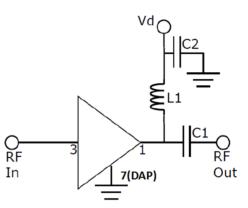
#### **Parts List**

Comp.	Value	Pkg.	Manf.	Purpose
C1	100 pF	0201	Murata GRM0335C1E101	DC Block
C2	100 pF	0402	Murata GRM1555C1E101	Bypass
L1	470 Ω	0402	Murata BLM15GG471	Choke

## **Evaluation Board**



### **Application Schematic**



## **Application Information**

The MAAM-011101 is designed to be easy to use yet high performance. The ultra small size, no matching, and simple bias allows easy placement on any system board.

#### LO Buffer applications:

The MAAM-011101 is good as a LO buffer since it has excellent isolation, selectable power output, low phase noise, and 50  $\Omega$  match (even under heavy drive). It is designed to deliver saturated output levels up to +20 dBm common to driving mixer configurations. It is typically used in conjunction with filters or splitters after the VCO or PLL.

#### PA Driver applications:

The MAAM-011101 makes a very good low cost driver before the transmit power amplifier. Set typically 7 dB backed off P1dB as a linear driver, it still delivers up to +12 dBm. Often cascaded in series with an attenuator, it allows gain control with little pulling due to mis-match. The low gain expansion allows little AM-to-AM distortion.

#### Grounding:

It is recommended that the total ground (common mode) inductance not exceed 0.03 nH (30 pH). This is equivalent to at least four 8 mil (200 u) vias per 8 mil board (200 u) be place under the device to ground

#### DC Bias Tee:

To bias properly, a DC voltage must be applied at the output pin. Typically this is down with a 2 element bias network that consists of a choke and a DC blocking capacitor. We recommend a high Q inductor for the choke and quality capacitor for the DC block.

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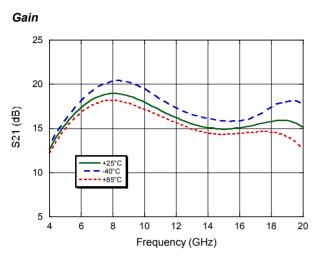
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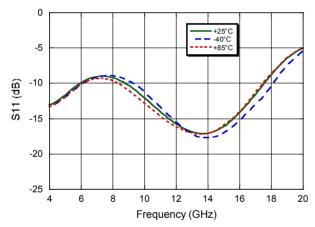
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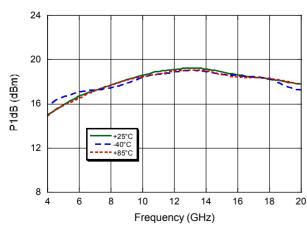
### Typical Performance Curves over temperature, $V_D$ = +8 V, $Z_0$ = 50 $\Omega$

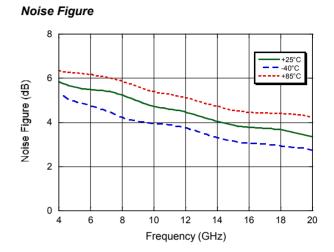


Input Return Loss

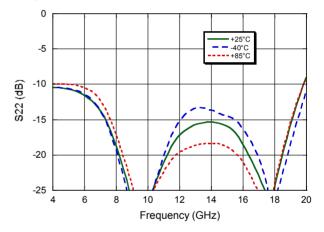


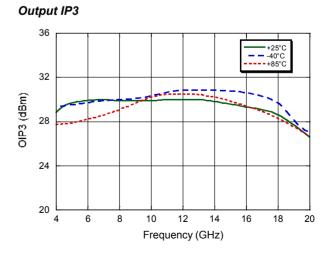
Output P1dB





**Output Return Loss** 





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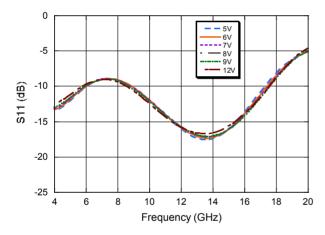
Typical Performance Curves over supply voltage,  $T_A = +25^{\circ}C$ ,  $Z_0 = 50 \Omega$ 

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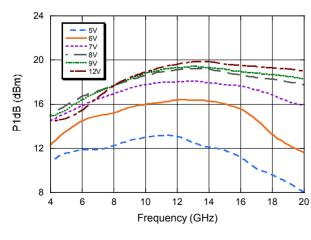
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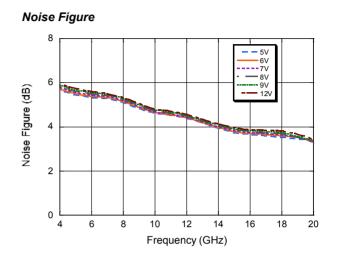
#### Gain 25 5V 6V •7V • 8V 20 av 90 12 S21 (dB) 15 10 5 6 8 10 12 14 16 18 20 4 Frequency (GHz)

Input Return Loss

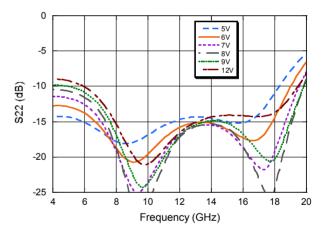


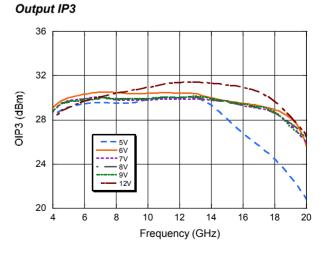
Output P1dB





**Output Return Loss** 





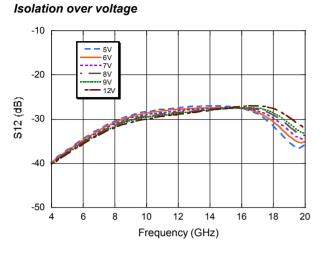
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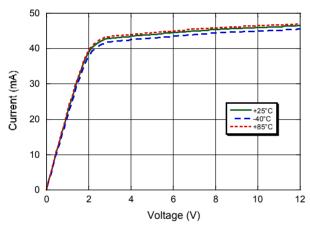
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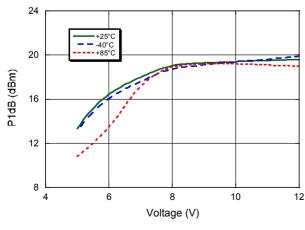
## **Typical Performance Curves**



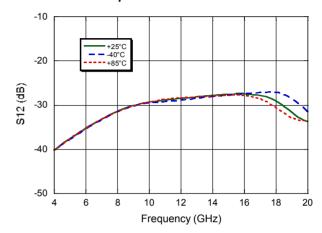
Current vs. Voltage over temperature



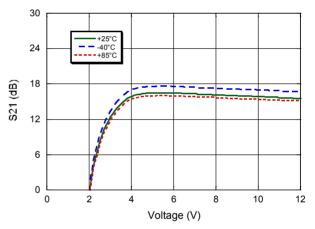


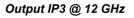


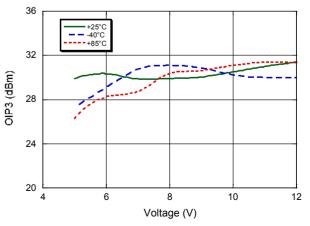
Isolation over temperature



Gain vs. Voltage over temperature @ 12 GHz







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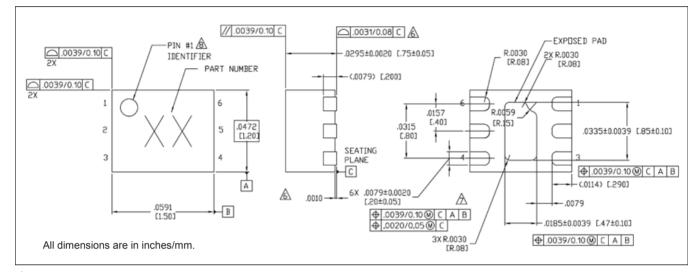
<sup>6</sup> 





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### Lead-Free 1.5 x 1.2 mm 6-Lead TDFN<sup>†</sup>



 Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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