

S-Band PIN Diode Limiter

2 - 4 GHz



MADL-011173-DIE

Rev. V1

Features

- Low Insertion Loss: < 0.5 dB
- Return Loss: > 20 dB
- Handles 41 dBm CW Power
- Low Flat Leakage Power: < 18 dBm
- Die Size: 2.79 x 2.64 mm
- RoHS* Compliant
- Passive Device

Applications

- ISM
- Multi Market
- Radar
- EW

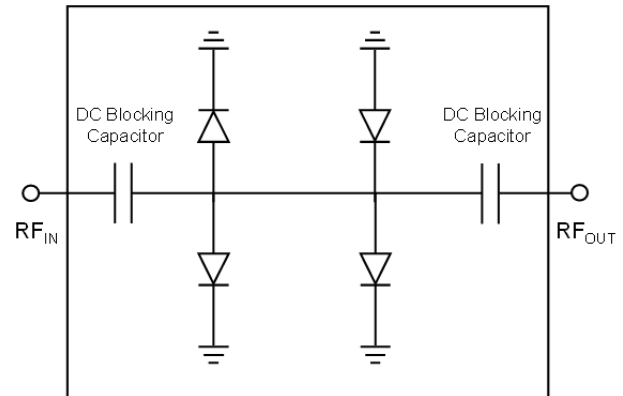
Description

The MADL-011173-DIE is an integrated AlGaAs PIN Diode limiter. It is a passive device, DC blocked at both the input and output ports.

The limiter is ideally designed to provide low insertion loss, at zero bias, as well as low flat leakage power with fast signal response/recovery times. It can limit up to 41 dBm incident CW power at room temperature. It is available in die form with a compact die dimension of 2.79 x 2.64 mm.

The MADL-011173 PIN limiter is designed for use in passive limiter control circuits to protect sensitive receiver components such as low noise amplifiers (LNA), detectors, and mixers.

Functional Schematic



Pin Configuration

| Pin # | Pin Name | Description |
|-------|-------------------|-------------|
| 1 | RF _{IN} | RF Input |
| 2 | RF _{OUT} | RF Output |
| 3 - 6 | GND | GND |

Ordering Information

| Part Number | Package |
|-----------------|-----------------|
| MADL-011173-DIE | Die in Gel Pack |

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

Electrical Specifications: $T_A = 25^\circ\text{C}$, $Z_0 = 50 \Omega$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
|---------------------------------------|--|-------|------|------|------------|
| Insertion Loss | $P_{IN} = -10 \text{ dBm}$ 2 GHz 4 GHz | dB | — | 0.5 | 0.7 0.8 |
| Input Return Loss | $P_{IN} = -10 \text{ dBm}$ 2 - 4 GHz | dB | — | 20 | — |
| Output Return Loss | $P_{IN} = -10 \text{ dBm}$ 2 - 4 GHz | dB | — | 20 | — |
| Max CW Incident Power | 2 - 4 GHz | dBm | — | 41 | — |
| CW Flat Leakage | $P_{IN} > 27 \text{ dBm}$ 2 GHz 4 GHz | dBm | — | 15 | 16 15 |
| Spike Leakage Power | $P_{IN} = 42 \text{ dBm}$, 100 μs , 1% DC 2 - 4 GHz | dBm | — | 16 | — |
| Recovery Time (1dB Insertion Loss) | $P_{IN} = 42 \text{ dBm}$, 100 μs , 1% DC 2 - 4 GHz | ns | — | 80 | — |
| Input IP3 | 10 MHz Offset, $P_{IN}/\text{tone} = 0 \text{ dBm}$ | dBm | — | 33 | — |

Absolute Maximum Ratings^{1,2}

| Parameter | Absolute Maximum |
|---|------------------|
| Incident CW RF Power at +25°C | 41.3 dBm |
| Peak Incident Power 1 μs pulse, 1% D.C. at +25°C | 45 dBm |
| Junction Temperature ³ | +150°C |
| Operating Temperature | -40°C to +85°C |
| Storage Temperature | -55°C to +150°C |

1. Exceeding any one or combination of these limits may cause permanent damage to this device.
2. MACOM does not recommend sustained operation near these survivability limits.
3. Operating at nominal conditions with $T_J \leq +150^\circ\text{C}$ will ensure MTTF > 1×10^6 hours.

Handling Procedures

The protective polymer coating on the active areas of the die provides scratch and impact protection, particularly for the metal air bridge, which contacts the diode's anode. Die should primarily be handled with vacuum pickup tools, or alternatively with plastic tweezers.

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1B devices.

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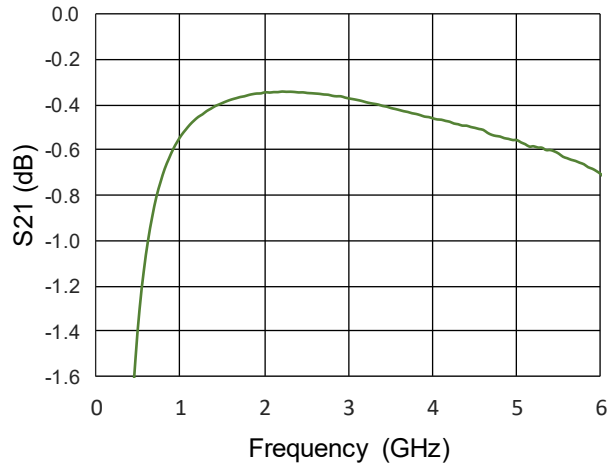


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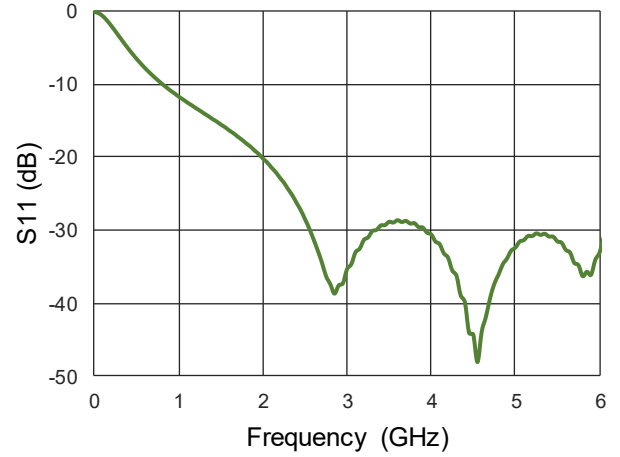
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Typical Small-Signal Performance, Die On-Wafer: $T_A = 25^\circ\text{C}$, $Z_0 = 50 \Omega$

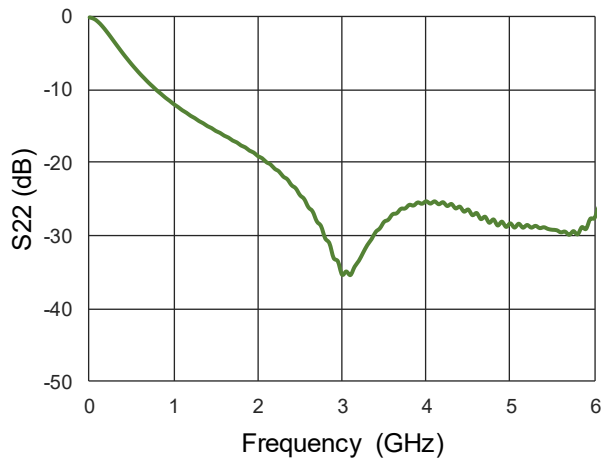
Insertion Loss



Input Return Loss



Output Return Loss



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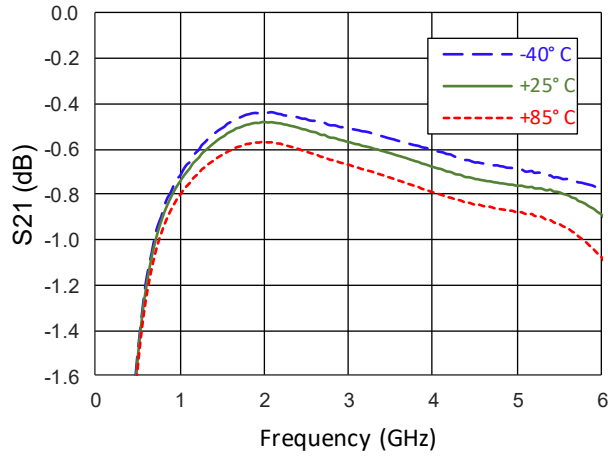


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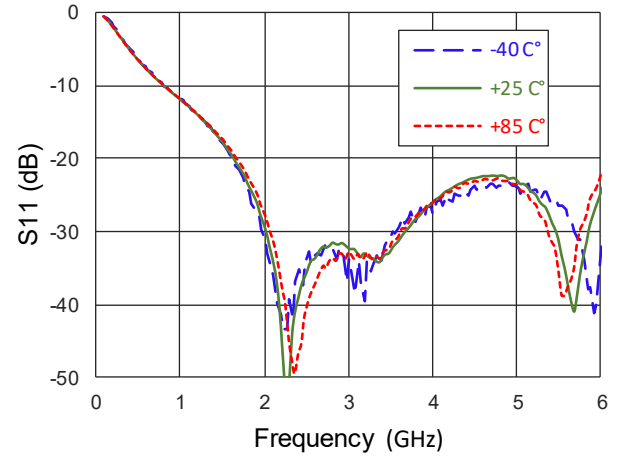
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Typical Small-Signal Performance, Die On-Board: $T_A =$ Over Temperature, $Z_0 = 50 \Omega$

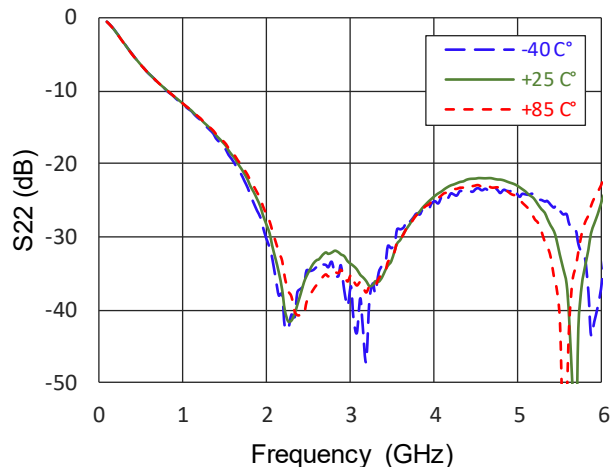
Insertion Loss



Input Return Loss



Output Return Loss



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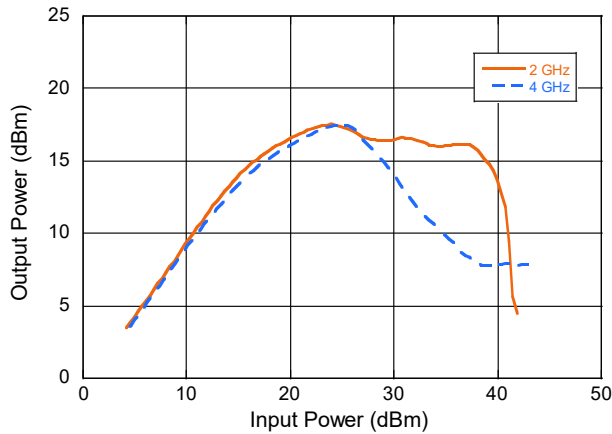


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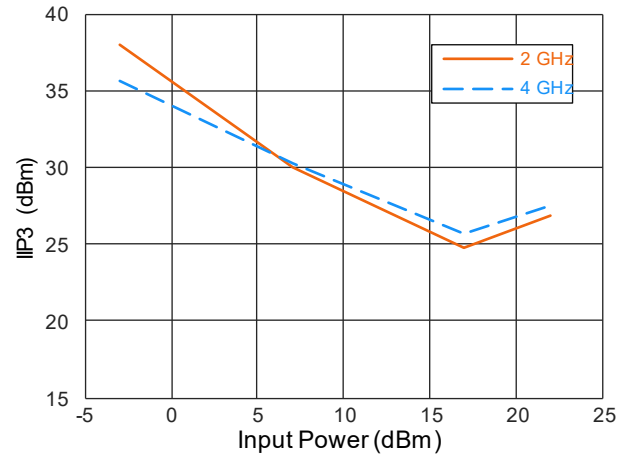
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Typical RF Power Performance, Die On-Board: $Z_0 = 50 \Omega$

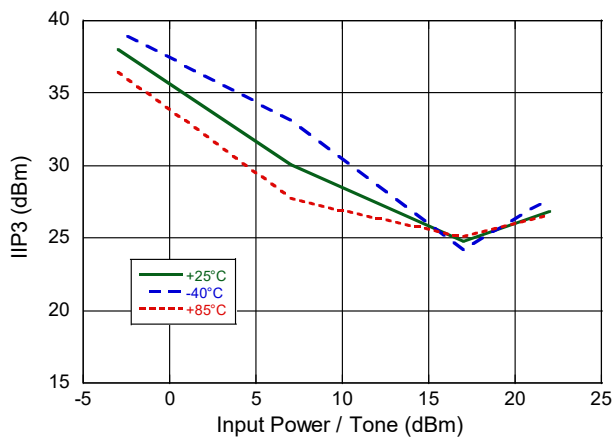
CW Flat Leakage Power @ $T_A = 25^\circ\text{C}$



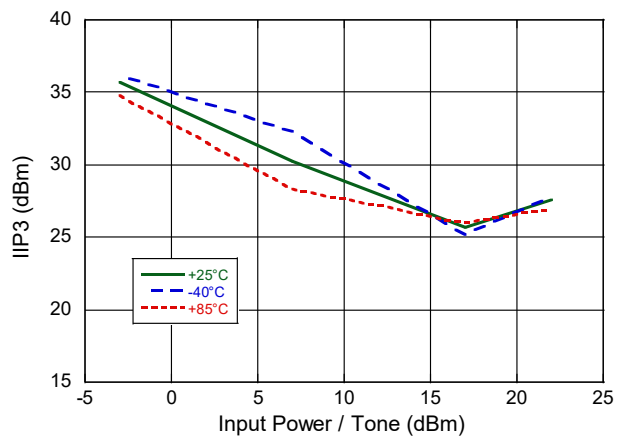
Input IP3 @ $T_A = 25^\circ\text{C}$



Input IP3 over Temperature @ 2 GHz



Input IP3 over temperature @ 4 GHz



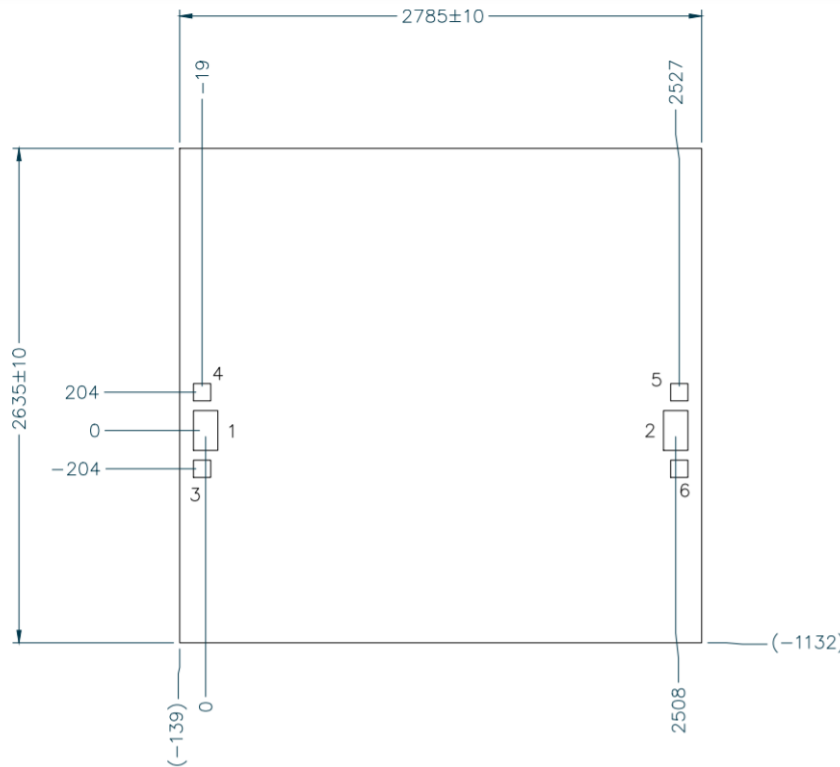
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Die Outline Drawing



| BOND PAD DIM (μm) | | |
|--------------------------------|-----|-----|
| PAD | X | Y |
| 1,2 | 130 | 212 |
| 3,4,5,6 | 92 | 92 |

NOTES:

1. UNLESS OTHERWISE SPECIFIED, ALL DIMENSIONS SHOWN ARE μm WITH A TOLERANCE OF $\pm 5\mu\text{m}$.
2. DIE THICKNESS IS $100 \pm 5\mu\text{m}$
3. BOND PAD/BACKSIDE METALLIZATION: GOLD.

Recommended Die Attachment

The die edge to die attach pad edge is recommended to be 5 mils minimum. High density solid Cu via farm or solid Cu heat slug is recommended under the attach pad for optimum thermal heat dissipation.

Eutectic die attachment is not recommended for this part. A high thermal conductivity epoxy shall be used. Voiding under the die should be minimized and no voiding should be present under the diode locations.

Wire Bonding Recommendation

For optimum bonding power handling performance and minimum bonding inductance, it is recommended to bond this part with two 3 x 0.5 mil gold ribbon wires on both the input and output RF pads. Low loop profile and minimum bond length are recommended.

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