MAMG-001214-090PSM

L-Band 90 W 2-Stage Fully Matched GaN Module
Surface Mount Laminate Package

Rev. V3

Features

- Compact Size (14x24 mm^2)
- GaN on SiC D-Mode Transistor Technology
- Fully Matched, De-Coupled DC and RF
- Typical Bias: 45 V, Class AB
- Intended for Pulsed RADAR Applications
- Output Power > 90 W, with 30 dB Gain and 60% Power Added Efficiency
- Up to 3 ms Pulse Width and 10% Duty Cycle
- MTTF = 600 years (T_J < 200°C)
- Thermally Enhanced Laminate LGA Package
- RoHS* Compliant. Lead Free Reflow Compatible
- MSL-3

Description

The MAMG-001214-090PSM is a 2-stage GaN power module in a "True SMT" laminate package. The module is fully matched. Under pulsed conditions, it can deliver output power greater than 90 W, with 30 dB typical associated gain and 60% typical power added efficiency.

Flexible design allows for gate and/or drain pulsing. Additional features include a gate voltage sense port for use in temperature compensation or pulse droop compensation. The overall package size is very small, only 14x24 mm^2. The module's compact size, combined with excellent RF performance makes this product an ideal solution for pulsed RADAR applications where low cost, light weight and small size are the key.

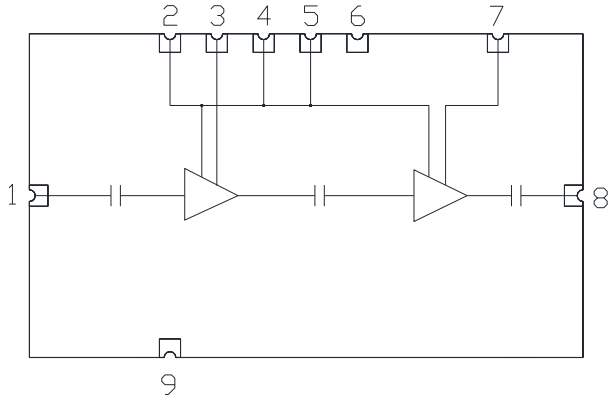
Ordering Information[1]

Part Number	Package
MAMG-001214-090PSM	Bulk Packaging
MAMG-0T1214-090PSM	100 Piece Reel
MAMG-L21214-090PSM	Evaluation Board[2]

1. Reference Application Note M513 for reel size information.
2. Includes one module surface-mounted onto board.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

Functional Schematic



Pin Configuration

Pin No.	Function
1	RF IN
2	VG [3]
3	VD1
4	NC [4]
5	VG sense [5]
6	Ground
7	VD2
8	RF OUT
9	NC [4]

3. One common gate voltage for both stages in the module.
4. Do not connect.
5. Do not connect to ground if not used.

Electrical Specifications [6]

Parameter	Symbol	Min.	Typ.	Max.	Units
RF FUNCTIONAL TESTS: Freq. = 1200-1400 MHz, V_{DD} = 45 V, I_{DQ} = 300 mA, T_A = 25°C, Z_L = 50 Ω, Pulse Width = 1 ms, Duty Cycle = 10%, P_{IN} = 19 dBm					
Peak Output Power [7]	P_{OUT}	49.1	49.8	-	dBm
		82	99	-	W
Power Gain	G_P	30.1	30.9	-	dB
Power Added Efficiency	PAE	56	60	-	%
Pulse Droop [8]	Droop	-	0.3	0.4	dB
2nd Harmonic	2F0	-	-40	-	dBc
3rd Harmonic	3F0	-	-52	-	dBc
Load Mismatch Stability	VSWR-S	-	5:1	-	-
Load Mismatch Tolerance	VSWR-T	-	6:1	-	-

6. Typical RF performance measured in RF evaluation board (see layout on page 4).
7. Peak output power measured at center of pulse.
8. Pulse droop measured between 10% and 90% of pulse.

Absolute Maximum Ratings [9,10,11,12,13]

Parameter	Absolute Maximum
Input Power	26 dBm
Drain Supply Voltage (pulsed), V_{DD}	+55 V
Gate Supply Voltage Range, V_{GG}	-9 V to -2.5 V
Supply Current, I_{DD}	4.0 A
Power Dissipation, Pulsed Mode @ 85ºC	80 W
Junction Temperature[14]	200 °C
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C
ESD Maximum - Human Body Model (HBM)	600 V
ESD Maximum - Charged Device Model (CDM)	300 V

9. Exceeding any one or combination of these limits may cause permanent damage to this device.
10. MACOM does not recommend sustained operation near these survivability limits.
11. For saturated performance it is recommended that the sum of $(3 * V_{DD} + \text{abs}(V_{GG})) \leq 175$ V.
12. CW operation is not recommended.
13. Operating at nominal conditions with $T_J \leq 200$°C will ensure MTTF > 1×10^6 hours. Junction temperature directly affects device MTTF and should be kept as low as possible to maximize lifetime.
14. Junction Temperature $(T_J) = T_C + \Theta_{JC} * ((V * I) - (P_{OUT} - P_{IN}))$.
    Typical Transient Thermal Resistance Θ_{JC} = 1.7 °C/W (3 ms pulses, 10% duty cycle)

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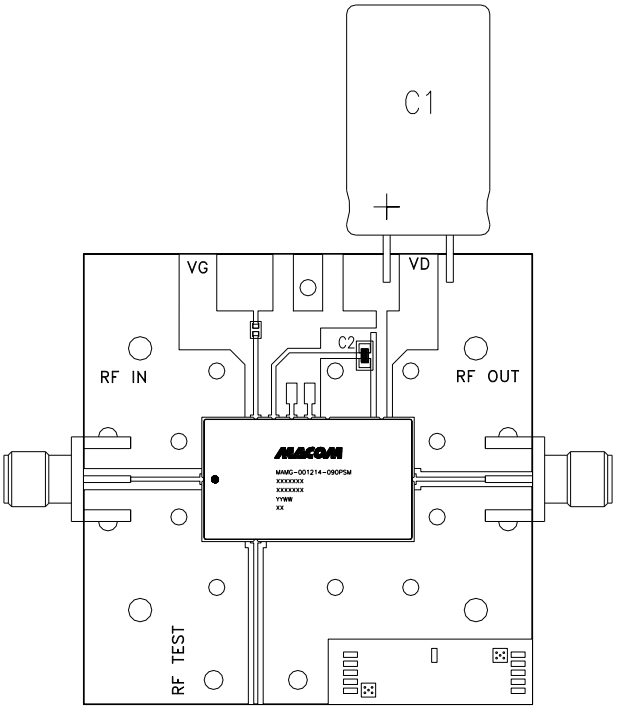
Evaluation Board Outline



Parts List

Part	Value	Case Style
C1	100 µF	Radial
C2	10 nF	0603

Parts are measured and sampled in the evaluation board shown on the left. The board is made of 8-mil thick RO4003C and is bolted onto a Ni-plated Aluminum plate. Electrical and thermal ground is provided using a Cu-filled via-hole array (pictured below). Very few external components are used, as DC blocks are not required.

Bias Sequencing

Turning the device ON

1. Set V_G to the pinch-off value (V_P), typically -6 V.
2. Turn on V_D to nominal voltage (50 V).
3. Increase V_G to desired quiescent current.
4. Apply RF power to desired level.

Turning the device OFF

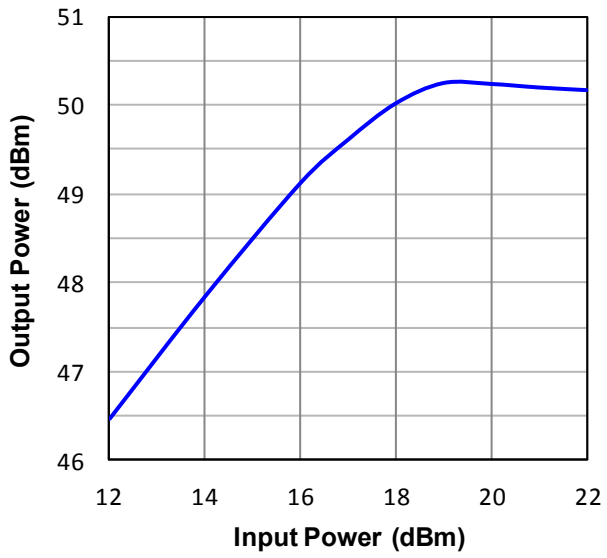
1. Turn off RF power.
2. Decrease V_G down to V_P.
3. Turn off V_D.
4. Turn off V_G.

Applications Section

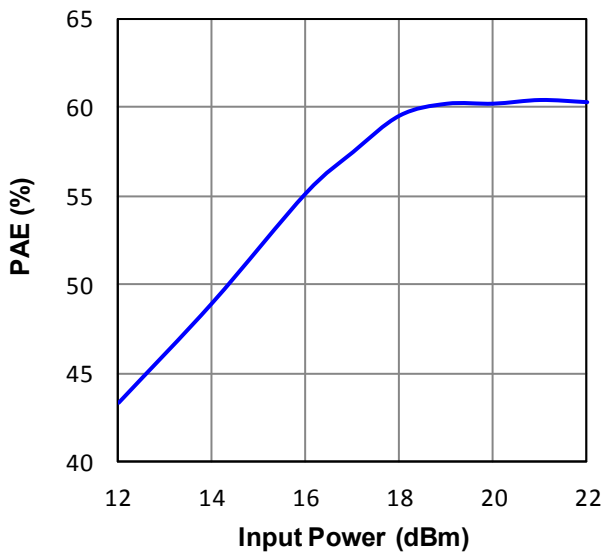
Typical Large-Signal Performance Curves:

Freq. = 1.3 GHz, 1 ms Pulses, 10% Duty Cycle, V_{DD} = 45 V, I_{DQ} = 300 mA, T_A = 25°C

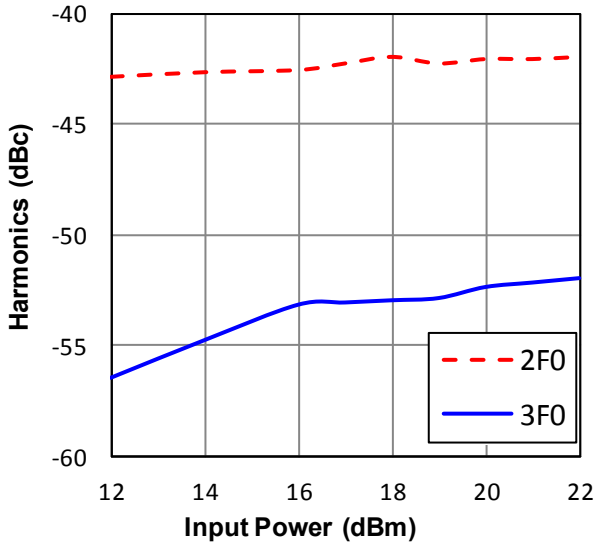
Output Power vs. Input Power



Power Added Efficiency vs. Input Power



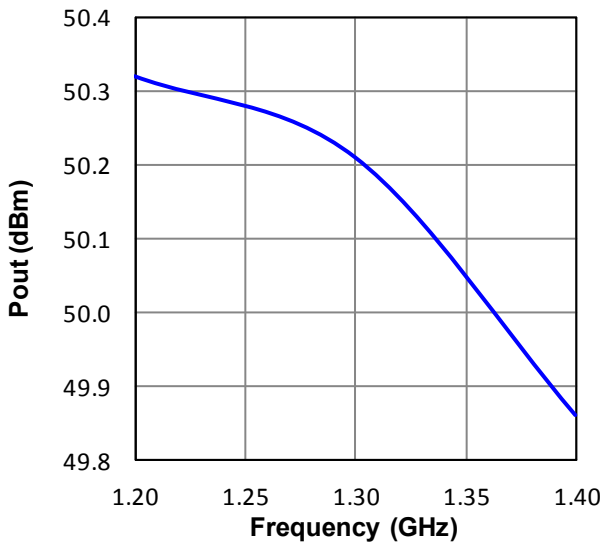
2F0 and 3F0 Harmonics vs. Input Power



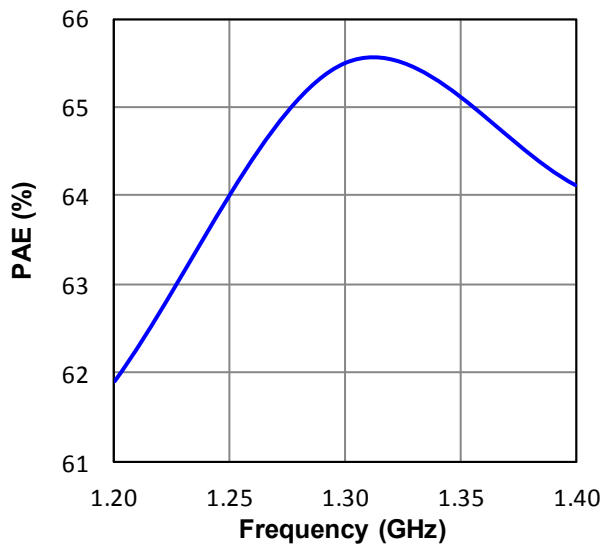
Applications Section

Typical Large-Signal Performance Curves Over Frequency:
1 ms Pulses, 10% Duty Cycle, V_{DD}= 45 V, I_{DQ} = 300 mA, T_A = 25°C

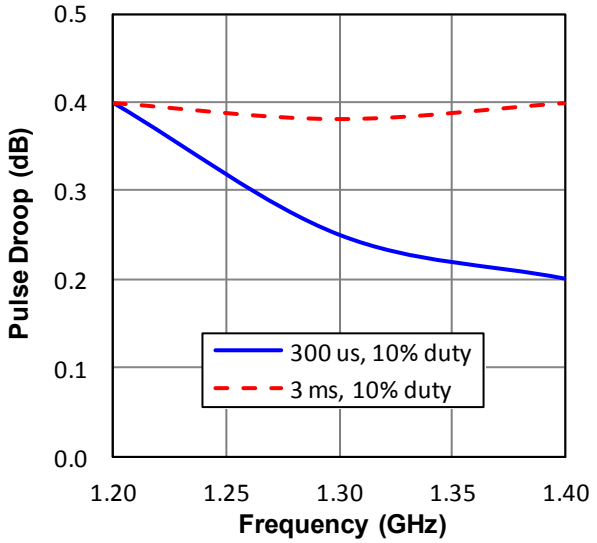
Output Power vs. Frequency



Power Added Efficiency vs. Frequency



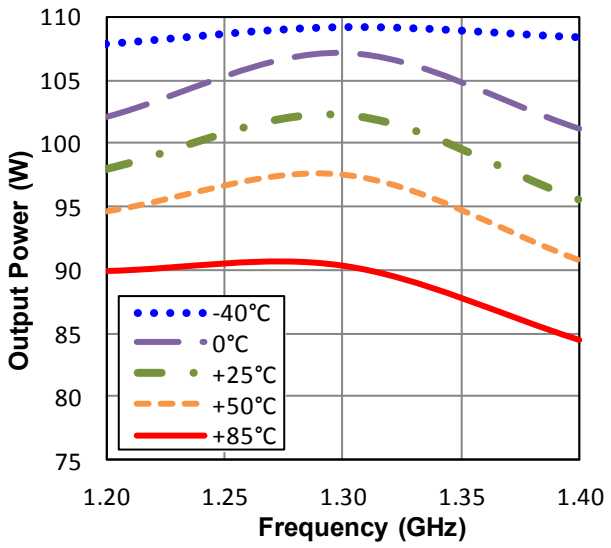
Pulse Droop vs. Frequency



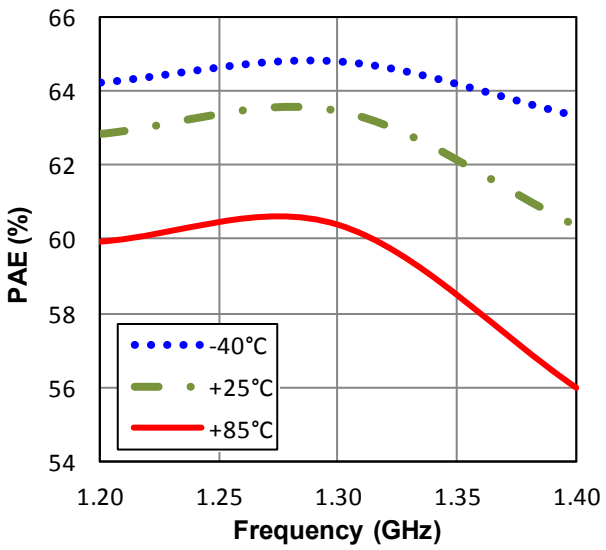
Applications Section

Typical Large-Signal Performance Curves Over Temperature: 1 ms Pulses, 10% Duty Cycle, V_{DD}= 45 V, I_{DQ} = 300 mA

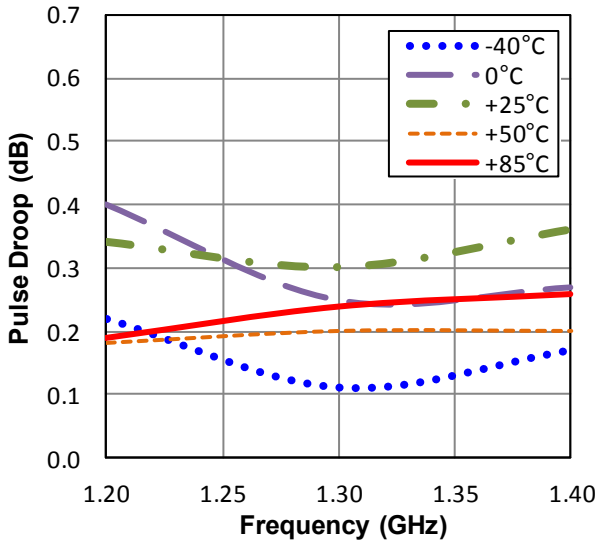
Output Power vs. Frequency



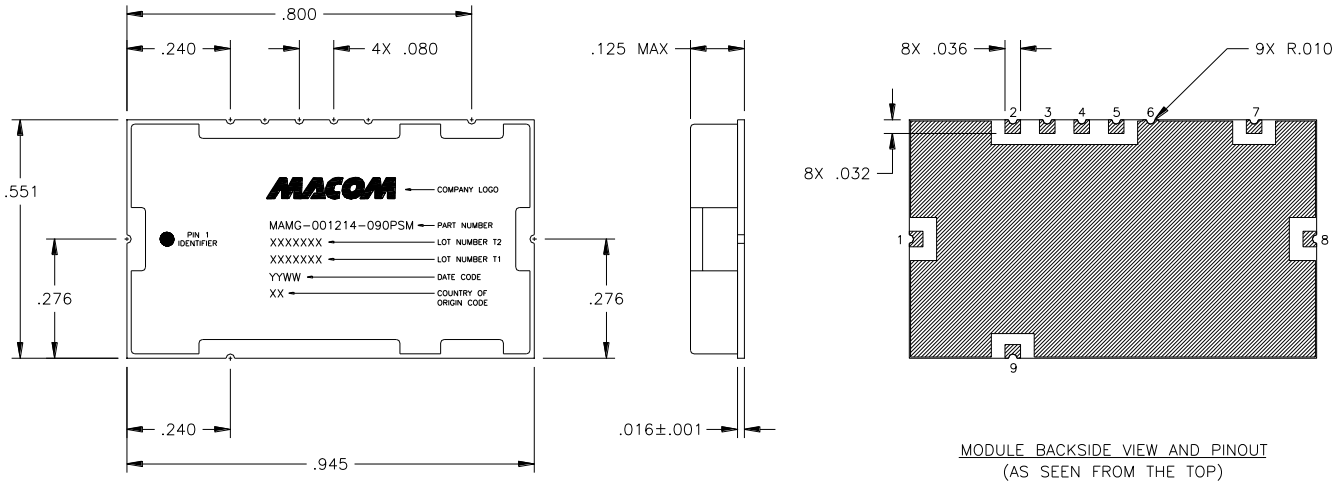
PAE vs. Frequency



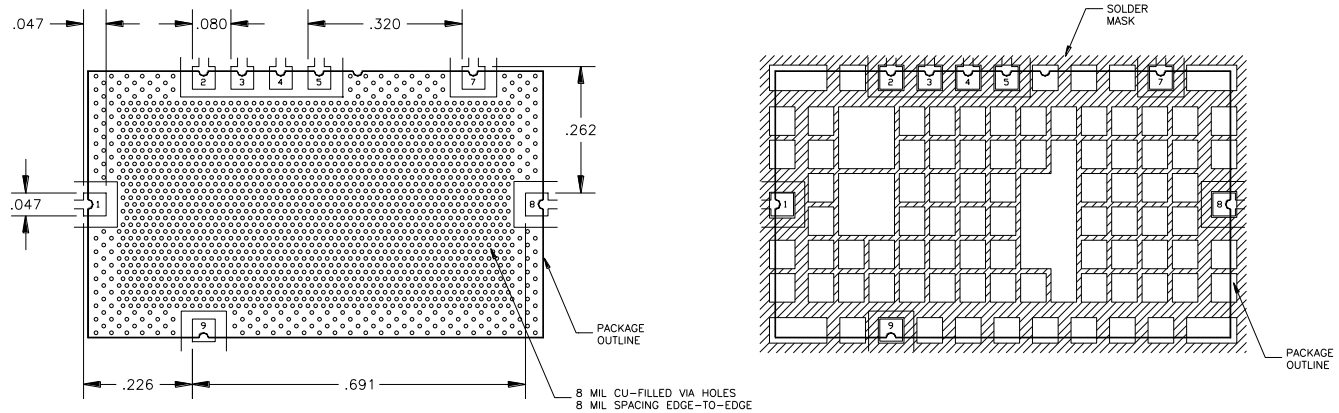
Pulse Droop vs. Frequency



Package Outline [15,16,18]



Recommended Landing Pattern [15,16,17,18]



15. All dimensions are in inches.
16. Reference Application Note S2083 for lead-free solder reflow recommendations. Plating is Ni/Pd/Au.
17. Landing pattern indicates solder mask opening. Cu-filled via-holes under the ground are used for optimal thermal performance. Recommended pattern: 8-mil diameter, 8-mil spacing.
18. Layout drawing available upon request.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Nitride Devices and Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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