Low Cost Two-Way SMT Power<br>Divider, 1850 - 1990 MHz

## Features

- Small Size and Low Profile
- Industry Standard SOIC-8 SMT Plastic Package
- Excellent Amplitude and Phase Balance
- Superior Repeatability
- Typical Insertion Loss 0.7 dB
- Typical Isolation 21 dB
- 1 Watt Power Handling
- Lead-Free SOIC-8 Package
- 100\% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- $260^{\circ} \mathrm{C}$ Reflow Compatible
- RoHS* Compliant Version of DS52-0002


## Description

M/A-COM's MAPDCC0002 is an IC-based monolithic power divider in a low cost SOIC-8 plastic package. This 2-way power divider is ideally suited for applications where small size, low insertion loss, superior phase/amplitude tracking and low cost are required. Typical applications include base station switching networks and other communication applications where size and PCB real estate are a premium. Available in tape and reel.

The MAPDCC0002 is fabricated using a passiveintegrated circuit process. The process features fullchip passivation for increased performance and reliability.

## Ordering Information

| Part Number | Package |
| :---: | :---: |
| MAPDCC0002 | Bulk Packaging |
| MAPDCC0002TR | 1000 piece reel |
| MAPDCC0002-TB | Sample Test Board |

Note: Reference Application Note M513 for reel size information.

## Functional Block Diagram



## Pin Configuration

| Pin No. | Function |
| :---: | :---: |
| 1 | GND |
| 2 | RF-IN |
| 3 | GND |
| 4 | GND |
| 5 | RF-1 (out) |
| 6 | GND |
| 7 | GND |
| 8 | RF-2 (out) |

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Electrical Specifications ${ }^{1}: \mathrm{T}_{\mathrm{A}}=\mathbf{2 5}^{\circ} \mathrm{C}$

| Parameter | Test Conditions | Frequency | Units | Min | Typ | Max |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | Above 3.0 dB | $1850-1990$ | dB | - | 0.5 | 0.6 |
| Isolation | - | $1850-1990$ | dB | 15 | 21 | - |
| VSWR | Input RL | $1850-1990$ | Ratio | - | $1.2: 1$ | $1.4: 1$ |
| VSWR | Output RL | $1850-1990$ | Ratio | - | $1.4: 1$ | $1.6: 1$ |
| Amplitude Balance | - | $1850-1990$ | dB | - | 0.05 | 0.15 |
| Phase Balance | - | $1850-1990$ | Deg. | - | 1.0 | 3.0 |

1. All specifications apply with a 50 -ohm source and load impedance.

## Absolute Maximum Ratings ${ }^{\text {2,3 }}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power ${ }^{4}$ | 1 W CW |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

2. Exceeding any one or combination of these limits may cause permanent damage to this device.
3. $M / A-C O M$ does not recommend sustained operation near these survivability limits.
4. With internal load dissipation of 0.125 W maximum

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Recommended PCB Configuration


GMIC Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

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## Typical Performance Curves

Insertion Loss vs. Frequency (above theoretical split loss)


Input VSWR vs. Frequency


Isolation vs. Frequency


Output VSWR vs. Frequency


## Lead-Free, SOIC-8 ${ }^{\dagger}$



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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

[^1]:    ${ }^{+}$Reference Application Note M538 for lead-free solder reflow recommendations.

