

Avionics Pulsed Power Transistor 50 W, 960 - 1215 MHz, 10 µs Pulse, 10% Duty

Rev. V2

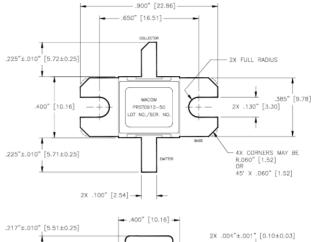
Features

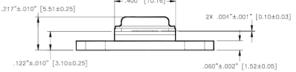
- NPN Silicon Microwave Power Transistors
- Common Base Configuration
- · Broadband Class C Operation
- High Efficiency Inter-Digitized Geometry
- · Diffused Emitter Ballasting Resistors
- · Gold Metallization System
- Internal Input and Output Impedance Matching
- Hermetic Metal/Ceramic Package
- RoHS* Compliant

Description

The MAPRST0912-50 is a RF power transistor. These high power transistors are ideal for avionics, communications, radar, and industrial, scientific, and medical applications.

Outline Drawing





Unless otherwise noted, tolerances are inches $\pm .005"$ [millimeters ± 0.13 mm]

Electrical Specifications: $T_A = +25^{\circ}C \pm 5^{\circ}C$, $V_{CC} = 50 \text{ V}$, $P_{IN} = 6.2 \text{ W}$ (unless otherwise noted)

Parameter	Test Conditions	Symbol Min.		Max.	Units
Collector-Emitter Breakdown Voltage	I _C = 15 mA	BV _{CES}	65	-	V
Collector-Emitter Leakage Current	V _{CE} = 40 V	I _{CES}	-	2.0	mA
Thermal Resistance	F = 960, 1090, 1215 MHz	R _{TH(JC)}	1	0.80	°C/W
Output Power	F = 960, 1090, 1215 MHz	Po	50	-	W
Power Gain	F = 960, 1090, 1215 MHz	G _P	9.1	-	dB
Input Return Loss	F = 960, 1090, 1215 MHz	RL	40	-	dB
Collector Efficiency	F = 960, 1090, 1215 MHz	ης	-	-9	%
Load Mismatch Stability	F = 960 MHz	VSWR-S	-	10:1	-
Load Mismatch Tolerance	F = 960, 1090, 1215 MHz	VSWR-T	-	1.5:1	-

^{*} Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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Typical RF Performance

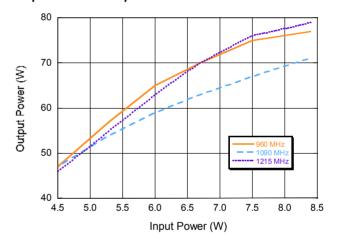
Freq.	Pin	Pout	Gain	∆Gain	lc	Eff	RL	VSWR-S	VSWR-T	VSWR-T P1dB Overdi	
(MHz)	(W)	(W)	(dB)	(dB)	(A)	(%)	(dB)	(1.5:1)	(10:1)	Pout (W)	Δ Ρο
960	6.2	65.9	10.25	-	2.66	49.6	-22.2	S	Р	73.4	0.48
1090	6.2	61.9	9.98	-	2.58	48.0	-15.2	S	-	68.7	0.45
1215	6.2	64.6	10.16	0.35	2.50	51.6	-15.9	S	-	74.8	0.63

Note: $\triangle Po(dB)$ is the difference between Pout at 1dB overdrive and Pout at Pin = 6.2 W.

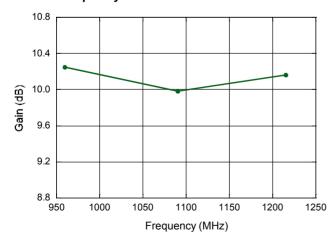
Absolute Maximum Ratings @ +25°C

Parameter	Rating		
Collector-Emitter Voltage (V _{CES})	65 V		
Emitter-Base Voltage (V _{EBO})	3.0 V		
Collector Current (Peak) (I _C)	5.3 A		
Power Dissipation @ +25°C (P _{TOT})	220 kW		
Storage Temperature (T _{STG})	-65°C to +200°C		
Junction Temperature (T _J)	200°C		

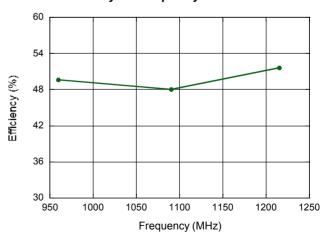
Output Power vs. Input Power



Gain vs. Frequency



Collector Efficiency vs. Frequency



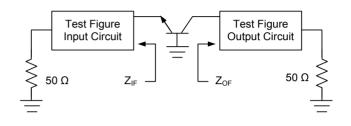


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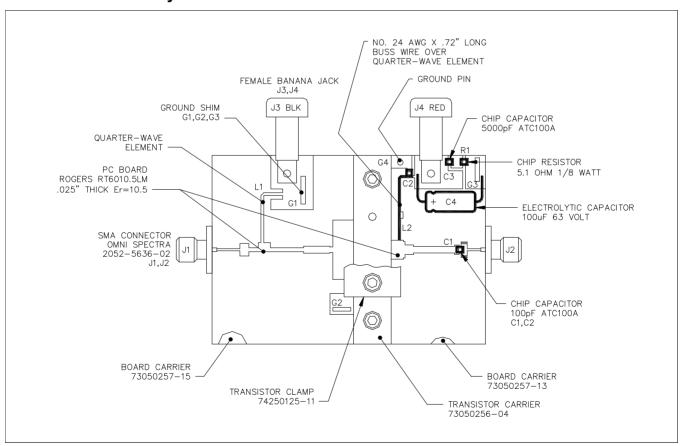
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Broadband Test Fixture Impedance

Frequency (MHz)	Z _{IF} (Ω)	Z _{OF} (Ω)		
960	3.5 - j7.5	12.0 - j7.8		
1030	3.8 - j7.0	11.1 - j6.4		
1090	3.9 - j6.8	10.6 - j5.1		
1150	3.9 - j6.8	10.8 - j3.8		
1215	3.6 - j7.0	11.1 - j3.2		



Test Fixture Assembly



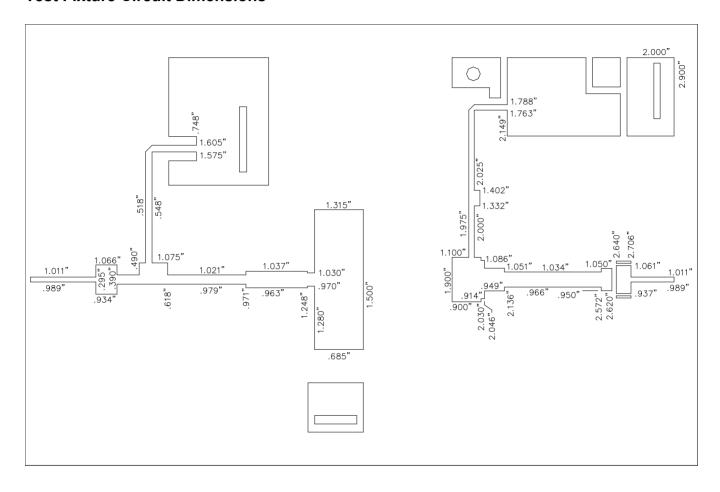
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Test Fixture Circuit Dimensions



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