## Features

- Low Voltage Operation: 2.7 V
- High Power: +38dBm (typ) P0.1dB
- High IP3: +56 dBm
- Low Insertion Loss: 0.25 dB @ 1 GHz
- High Isolation: 25 dB @ 1 GHz
- Lead-Free SC70 Package
- $100 \%$ Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and $260^{\circ} \mathrm{C}$ Reflow Compatible


## Description

M/A-COM's MASW-008853 is a GaAs PHEMT MMIC single pole double throw (SPDT) high power switch in a low cost SC70 six lead package. The MASW-008853 is ideally suited for applications where high power, low control voltage, low insertion loss, high isolation, small size, and low cost are required.

Typical applications are for CDMA handset systems that connect separate transceiver and/or GPS functions to a common antenna, as well as other related handset and general purpose applications. The MASW-008853 can be used in all systems operating up to 5.0 GHz requiring high power at low control voltage.

The MASW-008853 is fabricated using a 0.5 micron gate length GaAs pHEMT process. The process features full passivation for performance and reliability.

## Ordering Information ${ }^{1,2}$

| Part Number | Package |
| :---: | :---: |
| MASW-008853-000000 | Bulk Packaging |
| MASW-008853-TR3000 | 3000 piece reel |
| MASW-008853-001SMB | Sample Test Board |

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

## Functional Block Diagram



## Pin Configuration

| Pin No. | Pin Name | Description |
| :---: | :---: | :---: |
| 1 | RF1 | RF Port 1 |
| 2 | GND | RF Ground |
| 3 | RF2 | RF Port 2 |
| 4 | V2 | Vcontrol 2 |
| 5 | RFC | RF Common |
| 6 | V1 | Vcontrol 1 |

## Absolute Maximum Ratings ${ }^{3,4}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power | +38 dBm |
| $(0.5-3 \mathrm{GHz}, 3 \mathrm{~V}$ Control $)$ | +8.5 volts |
| Operating Voltage | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Storage Temperature |  |

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. $\mathrm{M} / \mathrm{A}-\mathrm{COM}$ does not recommend sustained operation near these survivability limits.
[^0]GaAs SPDT 2.7 V High Power Switch
DC -5.0 GHz
Rev. V2
Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{C}}=0 \mathrm{~V} / 2.7 \mathrm{~V}, \mathrm{Z}_{0}=50 \Omega^{5}$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss ${ }^{6}$ | $\begin{aligned} & 1 \mathrm{GHz} \\ & 2 \mathrm{GHz} \\ & 3 \mathrm{GHz} \\ & 4 \mathrm{GHz} \\ & 5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | - - - | $\begin{aligned} & 0.30 \\ & 0.36 \\ & 0.45 \\ & 0.70 \\ & 1.10 \end{aligned}$ | $\begin{gathered} 0.65 \\ - \\ - \\ - \end{gathered}$ |
| Isolation | 1 GHz 2 GHz 3 GHz 4 GHz 5 GHz | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | 23 - - | $\begin{aligned} & 25 \\ & 19 \\ & 15 \\ & 13 \\ & 11 \end{aligned}$ | $\begin{aligned} & - \\ & - \\ & - \end{aligned}$ |
| Return Loss | DC-3 GHz | dB | - | 20 | - |
| IP3 | $825 \mathrm{MHz}$ <br> Two Tone, +24 dBm Total Pin, 5 MHz Spacing | dBm | - | 56 | - |
| Cross Modulation | Two-tone signal input: Tx1 = +22 dBm @ 820 MHz , $\mathrm{Tx} 2=+22 \mathrm{dBm} @ 821 \mathrm{MHz}$, $R_{X}$ interfere $=-23 \mathrm{dBm} @ 869 \mathrm{MHz}$ | dBm | - | -99 | - |
|  | Two-tone signal input: Tx1 = +18 dBm @ 1880 MHz , $\mathrm{Tx} 2=+18 \mathrm{dBm} @ 1881 \mathrm{MHz}$, <br> $R_{x}$ interfere $=-23 \mathrm{dBm} @ 1960 \mathrm{MHz}$ | dBm | - | -94 | - |
| P0.1dB | 1 GHz | dBm | - | 38 | - |
| Trise, Tfall | 10\% to 90\% RF, 90\% to 10\% RF | nS | - | 70 | - |
| Ton, Toff | 50\% control to $90 \%$ RF, $50 \%$ control to $10 \%$ RF | nS | - | 100 | - |
| Transients | In Band | mV | - | 25 | - |
| Control Current | $\mathrm{V}_{\mathrm{C}}=2.7 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 5 | 20 |

5. For positive voltage control, external DC blocking capacitors are required on all RF ports.
6. Insertion loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for $100 \mathrm{MHz}-1 \mathrm{GHz}, 39 \mathrm{pF}$ for $0.5 \mathrm{GHz}-3 \mathrm{GHz}$.

## Truth Table ${ }^{7,8,9}$

| V1 | V2 | ANT- RF1 | ANT - RF2 |
| :---: | :---: | :---: | :---: |
| 1 | 0 | On | Off |
| 0 | 1 | Off | On |

7. For positive voltage control, external DC blocking capacitors are required on all RF ports.
8. Differential voltage, V (state 1$)-\mathrm{V}$ (state 0 ), must be +2.7 V minimum, but must not exceed 8.5 V .
9. $0=-5 \mathrm{~V}$ to $+2.3 \mathrm{~V}, 1=-2.3 \mathrm{~V}$ to +5 V .

## Qualification

Qualified to M/A-COM specification REL-201, Process Flow -2.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Typical Performance Curves, 1000 pF

## Insertion Loss <br> 

RFC Return Loss


RF1/RF2 Return Loss


Isolation


Lead-Free SC70 Plastic Package ${ }^{\dagger}$

${ }^{\dagger}$ Reference Application Note M538 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.

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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

