

# 2.5 V GaAs SPDT Switch 0.5 - 3.0 GHz

Rev. V3

#### **Features**

- Low Voltage Operation: 2.5 V
- Low Insertion Loss: 0.3 dB @ 1 GHz
- Isolation: 34 dB @ 2.4 GHz
- 0.5 micron GaAs pHEMT Process
- Lead-Free 1.2 x 1.5 mm 6-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS Compliant\* and 260°C Reflow Compatible

### Description

The MASWSS0167 is a GaAs pHEMT MMIC single pole double throw (SPDT) switch in a lead-free 1.2 x 1.5 mm 6-lead PQFN package. This device is ideally suited for applications where low control voltage, low insertion loss, moderate isolation, small size and low cost are required.

Typical applications are for filter and antenna switching in wireless LAN systems that connect separate receive functions to a common antenna, as well as other handset and general purpose switching applications.

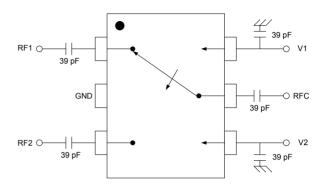
The MASWSS0167 is fabricated using a 0.5 micron gate length GaAs pHEMT process. The process features full passivation for performance and reliability.

### Ordering Information<sup>1,2</sup>

Part Number	Package		
MASWSS0167TR-3000	3000 piece reel		
MASWSS0167SMB	Sample Test Board		

- 1. Reference Application Note M513 for reel size information.
- 2. All sample boards include 5 loose parts.

### **Functional Schematic**



### **Pin Configuration**

Pin No.	Pin Name	Description		
1	RF1	RF In/Out		
2	GND	RF Ground		
3	RF2	RF In/Out		
4	V2	Control 2		
5	RFC	RF Common		
6	V1	Control 1		

### **Handling Procedures**

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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## Electrical Specifications<sup>3</sup>: $T_A = 25$ °C, $V_C = 0$ V / +2.5 V, $Z_0 = 50$ $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss <sup>4</sup>	1 GHz 2 GHz 3 GHz		_	0.30 0.40 0.50	0.50 — —
Isolation	1 GHz 2 GHz 3 GHz		23 — —	25 27 24	_
VSWR	0.5 - 3.0 GHz		_	1.1	_
IP3	2-Tone, +10 dBm/tone, 5 MHz Spacing, > 50 MHz		_	48	_
P1dB	_		_	28	_
Linear Pout	2.5 GHz, OFDM, QAM-64,54Mbps, EVM=2.5% 2.5 V 3.0 V 5.0 V	dBm		21.0 23.5 28.5	
T <sub>RISE</sub> , T <sub>FALL</sub>	10% to 90% RF, 90% to 10% RF	ns	_	30	_
T <sub>ON</sub> , T <sub>OFF</sub>	50% control to 90% RF, and 50% control to 10% RF		_	35	_
Transients	In Band		_	60	_
Control Current	V <sub>C</sub>   = 2.5 V		_	1	5

<sup>3.</sup> For positive voltage control, external DC blocking capacitors are required on all RF ports.

### Absolute Maximum Ratings<sup>5,6</sup>

Parameter	Absolute Maximum		
Input Power (0.5 - 3 GHz, 2.5 V Control)	+32 dBm		
Operating Voltage	+8.5 volts		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

<sup>5.</sup> Exceeding any one or combination of these limits may cause permanent damage to this device.

### Truth Table<sup>7</sup>

V1	V2	RFC - RF1	RFC - RF2
0	1	On	Off
1	0	Off	On

<sup>7.</sup>  $0 = 0 \pm 0.2 \text{ V}$ , 1 = 2.5 to 5 V

<sup>4.</sup> Insertion Loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for 100 MHz - 500 MHz, 39 pF for 0.5 - 3 GHz.

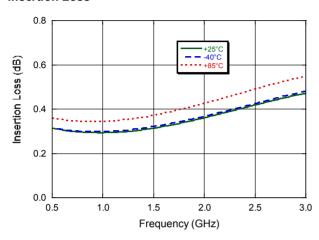
M/A-COM Technology Solutions does not recommend sustained operation near these survivability limits.

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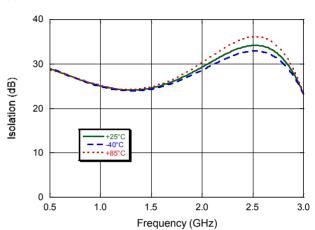
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## **Typical Performance Curves**

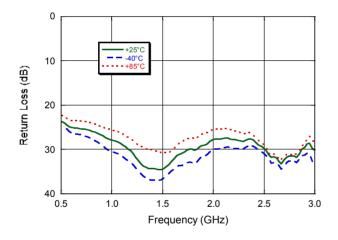
#### Insertion Loss



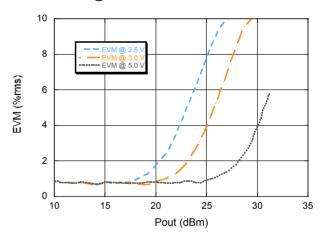
#### Isolation



### Return Loss



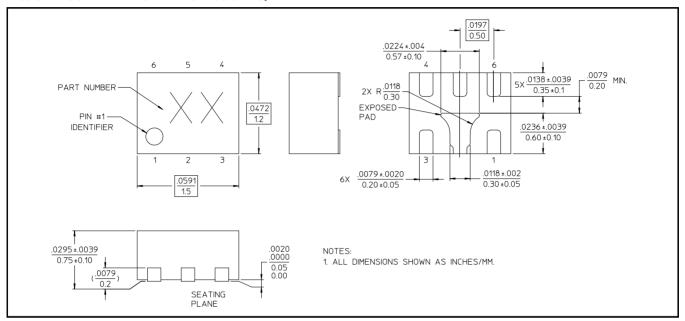
### EVM vs. Pout @ 2.5 GHz



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### Lead-Free 1.2 x 1.5 mm 6-Lead PQFN<sup>†</sup>



<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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