

Rev. V1

#### **Features**

- Guaranteed performance @ 960-1215MHz, 36Vdc
- Output power: 30W peak
- Minimum gain: 9.0dB min., 9.5dB typ.
- 100% tested for load mismatch at all phase angles with 10:1 VSWR
- · Hermetically sealed, industry standard package
- · Silicon nitride passivated
- Gold metallized, emitter ballasted for long life and resistance to metal migration
- Internal input matching for broadband operation

#### **Description and Applications**

Designed for 960–1215 MHz long or short pulse common base amplifier applications such as JTIDS and Mode–S transmitters.

#### **Product Image**



#### **Maximum Ratings**

Rating		Value	Unit
Collector–Emitter Voltage		55	Vdc
Collector–Base Voltage (1)	V <sub>CBO</sub>	55	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	3.5	Vdc
Collector Current — Continuous (1)	Ic	3.0	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1), (2) Derate above 25°C		110 0.625	Watts mW/°C
Storage Temperature Range	T <sub>stg</sub>	- 65 to + 200	°C
Junction Temperature	TJ	200	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (3)		1.6	°C/W

#### NOTES:

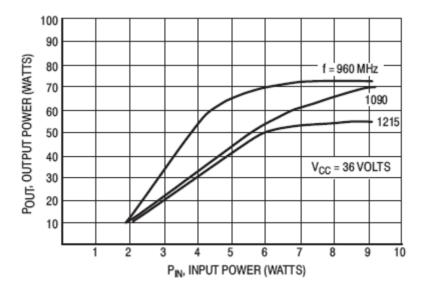
- Under pulse RF operating conditions.
- These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as pulsed RF amplifiers.
- Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques. (Worst case θ<sub>JC</sub> value measured @ 23% duty cycle)



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### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

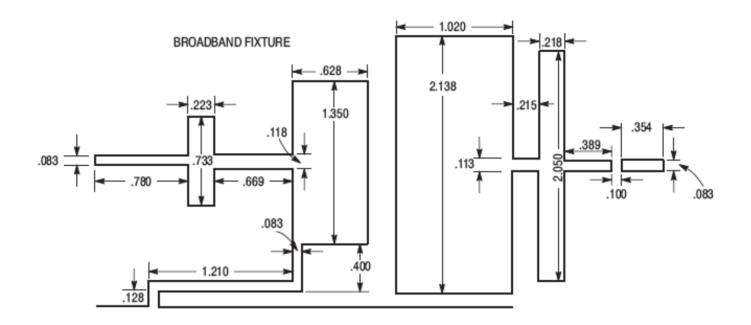
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 25 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	55	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 25 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	55	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 5.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	3.5	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 36 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	_	2.0	mAdc
ON CHARACTERISTICS					
DC Current Gain (I <sub>C</sub> = 500 mAdc, V <sub>CE</sub> = 5.0 Vdc)	h <sub>FE</sub>	20	_	_	_
FUNCTIONAL TESTS (10 μs Pulses @ 50% duty cycle for 3.5 ms;	overall duty cyc	cle – 25%)			
Common–Base Amplifier Power Gain (V <sub>CC</sub> = 36 Vdc, P <sub>out</sub> = 30 W Peak, f = 960 MHz)	G <sub>PB</sub>	9.0	9.5	_	dB
Collector Efficiency (V <sub>CC</sub> = 36 Vdc, P <sub>out</sub> = 30 W Peak, f = 960 MHz)	η	40	45	_	%
Load Mismatch (V <sub>CC</sub> = 36 Vdc, P <sub>out</sub> = 30 W Peak, f = 960 MHz, VSWR = 10:1 All Phase Angles)	Ψ	No Degradation in Output Power			

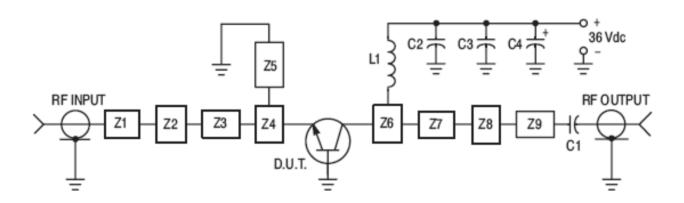


### Output power versus input power



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C1 - 75 pF 100 Mil Chip Capacitor

C2 - 39 pF 100 Mil Chip Capacitor

 $C3 - 0.1 \mu F$ 

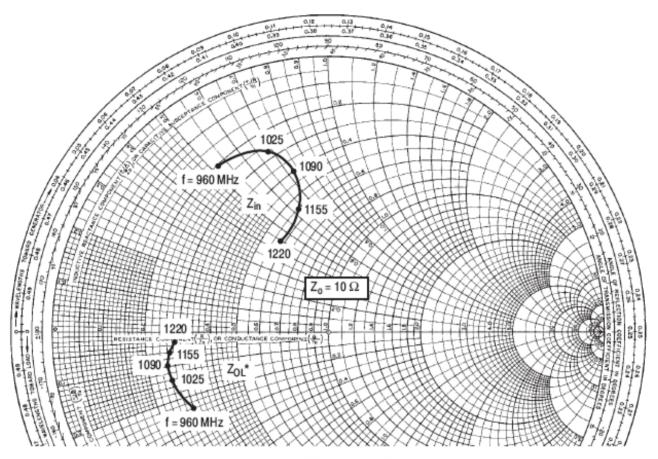
C4 — 1000 μF, 50 Vdc, Electrolytic

L1 - 3 Turns #18 AWG, 1/8" ID, 0.18 Long

Z1–Z9 — Microstrip, See Details
Board Material — Teflon, Glass Laminate
Dielectric Thickness = 0.030"
ε<sub>r</sub> = 2.55, 2 Oz. Copper



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 $P_{out} = 30 \text{ W Pk} \quad V_{CC} = 36 \text{ V}$ 

f MHz	Zin Ohms	Z <sub>OL</sub> * Ohms
960	2.05 + j5.2	2.9 - j2.35
1025	2.67 + j6.34	2.55 - j1.3
1090	4.0 + j7.1	2.52 - j0.9
1155	5.5 + j6.2	2.6 - j0.6
1220	5.7 + j4.3	2.8 - j0.3

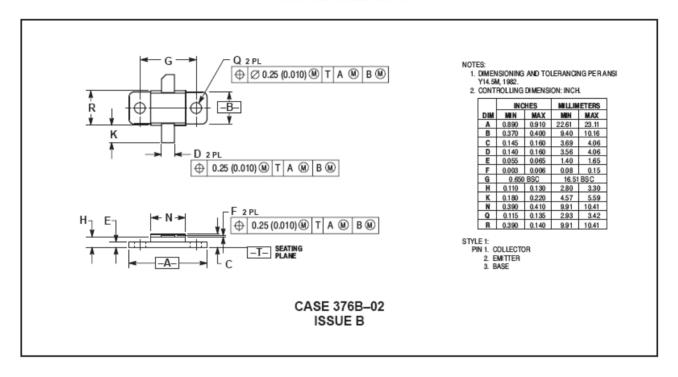
Z<sub>OL</sub>\* = Conjugate of the optimum load impedance into which the device operates at a given output power, voltage, and frequency.

### Series equivalent input/output impedances



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#### PACKAGE DIMENSIONS



## MRF10031



Microwave Power Silicon NPN Transistor 30W (peak), 960–1215MHz, 36V

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