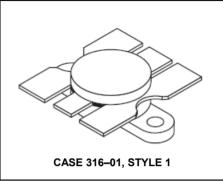


The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V

Designed primarily for wideband large-signal output amplifier stages in 30-200 MHz frequency range.

- Guaranteed performance at 150 MHz, 28 Vdc • Output power = 100 W Minimum gain = 9.0 dB
- Built-in matching network for broadband operation .
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Gold metallization system for high reliability .
- High output saturation power ideally suited for 30 W • carrier/120 W
- Peak AM amplifier service
- Guaranteed performance in broadband test fixture

Product Image



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	35	Vdc
Collector-Base Voltage	V _{CBO}	65	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous — Peak (10 seconds)	Ι _C	12 18	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	270 1.54	Watts W/ºC
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic		Symbol	Max		Unit
Thermal Resistance, Junction to Case		R _{eJC}	0.65		°C/W
ELECTRICAL CHARACTERISTICS (T _C = 25°C unless otherwise noted.)					
Characteristic	Symbol	Min	Тур	Max	Unit

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The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V

OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, I _B = 0)	V _{(BR)CEO}	35	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, V _{BE} = 0)	V _{(BR)CES}	65	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 100 mAdc, I _E = 0)	V _{(BR)CBO}	65	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	_	—	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	Ісво	_	_	5.0	mAdc
ON CHARACTERISTICS	ł				
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	h _{FE}	10	25	80	—
NOTE:	1	•	1	1	(continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

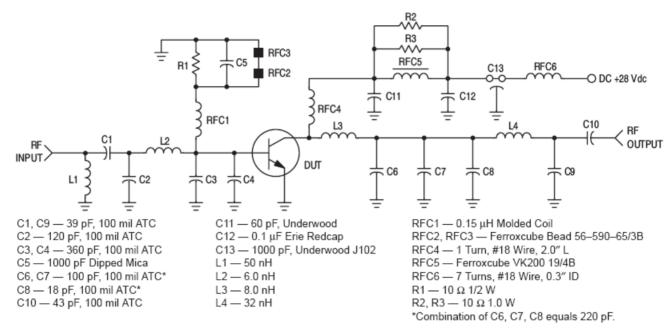
ELECTRICAL CHARACTERISTICS - continued (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	150	175	pF
FUNCTIONAL TESTS (Figure 2)					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 100 W, f = 150 MHz, I _C (Max) = 6.5 Adc)	G _{PE}	9.0	10	_	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 100 W, f = 150 MHz, I _C (Max) = 6.5 Adc)	η	55	60	_	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 100 W CW, f = 150 MHz, VSWR = 30:1 all phase angles)	Ψ	No Degradation in Output Power			

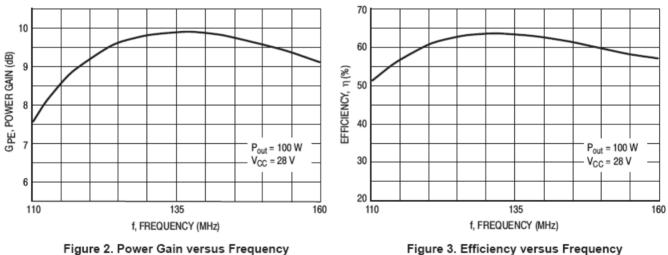
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The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V







Broadband Test Fixture

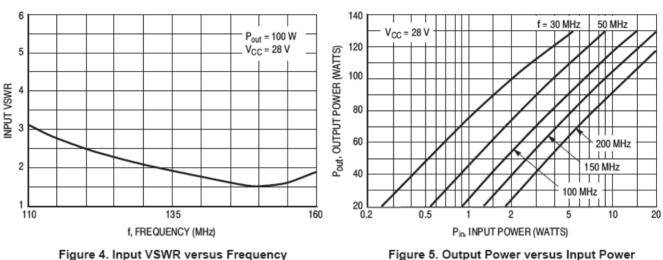
igure 3. Efficiency versus Frequency Broadband Test Fixture

MRF317



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The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V



Broadband Test Fixture

Figure 5. Output Power versus Input Power

TYPICAL PERFORMANCE CURVES

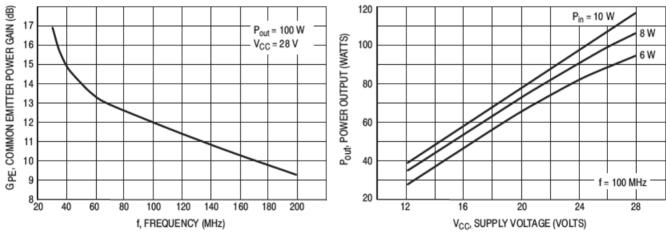


Figure 6. Power Gain versus Frequency

Figure 7. Power Output versus Supply Voltage

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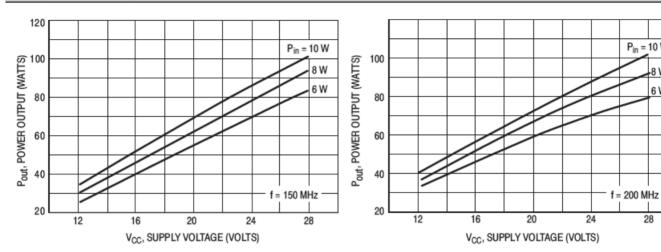
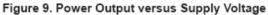
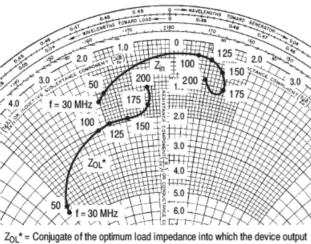


Figure 8. Power Output versus Supply Voltage





V _{CC} = 28 V, P _{out} = 100 W				
f MHz	Z _{in} OHMS	Z _{OL} * OHMS		
30	1.2 – j2.0	4.3 – j5.0		
50	1.0 – j1.8	4.0 – j4.9		
100	0.3 + j0.7	2.0 - j2.3		
125	0.3 + j1.0	1.9 – j1.9		
150	0.6 + j1.3	1.9 - j1.3		
175	1.0 + j1.5	1.6 - j0.6		
200	0.9 + j1.0	1.1 – j0.6		

operates at a given output power, voltage and frequency.



Rev. V1

Pin = 10 W

8 W

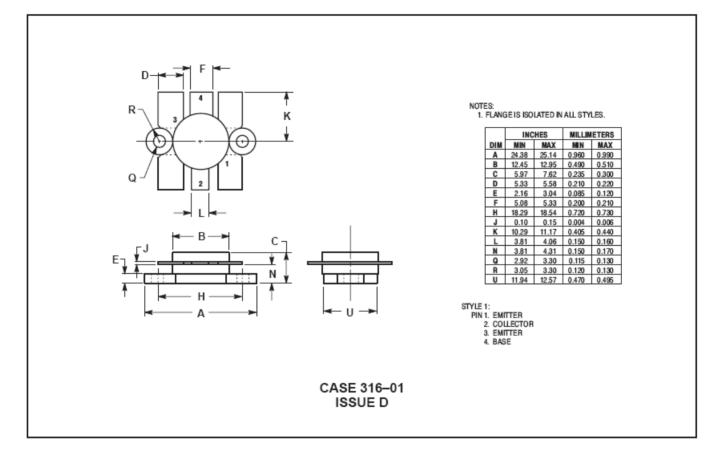
6 W



The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V

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The RF Line NPN Silicon Power Transistor 100W, 30-200MHz, 28V

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