## Features

- Integral TTL Driver
- Isolation: 50 dB Typ. At 1 GHz
- Low DC Power Consumption
- Surface Mount Package
- Low Cost/High Performance
- 50 Ohm Nominal Impedance
- Lead-Free CR-14 Package
- $260^{\circ} \mathrm{C}$ Reflow Compatible
- RoHS* Compliant


## Description

M/A-COM's SW15-0314 is a GaAs MMIC SP4T absorptive switch with an integral silicon ASIC driver. This device is in a 24 -lead ceramic surface mount package. These switches exhibit excellent performance from DC to 3 GHz , with very low DC power dissipation. The SW15-0314 is ideally suited for wireless infrastructure applications. Available with enhanced performance as fully hermetic version. Environmentally screenable as SW-314.

## Ordering Information

| Part Number | Package |
| :---: | :---: |
| SW15-0314 | Bulk Packaging |
| SW15-0314-TB | Sample Test Board |

Note: Reference Application Note M513 for reel size information.

## Functional Block Diagram



## Pin Configuration

| Pin No. | Function | Pin No. | Function |
| :---: | :---: | :---: | :---: |
| 1 | RFC | 13 | C3 |
| 2 | GND | 14 | C4 |
| 3 | GND | 15 | GND |
| 4 | RF1 | 16 | GND |
| 5 | GND | 17 | GND |
| 6 | GND | 18 | RF3 |
| 7 | RF2 | 19 | GND |
| 8 | GND | 20 | GND |
| 9 | Vee | 21 | RF4 |
| 10 | Vcc | 22 | GND |
| 11 | C1 | 23 | GND |
| 12 | C2 | 24 | GND |

The metal bottom of the case must be connected to RF and DC ground.

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Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}^{\circ} \mathrm{C}^{\mathbf{1 , 2}}$

| Parameter | Test Conditions | Frequency | Units | Min | Typ | Max |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | - | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-3.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & - \\ & - \\ & - \end{aligned}$ | $\begin{aligned} & 1.0 \\ & 1.2 \\ & 1.2 \\ & 1.4 \end{aligned}$ | $\begin{aligned} & 1.3 \\ & 1.4 \\ & 1.6 \\ & 1.8 \end{aligned}$ |
| Isolation | - | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-3.0 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 50 \\ & 40 \\ & 30 \\ & 25 \end{aligned}$ | $\begin{aligned} & 60 \\ & 50 \\ & 40 \\ & 35 \end{aligned}$ | - |
| VSWR | RFC, RF1-RF4 (On) | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-3.0 \mathrm{GHz} \end{aligned}$ | Ratio <br> Ratio <br> Ratio <br> Ratio | - - | $\begin{aligned} & 1.6: 1 \\ & 1.6: 1 \\ & 1.6: 1 \\ & 1.6: 1 \end{aligned}$ | - |
| VSWR | RF1-RF4 (Off) | $\begin{aligned} & \mathrm{DC}-0.5 \mathrm{GHz} \\ & \mathrm{DC}-1.0 \mathrm{GHz} \\ & \mathrm{DC}-2.0 \mathrm{GHz} \\ & \mathrm{DC}-3.0 \mathrm{GHz} \end{aligned}$ | Ratio <br> Ratio <br> Ratio <br> Ratio | $\begin{aligned} & - \\ & - \\ & \hline \end{aligned}$ | $\begin{aligned} & 1.3: 1 \\ & 1.5: 1 \\ & 1.9: 1 \\ & 2.4: 1 \end{aligned}$ | - |
| Trise, Tfall | 10\% to $90 \%$ | - | ns | - | 50 | - |
| Ton, Toff | $50 \%$ Control to $90 \%$ / 10\% RF | - | ns | - | 150 | - |
| Transients | In-Band (peak-peak) | - | mV | - | 50 | - |
| 1 dB Compression | Input Power | $\begin{gathered} 0.05 \mathrm{GHz} \\ 0.5 \mathrm{GHz} \text { to } 3 \mathrm{GHz} \end{gathered}$ | dBm dBm | - | $\begin{aligned} & +20 \\ & +27 \end{aligned}$ | - |
| IP3 | Two-Tone Input Power up to +5 dBm | $\begin{gathered} 0.05 \mathrm{GHz} \\ 0.5 \mathrm{GHz} \text { to } 3 \mathrm{GHz} \end{gathered}$ | dBm dBm | - | $\begin{aligned} & +35 \\ & +46 \end{aligned}$ | - |
| IP2 | Two-Tone Input Power up to +5 dBm | $\begin{gathered} 0.05 \mathrm{GHz} \\ 0.5 \mathrm{GHz} \text { to } 3 \mathrm{GHz} \end{gathered}$ | dBm dBm | - | $\begin{aligned} & +45 \\ & +60 \end{aligned}$ | - |
| Vcc | - | - | V | 4.5 | 5.0 | 5.5 |
| Vee | - | - | V | -8.0 | - | -5.0 |
| Icc | $\begin{gathered} \mathrm{Vcc}=4.5 \text { to } 5.5 \mathrm{~V} \\ \mathrm{Vctl}=0 \text { to } 0.8 \mathrm{~V}, \text { or } \mathrm{Vcc}- \\ 2.1 \mathrm{~V} \text { to } \mathrm{Vcc} \end{gathered}$ | - | mA | - | 0.2 | 4.0 |
| lee | $\mathrm{Vee}=-5.0 \mathrm{~V}$ to -8.0 V | - | mA | - | 0.1 | 1.0 |

1. All specifications apply when operated with bias voltages of +5 V for Vcc and -5 V for Vee.
2. When DC blocks are used, a 10 K ohm return to GND is required on the RFC port.

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## Absolute Maximum Ratings ${ }^{3,4,5}$

$\left.\begin{array}{|c|c|}\hline \text { Parameter } & \text { Absolute Maximum } \\ \hline \text { Max Input Power } & \\ 0.05 \mathrm{GHz} \\ 0.5-3.0 \mathrm{GHz}^{5} & +27 \mathrm{dBm} \\ +34 \mathrm{dBm}\end{array}\right]$
3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. $\mathrm{M} / \mathrm{A}-\mathrm{COM}$ does not recommend sustained operation near these survivability limits.
5. When the input power is applied to the terminated port, the absolute maximum is +30 dBm .
6. Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## Typical Performance Curves

## Insertion Loss vs. Frequency



## Recommended PCB Configuration



## Truth Table (Switch)

| TTL Control Inputs |  |  |  | Condition of Switch |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | RF Common to Each RF Port |  |  |  |
| C1 | C2 | C3 | C4 | RF1 | RF2 | RF3 | RF4 |
| 1 | 0 | 0 | 0 | On | Off | Off | Off |
| 0 | 1 | 0 | 0 | Off | On | Off | Off |
| 0 | 0 | 1 | 0 | Off | Off | On | Off |
| 0 | 0 | 0 | 1 | Off | Off | Off | On |

$0=$ TTL Low; 1 = TTL High

Isolation vs. Frequency


## Typical Performance Curves

## VSWR vs. Frequency



## Lead-Free, CR-14 Ceramic Package ${ }^{\dagger}$


† Reference Application Note M538 for lead-free solder reflow recommendations.

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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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