MH254 Hall-effect sensor is a temperature stable, stress-resistant, Low Tolerance of Sensitivity micro-power switch. Superior high-temperature performance is made possible through a dynamic offset cancellation that utilizes chopper-stabilization. This method reduces the offset voltage normally caused by device over molding, temperature dependencies, and thermal stress

MH254 is special made for low operation voltage, 1.65 V , to active the chip which is includes the following on a single silicon chip: voltage regulator, Hall voltage generator, small-signal amplifier, chopper stabilization, Schmitt trigger, CMOS output driver. Advanced CMOS wafer fabrication processing is used to take advantage of low-voltage requirements, component matching, very low input-offset errors, and small component geometries. This device requires the presence of unipolar magnetic fields for operation.

The package type is in a Halogen Free version has been verified by third party Lab.

## Features and Benefits

- CMOS Hall IC Technology
- Strong RF noise protection
- 1.65 to 6 V for battery-powered applications
- Operation down to 1.65 V , Unipolar Hall Switch Micro power consumption
- High Sensitivity for reed switch replacement applications
- Low sensitivity drift in crossing of Temp. range
- Ultra Low power consumption at 5 uA (Avg)
- High ESD Protection, $\mathrm{HBM}> \pm 4 \mathrm{KV}$ ( min )
- Totem-pole output


## Applications

- Solid state switch
- Handheld Wireless Handset Awake Switch ( Flip Cell/PHS Phone/Note Book/Flip Video Set)
- Magnet proximity sensor for reed switch replacement in low duty cycle applications
- Water Meter
- PDA
- PDVD
- NB
- Pad PC

MH254
Micropower CMOS Unipolar Hall Effect Switch

## Ordering Information



| Part No. | Temperature Suffix | Package Type |
| :---: | :---: | :---: |
| MH254ESQ | E ( $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ ) | SQ (DFN-2020) |

Custom sensitivity selection is available by MST sorting technology
Functional Diagram


Note: Static sensitive device; please observe ESD precautions. Reverse $V_{D D}$ protection is not included. For reverse voltage protection, a $100 \Omega$ resistor in series with $V_{D D}$ is recommended.
MH254, HBM > $\pm 4 K V$ which is verified by third party lab.

MH254
Micropower CMOS Unipolar Hall Effect Switch

## Absolute Maximum Ratings $\operatorname{At}\left(T a=25^{\circ} \mathrm{C}\right)$

| Characteristics |  |  | Values | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Supply voltage,(VDD) |  |  | 7 | V |
| Output Voltage,(Vout) |  |  | 7 | V |
| Reverse Voltage, (VDD) (Vout) |  |  | -0.3 | V |
| Magnetic flux density |  |  | Unlimited | Gauss |
| Output current,(Iovi) |  |  | 1 | mA |
| Operating temperature range, (Ta) |  |  | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range, (Ts) |  |  | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Maximum Junction Temp,(Tj) |  |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance | $\left(\theta_{\text {IA }}\right)$ |  | 540 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
|  | ( $\theta_{J c}$ ) |  | 390 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Package Power Dissipation, $\left(P_{D}\right)$ SQ |  |  | 230 | mW |

Note: Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximumrated conditions for extended periods may affect device reliability.

## Electrical Specifications

DC Operating Parameters : $T a=25^{\circ} \mathrm{C}, V_{D D}=1.8 \mathrm{~V}$

| Parameters | Test Conditions | Min | Typ | Max | Units |
| :--- | :--- | :---: | :---: | :---: | :---: |
| Supply Voltage, $\left(V_{D D}\right)$ | Operating | 1.65 |  | 6 | Volts |
| Supply Current,(IDD) | Awake State |  | 1.4 | 3 | mA |
|  | Sleep State |  | 3.6 | 7 | $\mu \mathrm{~A}$ |
|  | Average |  | 5 | 10 | $\mu \mathrm{~A}$ |
| Output Leakage | Output off |  |  | 1 | uA |
| Output High Voltage, $\left(V_{o H}\right)$ | Iour=0.5mA(Source) | $V_{\text {DD }}-0.2$ |  |  | V |
| Output Low Voltage, $\left(V_{o L}\right)$ | Iour=0.5mA(Sink) |  |  | 0.2 | V |
| Awake mode time,(Taw) | Operating |  | 40 | 80 | uS |
| Sleep mode time,(TsL) | Operating |  | 40 | 80 | mS |
| Duty Cycle, $(D, C)$ |  | 0.1 |  | $\%$ |  |
| Electro-Static Discharge | HBM |  |  | KV |  |

## Typical application circuit



C1: 10nF
C2: 100pF

## MH254ESQ Magnetic Specifications

DC Operating Parameters : Ta=25 ${ }^{\circ} \mathrm{C}, V_{D D}=1.8 \mathrm{~V}$

| Parameter | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Point | $\mathrm{B}_{\mathrm{OP}}$ | N pole to branded side, B > BOP, Oout On | -50 | -30 |  | Gauss |
| Release Point | $\mathrm{B}_{\mathrm{RP}}$ | N pole to branded side, B < BRP, Vout Off |  | -20 | -10 | Gauss |
| Hysteresis | $\mathrm{B}_{\mathrm{HY}}$ | $\|\mathrm{BOPx}-\mathrm{BRPx}\|$ |  | 10 |  | Gauss |

## MH254ESQ Output Behavior versus Magnetic Polar

DC Operating Parameters : Ta $=-40$ to $85^{\circ} \mathrm{C}$, VDD $=1.8 \mathrm{~V}$ to 6 V

| Parameter | Test condition | OUT |
| :---: | :--- | :--- |
| Null or weak magnetic field | B $=0$ or B < BRP | High |
| North pole | B $>$ Bop $(-55 \sim-10)$ | Low |



SQ Package


## MH254 <br> Micropower CMOS Unipolar Hall Effect Switch

## Performance Graph

Typical Supply Voltage( (VDD) Versus Flux Density


Typical Temperature ( $T_{A}$ ) Versus Supply Current(IDD)


Typical Supply Voltage(VDD) Versus Output Voltage(VDSoN)


Typical Temperature( $T_{A}$ ) Versus Flux Density


Typical Supply Voltage(VDD) Versus Supply current current(IDD)


Typical Temperature $\left(T_{A}\right)$ Versus Output Voltage(VDSon)


Micropower CMOS Unipolar Hall Effect Switch


## Package Power Dissipation

The power dissipation of the Package is a function of the pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $\mathrm{T}_{J(\max )}$, the maximum rated junction temperature of the die, $\mathrm{R}_{\theta J A}$, the thermal resistance from the device junction to ambient, and the operating temperature, Ta. Using the values provided on the data sheet for the package, PD can be calculated as follows:
$\mathrm{P}_{\mathrm{D}}=\frac{\mathrm{T}_{\mathrm{J}(\mathrm{max})}-\mathrm{Ta}}{\mathrm{R}_{\theta \mathrm{ja}}}$
The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature Ta of $25^{\circ} \mathrm{C}$, one can calculate the power dissipation of the device which in this case is 230 milliwatts.
$\mathrm{P}_{\mathrm{D}}(\mathrm{ST})=\frac{150^{\circ} \mathrm{C}-25^{\circ} \mathrm{C}}{540^{\circ} \mathrm{C} / \mathrm{w}}=230 \mathrm{~mW}$

The $540^{\circ} \mathrm{C} / \mathrm{W}$ for the SN package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 230 milliwatts. There are other alternatives to achieving higher power dissipation from the Package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

## Sensor Location, package dimension and marking

## MH254ESQ Package

SQ Package


Hall Plate Chip Location
(Top view)

2. Controlling dimension: mm;
3. Chip rubbing will be 10mil maximum;
4. Chip must be in PKG.
center.

## MH 254 SQ Package Date Code



Week Code

| week | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 13 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| code | SA | SB | SC | SD | SE | SF | SG | SH | SI | SJ | SK | SL | SM |
| week | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 |
| code | SN | SO | SP | SQ | SR | SS | ST | SU | SV | SW | SX | SY | SZ |
| week | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 |
| code | TA | TB | TC | TD | TE | TF | TG | TH | TI | TJ | TK | TL | TM |
| week | 40 | 41 | 42 | 43 | 44 | 45 | 46 | 47 | 48 | 49 | 50 | 51 | 52 |
| code | TN | TO | TP | TQ | TR | TS | TT | TU | TV | TW | TX | TY | TZ |

EX : 2014 Year_8 Week $\rightarrow$ SH

## QFN2020-3 Tape On Reel Dimension



SECTIDN A-A


## NOTES:

1. Material: Conductive polystyrene;
2. DIM in mm ;
3. 10 sprocket hole pitch cumulative tolerance $\pm 0.2$;
4. Camber not to exceed 1 mm in 100 mm ;
5. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole;
6. (S.R. OHM/SQ) Means surface electric resistivity of the carrier tape.

## IR reflow curve



ST Soldering Condition

## Packing specification:

| Package | Reel | Box | Carton |
| :---: | :---: | :---: | :---: |
| QFN2020-3 | $3,000 \mathrm{pcs} / \mathrm{reel}$ | $10 \mathrm{reel} / \mathrm{box}$ | 2 box/carton |
| Weight | 0.13 kg | 1.4 kg | 3.7 kg |

Inner box label : Size: $3.4 \mathrm{~cm} * 6.4 \mathrm{~cm}$
Bag and inner box Halogen Free Label


Carton label : Size: $5.6 \mathrm{~cm} * 9.8 \mathrm{~cm}$
Bag and inner box Halogen Free Label


## Combine:

When combine lot, one reel could have two D/C and no more than two DC. One carton could have two devices, no more than two;

## X-ON Electronics

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