

ELECTRIC DOUBLE LAYER CAPACITORS PRODUCT SPECIFICATION 規格書

CUSTOMER :

(**客戶**):志盛翔

DATE: (日期):2020-08-24 P.

CATEGORY (品名)	:	ELECTRIC DOUBLE LAYER CAPACITORS
DESCRIPTION (型号)	:	DRL 2.7V15F (φ12.5X25)
VERSION (版本)	:	01
Customer P/N	:	/
SUPPLIER	:	/

SUPPLIER			CUS	ΓOMER
PREPARED (拟定)	CHECKED (审核)		APPROVAL (批准)	SIGNATURE (签名)
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		SPECIFICAT	TION		ALTERN	ATION HIST	ORY
		DRL SERI	ES		R	ECORDS	
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

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1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	156
Capacitance (F)	15

2.2 <u>Rated voltage code</u>

Code	0Т
Voltage (W.V.)	2.7

2.3 <u>Type</u>

Code	СВ
Туре	Lead Cut

- 2.4 <u>Capacitance tolerance</u> "S" stands for -20% ~ +50%
- 2.5 <u>Diameter</u>

Code	Ι
Diameter	12.5

2.6 <u>Case length</u> 25=25mm

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3. Characteristics

 Standard atmospheric conditions

 Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

 Ambient temperature: 15°C to 35°C

 Relative humidity
 : 25% to75%

 Air Pressure
 : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions: Ambient temperature: $20^{\circ}C \pm 2^{\circ}C$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 70°C.

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	PERFORMANCE
 Rated voltage (WV) 3.1 Surge voltage (SV) 	WV (V.DC) 2.7 SV (V.DC) 2.8
3.2 Nominal capacitance (Tolerance)	 Condition> Constant current discharge method: Measuring circuit: Constant voltage power supply Cx C

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3.3	ESR	<condition> Measuring free Measuring poi <criteria> (20°C)Less that Rated Voltage (V) 2.7</criteria></condition>	juency :1kHz perature:20±2°C nt : 2mm max find the initial limit: Capacitanc e (F) 15	from the surface of Dimension $(D \times L, mm)$ 12.5X25	a sealing resin on the lead wire. ESR, AC(mΩ) (max) at 1kHz/20°C 80
3.4	Leakage current	<condition> 1. Ambient ter 2. The electrifie 3. Desistance <criteria> Less than the i I≤ 0.030mA I is the Leakag</criteria></condition>	nperature: $25^{\circ}C \pm 2^{\circ}C$ cation time:72H value of protective res nitial limit($25^{\circ}C \pm 2^{\circ}C$ ge current	C. istor less than 1Ω C):	
3.5	Temperature characteristic	 Condition> STEP 1 2 3 4 a. ESR -40°C/ H b. $\Delta C/C \ 20°C$:	Temperature(°C) 20±2 -40+3 eep at 15 to 35°C for 15 minutes or more 70±2 ESR 20°C: ESR ratio at Capacitance change ;	Item Capacitance ESR △ C/C ESR △ C/C ESR	Characteristics Within ±30% of initial capacitance Less than or equal to 4 times of the value of item 3.3 Within ±30% of initial capacitance Within ±30% of initial capacitance The limit specified in 3.3

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		(Com 1:4: or)	
		<condition> The Capacitor is sto voltage for 1000 +4</condition>	bred at a temperature of 70 \pm 2 °C with rated 8/0 hours .The result should meet the following table:
		<criteria></criteria>	
		Item	Performance
		Capacitance Chang	ge Within ±30% of initial capacitance
2.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
3.6	test	Appearance	No visible damage and no leakage of electrolyte
		<condition> Humidity Test: The capacitor shal 40±2°C, the chara <criteria></criteria></condition>	Il be exposed for 240±48 hours in an atmosphere of 90~95%RH at acceristic change shall meet the following requirement.
		Item	Performance
	Damp	Capacitance Chan	ge Within ±30% of initial capacitance
3.7	heat	ESR	Less than or equal to 4 times of the value of item 3.3
	test	Appearance	No visible damage and no leakage of electrolyte
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	a) Load null strangth			
	A static load force shall b	e applied to	the terminal in the avial di	rection and
	acting in a direction away f	From the body	for $10+1$ s	cetion and
	L and wire diameter	r (mm)	L oad force (N)	
				_
	$0.5 < d \le 0.8$		10	
3.8 Lead strength	b) Lead bending When the capacitor is placed table above is applied to one horizontal position and then for 2~3seconds. The additional bends are ma Lead wire diameter $0.5 < d \le 0.8$ Performance: The characteri	l in a vertical e lead and ther returned to a ide in the opport (mm) stic shall mee	position and the weight spec the capacitor is slowly rota vertical position thus comple osite direction Load force (N) 5 t the following value after a)	ified in the ted 90^0 to a eting bends
	Item	Performan		or 0) test.
	Capacitance Change	Within +30)% of initial canacitance	
		No visible	damage Legible marking an	d no
	Appearance	leakage of	electrolyte	u no
3.9 Resistance to vibration	Amplitude: 0.75mm(Total excurs Direction :X、Y、Z (3 axes) Duration: 2hours/ axial (Total 6 I The capacitors are supported as t	sion 1.5mm) hours) he following f	Fig2 ↓ ≤0. 3mm	
	Performance: Capacitance value capacitance when the value is me exam, Capacitance difference sha exam.	shall not show easured withir all be within =	w drastic change compared to a 30 minutes. Prior to the con ±10% compared to the initial	o the initial npletion of value the

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3.10	Solderability	The capacitor shall be tested under the following conditions:Solder: Sn-3Ag-0.5CuSoldering temperature:245±3°CImmersing time: 2.0±0.5sImmersing depth: 1.5~ 2.0mm from the root.Flux: Approx .25% rosin (JIS K5902) in ETHANOL (JIS K1501)Performance:At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	 A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylaehol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260 ±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass -epoxy board B) Soldering iron method Bit temperature : 350 ±10°C Application time : 3.5 ±0.5 s Heat protector: t=1.6mm glass -epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured:

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5. Notice item

(1) The capacitor has fixed polarity.

(2) The capacitor should be used under rated voltage.

(3) The capacitor should not be used in the charge and discharge circuit with high frequency.

(4) The ambient temperature affects the super capacitor life.

(5) Voltage reduction ΔV =IR will happen at the moment of discharge.

(6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.

(7) The capacitor should stored in the environment within -30°C~50°C temperature and less than 60% relative humidity.

(8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.

(9) Don't twist capacitor or make it slanting after installing.

(10) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)

(11) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

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