Features

- + +3V (MAX117) or +5V (MAX118) Operation
- Fully Assembled and Tested
- On-Board Oscillator Generates Timing Signals
- LED Display of Conversion Results
- Low-Power Shutdown Mode
- results appear on the LED output display. The MAX117 EV kit also evaluates the +5V MAX118. To evaluate the MAX118, order a MAX118 free sample, replace the MAX117 with the MAX118, and install the appropriate R9 resistor (see *Component List*).

The MAX117 evaluation kit (EV kit) is a fully assembled

evaluation board for the 8-bit, 8-channel, +3V MAX117 analog-to-digital converter. Channel selection can be

controlled by switches located on the board or by user-

provided logic-level signals. An on-board oscillator provides continuous conversions, and the conversion

DESIGNATION	QTY	DESCRIPTION
C1, C4	2	22µF, 10V electrolytic capacitors
C2, C5–C8	5	0.1µF, 50V ceramic capacitors
C3	1	1000pF, 50V ceramic capacitor
J1	1	20-pin ribbon-cable connector
JU1, JU4	2	3-pin jumper headers
JU2, JU3, JU5–JU8	6	2-pin jumper headers
LED1-LED8	8	Red LEDs
N1	1	Low-R _{DS(ON)} , N-channel FET International Rectifier IRML2402*
R1, R2, R3, R5, R6	5	100k Ω resistors
R4, R7, R8	3	10k Ω resistors
R9	1	330Ω, 10-pin SIP resistor (installed, used with MAX117)
		560Ω, 10-pin SIP resistor (not installed, used with MAX118)
R10	1	5.1k Ω resistor
SW1	1	4-position DIP switch
U1	1	MAX117CPI
U2	1	74HC564 inverting 8-bit latch
U3	1	74HC04 hex inverter
None	8	Shunts
None	1	Printed circuit board
None	4	Rubber feet

Component List

General Description

_Ordering Information

PART	TEMP. RANGE	BOARD TYPE
MAX117EVKIT-DIP	0°C to +70°C	Through Hole
Note: To evaluate the M	AX118, request a MAX	(118CPI free sample.

_Quick Start

Verify operation of the MAX117 evaluation board by configuring for continuous conversions using the onboard oscillator. Take the following steps:

- 1) Verify that the jumpers are configured as shown in Table 1.
- Connect a +3V, 100mA supply to the VDD pad. A 20mA supply is sufficient if the LED output display is disabled. To evaluate the MAX118, use a +5V supply and replace the 330Ω, 10-pin SIP resistor (R9) with the 560Ω SIP resistor included in the kit.
- 3) Connect an input signal to the AIN1 input pad.
- On switch SW1, set A0, A1, and A2 to the on position and set PD to the off position (Table 3).
- Observe the conversion results on the LED display as the input voltage is varied between ground and the supply voltage.

*International Rectifier: (310) 322-3331

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Evaluates: MAX117/MAX118

Table 1. Jumper Configuration for Continuous-Conversion Operation

JUMPER	CONNECTION	FUNCTION
JU1	1 & 2	Connects REF- to the drain of N1 (see the <i>Shutdown</i> section).
JU2	Open	Sets the MAX117 for READ mode operation (MODE = low).
JU3	Shorted	Enables the LED display.
JU4	2 & 3	Connects the \overline{CS} pin to the on-board oscillator.
JU5	Shorted	Connects the MAX117's V _{DD} pin to the on-board EV kit's VDD pad.
JU6	Shorted	Connects the RD pin to the on-board oscillator.
JU7	Shorted	Connects the MAX117's REF+ pin to VDD.
JU8	Shorted	Connects a pull-up resistor to the WR/RDY pin.

_Detailed Description

MAX117 Reference Voltage

The MAX117 reference can be connected to any voltage between V_{DD} and ground (V_{REF} + \geq V_{REF}-). To use the power-supply rails as the reference voltage, short jumper JU7 and install a shunt on JU1 (in either position). When using an external reference, remove the shunts from JU1 and JU7, and apply your external reference source to the VREF + and VREF - pads.

Continuous Conversions

A 74HC04, configured as a free-running oscillator, provides the continuous-conversion signals (CS and RD) on the evaluation board. The oscillator output also clocks the MAX117 conversion results into a 74HC564 octal latch for display by eight LEDs.

The oscillator must be disconnected when the MAX117 is controlled by external circuitry. Remove the shunts from jumpers JU4, JU6, and JU8 to disconnect the on-board oscillator. For additional flexibility, the digital signals and data outputs are available at the 20-pin connector, J1.

Shutdown

When switch $\overline{\text{PD}}$ is set to the on position, the MAX117 enters shutdown mode. In shutdown mode, the supply current drops below 1µA (excluding the reference current). The internal resistance from REF+ to REF- is typically 2k Ω , and current will flow through it even when the MAX117 is shut down. The evaluation kit uses a low-RDS(ON), N-channel MOSFET (N1) to disconnect REF-from ground when the MAX117's $\overline{\text{PWRDN}}$ pin is driven low. This MOSFET has less than 1 Ω of on-resistance at 2.7V VGs to prevent an excessive offset error from appearing at REF-. Jumper JU1 allows the user to remove N1 from the circuit to evaluate user-specific circuit configurations.

Current Measurement

The current supplied to the board includes the current drawn by the MAX117, the on-board oscillator, the output latch, and the LED display. Jumper JU5 is in series with the MAX117 VDp pin to facilitate MAX117 supply-current measurements. Measure the MAX117 supply current or shutdown current by removing the shunt that normally shorts JU5, and connecting an ammeter across the two jumper pins. Refer to Table 2 for the appropriate jumper/switch settings.

Table 2. Jumper/Switch Settings forMAX117 Current Measurements

CURRENT	JUMI	PERS	SWITCH	
MEASUREMENT	JU1	JU7	PD	
Supply Current, VREF = VDD (including reference current)	2 & 3 or 1 & 2	Shorted	Off	
Supply Current (excluding reference current	Open	Open	Off	
Power-Down Current	1 & 2	N/A	On	

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Jumper Selections

Jumpers JU1–JU8 allow the user various configuration options for the EV kit. Table 3 lists the various jumper functions.

Switch Selections

lect an -down r-down pin (PWRDN) are pulled up to V_{DD} through a 100k Ω ound when the in the on posisition for extertion or power-

internally or can be driven by external circuitry.	resisto	r. Thes	e pins a	are cor	p to V _{DD} through a 100kΩ nnected to ground when the A2, or PD) is in the on posi-
Connects the MODE pin to V_{DD} .	tion. Th nal log	ne swite jic to c	ches m	ust be	in the off position for exter- hannel selection or power-
Disables the LED display.	down r	node.			
Enables the LED display.	Table			D C	itah Calaatiana
Connects the $\overline{\text{CS}}$ pin to ground.	Table	; 4. J		-	itch Selections
Connects the $\overline{\text{CS}}$ pin to the	A0	A1	A2	PD	INPUT CHANNEL
on-board oscillator.	On	On	On	Off	IN1
The $\overline{\text{CS}}$ pin must be controlled		IN2			
by external logic.	On	Off	On	Off	IN3
Connects an ammeter across the pins to measure the supply	Off	Off	On	Off	IN4
current.	On	On	Off	Off	IN5
Connects the MAX117's VDD	Off	On	Off	Off	IN6
pin to the VDD pad.	the VDD pad. On Off Off IN7	IN7			
The $\overline{\text{RD}}$ pin must be controlled by external logic.	Off	Off	Off	Off	IN8, analog input internally connected to REF+; or chan-
Connects the \overline{RD} pin to the on-board oscillator.					nel selection controlled externally.
An external source must be connected to the REF+ pad.	Х	х	х	On	The MAX117 is in shutdown (PWRDN grounded).
REF+ pin connected to V _{DD} .					

The four-position DIP switch (SW1) is used to sele
input channel and to enable/disable power-
mode. The address pins (A0-A2) and the power

JU2	Open	external circuitry.				
	Shorted	Connects the MODE pin to V_{DD} .				
JU3	Open	Disables the LED display.				
103	Shorted	Enables the LED display.				
JU4	1 & 2	Connects the $\overline{\text{CS}}$ pin to ground.				
	2 & 3	Connects the \overline{CS} pin to the on-board oscillator.				
	Open	The $\overline{\text{CS}}$ pin must be controlled by external logic.				
JU5	Open	Connects an ammeter across the pins to measure the supply current.				
	Shorted	Connects the MAX117's V_{DD} pin to the VDD pad.				
JU6 -	Open	The $\overline{\text{RD}}$ pin must be controlled by external logic.				
	Shorted	Connects the RD pin to the on-board oscillator.				
JU7 -	Open	An external source must be connected to the REF+ pad.				
	Shorted	REF + pin connected to V_{DD} .				
JU8	Open	No pull-up connected to the WR/RDY pin.				
	Shorted	Connects a 5.1k Ω pull-up resistor to the \overline{WR}/RDY pin.				

FUNCTION

Connects REF- pin to the drain

Connects REF- pin to ground.

The MODE pin is pulled low

of MOSFET N1 (see the

Shutdown section).

Table 3. Jumper Functions POSITION

1 & 2

2&3

Open

JUMPER

JU1

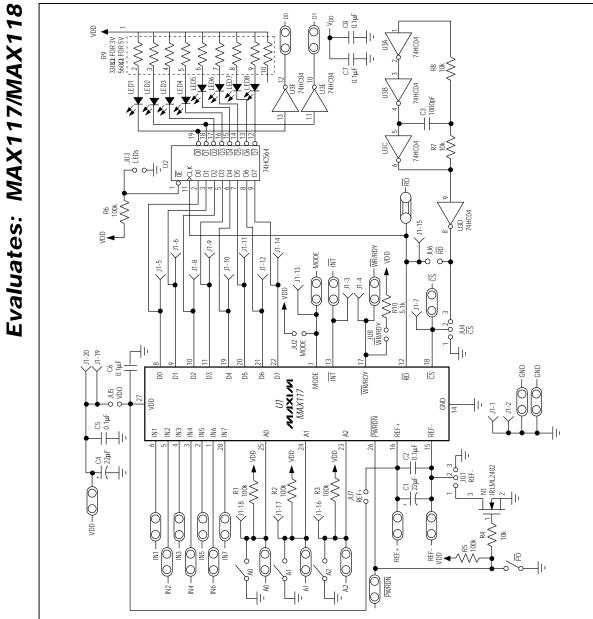


Figure 1. MAX117 EV Kit Schematic

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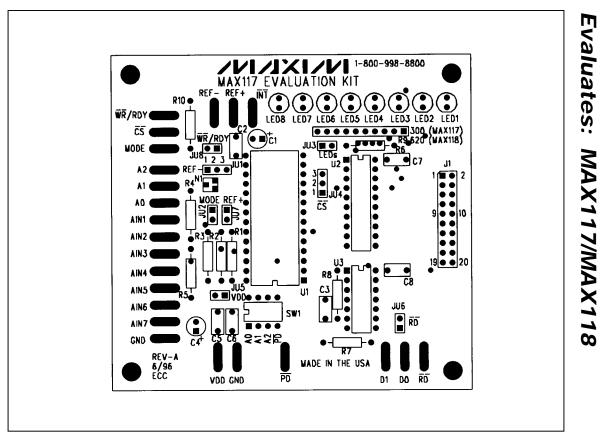


Figure 2. MAX117 EV Kit Component Placement Guide—Component Side

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Figure 3. MAX117 EV Kit PC Board Layout—Component Side

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MAX117 Evaluation Kit

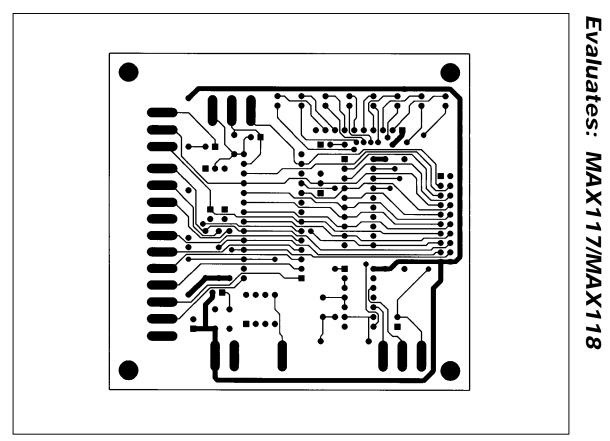


Figure 4. MAX117 EV Kit PC Board Layout—Solder Side

Evaluates: MAX117/MAX118

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NOTES

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