19-4356; Rev 0; 10/08

Battery Switch with Four Enable Inputs

General Description

The MAX14525 features a low R_{ON} 35mΩ (typ) load switch with four unique enable inputs. The MAX14525 is ideal for disconnecting the lithium-ion (Li+) battery from the loads in portable devices such as cell phones. The MAX14525 operates from a +2.2V to +5.5V supply voltage.

The MAX14525 features an extremely low 0.8µA (typ) quiescent supply current to maximize battery life in portable devices. It is enabled from four possible inputs: external charger connection capable of high voltage up to +28V, travel adapter (TA), on key (ON_K), factory mode enable (JIG), and switch enable (S_EN). The S_EN input is internally ANDed with the switched battery connection (IN).

The MAX14525 is available in a small 8-pin, 2mm x 2mm TDFN package and operates over the -40°C to +85°C extended temperature range.

Applications

Cell Phones

PDAs

GPS

UMPC Computers

Digital Cameras

Features

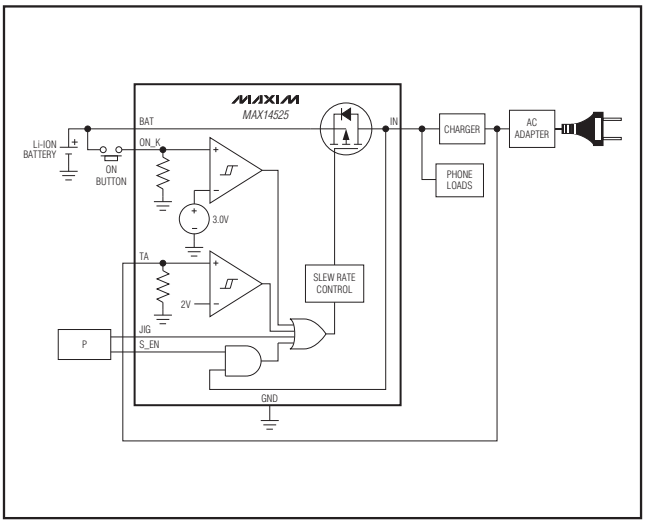
- **Low 35mΩ (typ) R_{ON} Load Switch**
- **Ultra Low, 0.8µA (typ) Supply Current**
- **Four Enable Inputs:**
  - **TA: +28V (max) Capable**
  - **ON_K: Accurate +3V Trigger Enable**
  - **JIG: Factory Mode Enable**
  - **S_EN: Logically ANDed with IN**
- **Space-Saving 8-Pin, 2mm x 2mm TDFN Package**
- **Controlled Turn-On to Limit dI/dt Pulses Due to Lead Inductance**

Ordering Information

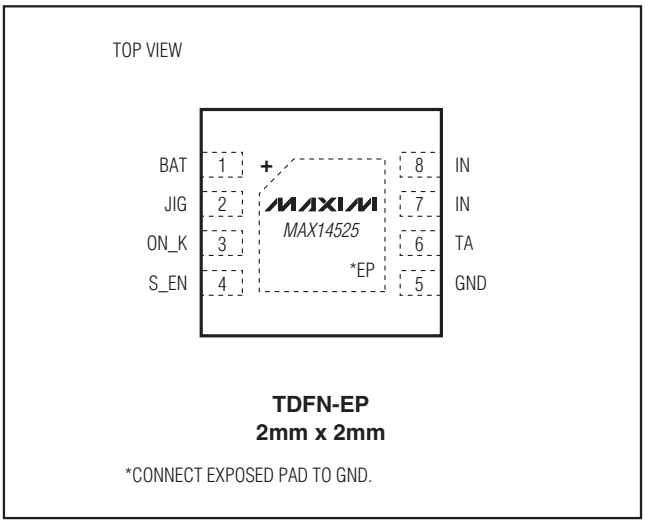
PART	PIN-PACKAGE	TOP MARK
MAX14525ETA+T	8 TDFN-EP*	ACQ

Note: *The device is specified over the -40°C to +85°C operating temperature range.*
+Denotes a lead-free/RoHS-compliant package.
**EP = Exposed pad.*

Typical Application Circuit



Pin Configuration



TDFN-EP
2mm x 2mm

*CONNECT EXPOSED PAD TO GND.

ABSOLUTE MAXIMUM RATINGS

(All voltages referenced to GND.)
IN, BAT, JIG, S_EN, ON_K ..... -0.3V to +6.0V
TA .....-0.3V to +28V
Continuous Power Dissipation (T_A = +70°C)
8-Pin TDFN (derate 11.9 mW/°C above +70°C) .....954 mW
Junction-to-Case Thermal Resistance
(θ_{JC}) (Note 1) .....37°C/W
Junction-to-Ambient Thermal Resistance
(θ_{JA}) (Note 1) .....84°C/W
Operating Temperature Range ..... -40°C to +85°C
Junction Temperature..... +150°C
Storage Temperature Range ..... -65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to **www.maxim-ic.com/thermal-tutorial**.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{BAT} = +2.2V to +5.5V, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at V_{BAT} = +3.6V and T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC CHARACTERISTICS						
Input Voltage Range	V_{BAT}, V_{IN}		2.2		5.5	V
On-Resistance	R_{ON}	I_{LOAD} = 100mA, V_{BAT} = +3.0V		35	90	mΩ
Disable Supply Current	I_{BAT_DIS}	V_{BAT} = +5.5V (V_{JIG} = V_{S_EN} = V_{ON_K} = V_{TA} = V_{IN} = 0)			1	µA
V_{BAT} Supply Current	I_{BAT}	V_{JIG} = V_{S_EN} = V_{BAT} , V_{ON_K} = V_{TA} = 0		0.8	4.5	µA
Increase in Supply Current with V_{JIG}/V_{S_EN} Voltage	ΔI_{BAT}	V_{JIG} = V_{S_EN} = V_{IH} or V_{IL}			2	µA
Increase in Supply Current with V_{ON_K} Voltage	ΔI_{BAT}	V_{BAT} = V_{ON_K} = +3.6V			4.5	µA
Peak Current	I_{LIM}	V_{BAT} = +3.6V	5			A
UVLO Undervoltage Lockout		Ramping V_{BAT}			1.9	V
LOGIC INPUT						
TA Threshold Voltage	V_{TA_TH}		1.15	1.7	2.5	V
TA Threshold Hysteresis				1%		
TA Input Resistance		V_{TA} = 1V	50	100	180	kΩ
JIG, S_EN Input Logic-High	V_{IH}		1.4			V
JIG, S_EN Input Logic-Low	V_{IL}				0.4	V
JIG, S_EN Input Leakage Current	I_{IN}	V_{BAT} = +5.5V	-200		+200	nA
IN AND Gate Threshold Voltage	V_{IN_TH}		0.3 x V_{BAT}		0.6 x V_{BAT}	V
ON_K Threshold Voltage	$V_{ON\_K\_TH}$	Low-to-high transition (Figure 1)	2.94	3.0	3.06	V
ON_K Threshold Hysteresis				1%		

ELECTRICAL CHARACTERISTICS (continued)

(V_{BAT} = +2.2V to +5.5V, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at V_{BAT} = +3.6V and T_A = +25°C.) (Note 2)

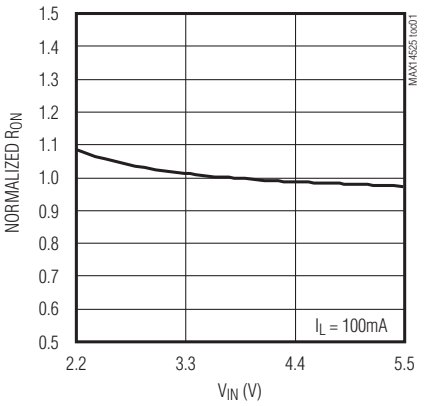
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
ON_K Input Leakage Current	I_{ON_K}	V_{BAT} = V_{ON_K} = +3.6V			3	µA
SWITCH DYNAMICS (R_L = 20Ω, C_L = 0.1µF) (Figure 2)						
Turn-On Delay Time	t_{ONDLY}	From any enable high to V_{IN} = 10% of V_{BAT}		600	2600	µs
Turn-On Rise Time	t_{ONRISE}	V_{IN} 10% to 90% of V_{BAT}	500	1800	5000	µs
Turn-Off Delay Time	t_{OFFDLY}	From any enable low to V_{IN} = 90% of V_{BAT}		130	300	µs
Turn-Off Fall Time	$t_{OFFFALL}$	V_{IN} 90% to 10% of V_{BAT}		60	150	µs

Note 2: Devices are tested at T_A = +25°C. Specifications over temperature are guaranteed by design.

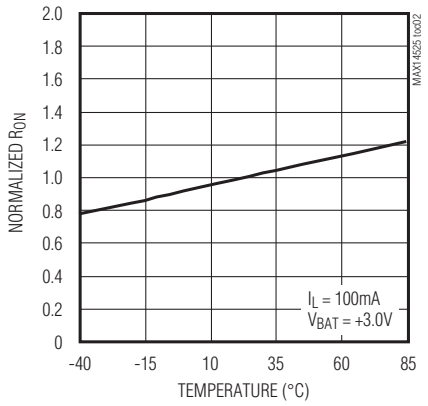
Typical Operating Characteristics

(V_{IN} = +3.6V, T_A = +25°C, unless otherwise noted.)

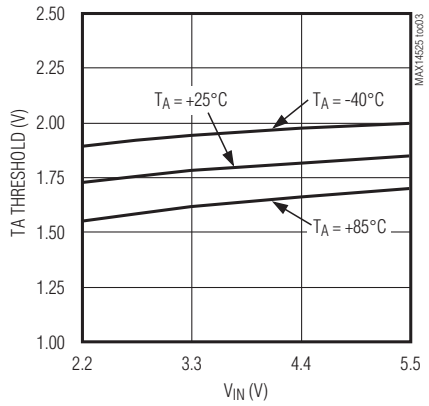
NORMALIZED R_{ON} vs. V_{IN}



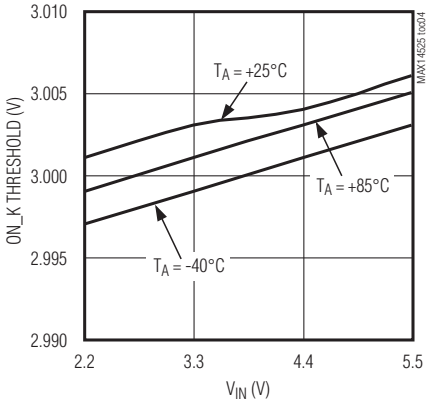
NORMALIZED R_{ON} vs. TEMPERATURE



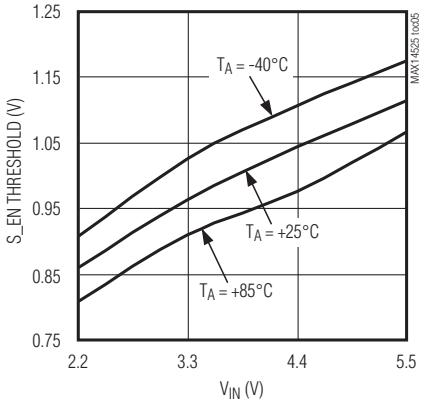
TA THRESHOLD vs. V_{IN}



ON_K THRESHOLD vs. V_{IN}



S_EN THRESHOLD vs. V_{IN}

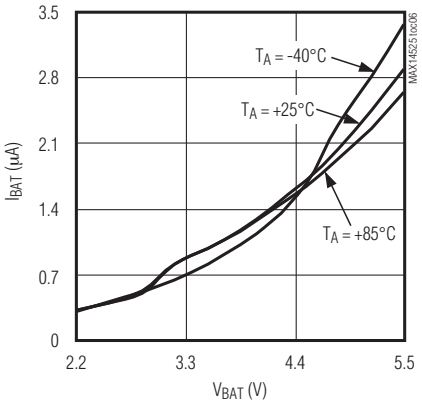


Battery Switch with Four Enable Inputs

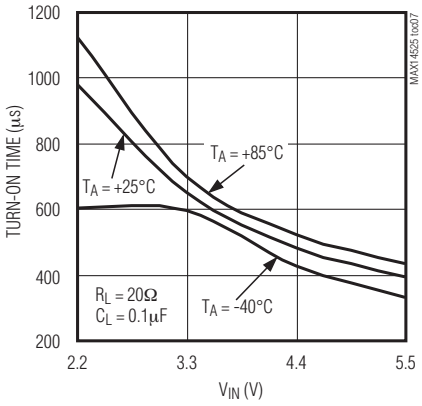
Typical Operating Characteristics (continued)

(V_{IN} = +3.6V, T_A = +25°C, unless otherwise noted.)

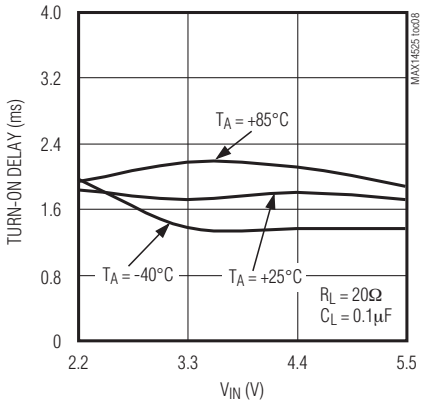
BAT CURRENT vs. VOLTAGE



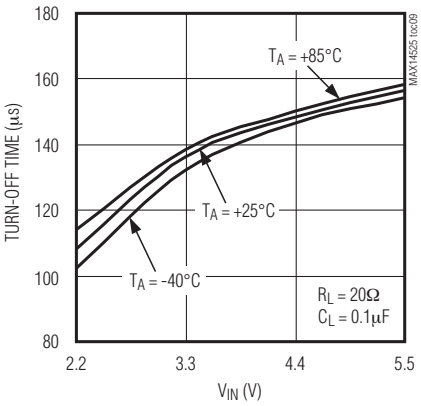
TURN-ON TIME vs. V_{IN}



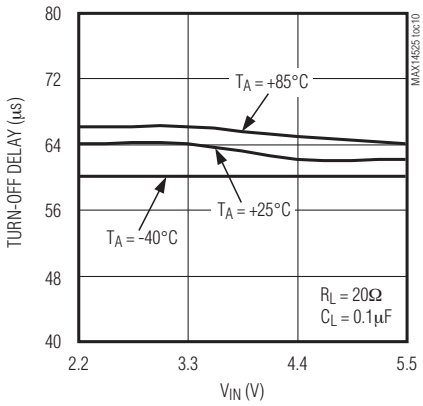
TURN-ON DELAY vs. V_{IN}



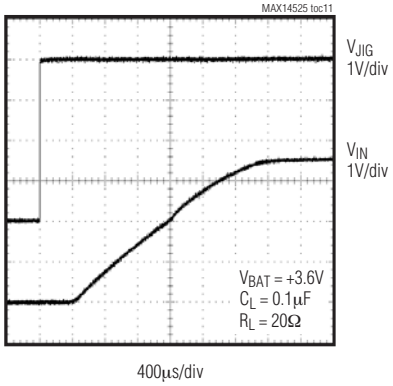
TURN-OFF TIME vs. V_{IN}



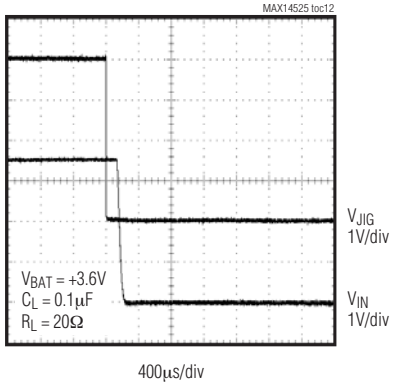
TURN-OFF DELAY vs. V_{IN}



TURN-ON RESPONSE



TURN-OFF RESPONSE



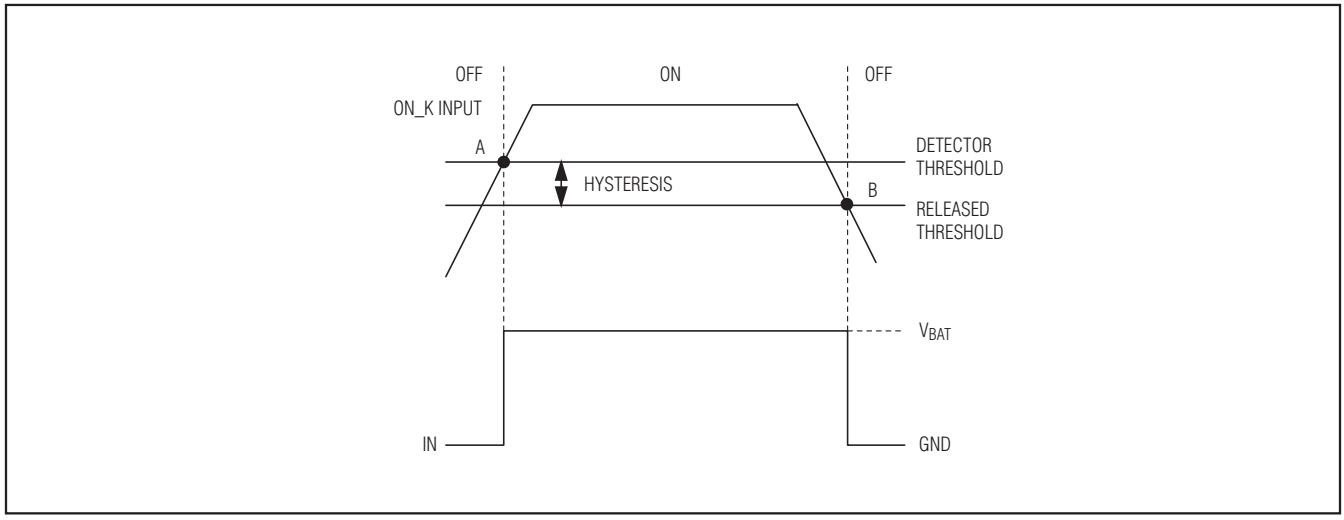


Figure 1. ON_K Input Operation Diagram

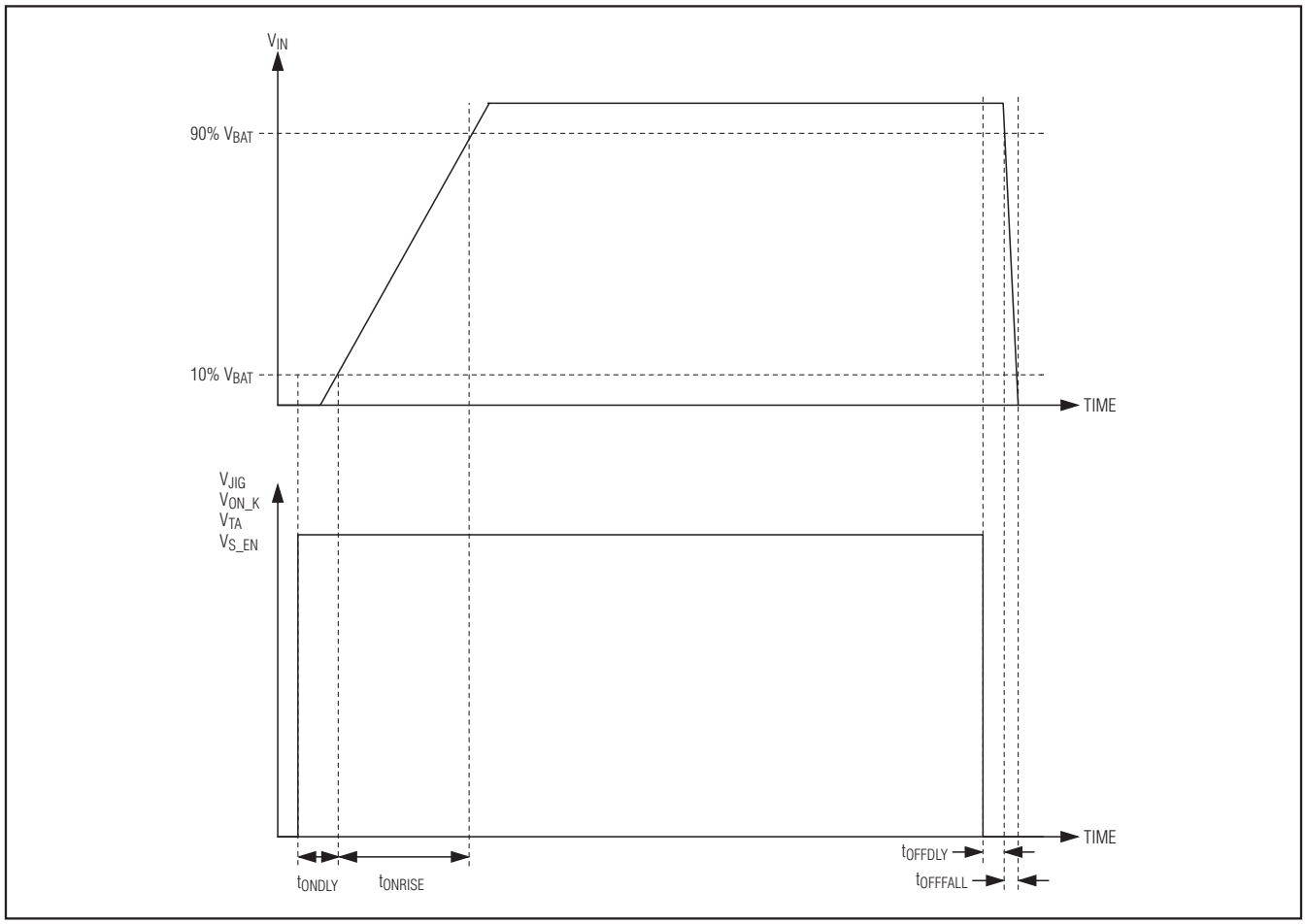


Figure 2. Turn-On Delay Time, Turn-On Rise Time, Turn-Off Delay Time, and Turn-Off Fall Time

Battery Switch with Four Enable Inputs

Pin Description

PIN	NAME	FUNCTION
1	BAT	Lithium-Ion (Li+) Battery Connection
2	JIG	Enable Input with Standard Logic Threshold
3	ON_K	Enable Input with Accurate Threshold (+3.0V)
4	S_EN	Enable Input with Standard Logic Threshold Logically ANDed with IN
5	GND	Ground
6	TA	Enable Input with High Threshold
7, 8	IN	Power Switch Input. The power switch input voltage range is from +2.2V to +5.5V. Connect a 0.1µF capacitor from IN to GND. Connect pins 7 and 8 together for proper operation.
—	EP	Exposed Pad. Connect EP to ground. Do not use EP as the only ground connection.

Detailed Description

The MAX14525 features a low 35mΩ (typ) R_{ON} load switch with four unique enable inputs. The MAX14525 can be used to disconnect the lithium-ion battery from the loads in portable devices such as cell phones. It operates from a +2.2V to +5.5V supply voltage.

The MAX14525 features an ultra-low 0.8µA (typ) quiescent supply current to maximize battery life in portable devices. The device is enabled from four possible inputs: external charge connection travel adapter (TA), on key (ON_K), factory mode enable (JIG), and switch enable (S_EN). The S_EN input is internally ANDed with the switched battery connection (IN).

TA Input

The TA input on the MAX14525 can be connected directly to the external charger source. The TA input is high-voltage capable (+28V max) and features a high threshold voltage to limit false voltage trips, and an input resistance of 100kΩ (typ) to ground.

ON_K Input

The ON_K line is active high and is pulled up to the lithium-ion battery through a momentary push button switch. This input features an accurate voltage detector threshold which does not enable the load switch until the battery threshold is above +3.0V ±2%. When the battery has a very low charge and the on key is pressed, the accurate threshold does not allow the phone to boot up.

JIG Input

The JIG input on the MAX14525 is a logic-level input (+1.8V compatible) from an external source to indicate the device has been connected to a factory cable. This signal requires a standard logic-input threshold voltage (+1.4V high).

S_EN Input

The S_EN input on the MAX14525 is a logic-level input (+1.8V compatible) from an external source used to hold the switch on when the triggering condition (TA, ON_K, or JIG) is removed. The standard logic threshold voltage comes from the host microprocessor that pulls S_EN high once the code has begun running on the microprocessor. The S_EN input is internally ANDed with the voltage on IN. The IN connection to the AND gate thresholds are standard CMOS values of 1/3 and 2/3 of V_{BAT}.

Chip Information

PROCESS: CMOS

Package Information

For the latest package outline information and land patterns, go to **www.maxim-ic.com/packages**.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
8 TDFN-EP	T822+2	**21-0168**

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