

General Description

The MAX1481/MAX1484/MAX1485/MAX1486 provide software-selectable, half-/full-duplex, low-power, slewrate-limited, and high-speed (12Mbps) RS-485/RS-422 operation in a 10-pin µMAX® package—the smallest 10-pin package available.

The software-selectable, half-/full-duplex MAX1485/ MAX1486 make obsolete the normally larger and more expensive solutions required for selectable half-/fullduplex RS-485/RS-422 operation: 1) a 14-pin, fullduplex transceiver configured via jumpers between receiver and transmitter lines, or 2) two 8-pin, halfduplex transceivers, which require an additional inverter logic gate for software selectability.

The MAX1481/MAX1484 are functionally equivalent to the industry-standard MAX491 and 75180, and are designed for space-constrained, full-duplex RS-422 applications.

All parts contain one driver and one receiver and feature a 1/8-unit-load receiver input impedance, allowing up to 256 transceivers on the bus. The MAX1481/ MAX1485 feature reduced-slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free data transmission up to 250kbps. The MAX1484/MAX1486 driver slew rates are not limited, allowing them to transmit up to 12Mbps.

The MAX1481/MAX1484/MAX1485/MAX1486 draw only 300µA of supply current. The MAX1481 has a low-power shutdown mode that reduces supply current to only 0.1µA. All devices operate from a single 5V supply.

Drivers are output short-circuit current limited and are protected against excessive power dissipation by thermal-shutdown circuitry that places the driver outputs into a high-impedance state. The receiver input has a failsafe feature that guarantees a logic-high output if the input is open circuit.

Features

- ♦ 10-Pin µMAX Package: Smallest 10-Pin Package
- **♦** Software-Selectable Half-/Full-Duplex Operation (MAX1485/MAX1486)
- ♦ 0.1µA Low-Current Shutdown Mode (MAX1481)
- ♦ Slew-Rate Limiting Allows Error-Free Data Transmission (MAX1481/MAX1485)
- **♦ 12Mbps High-Speed Operation** (MAX1484/MAX1486)
- Allow up to 256 Transceivers on the Bus

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX1481CUB	0°C to +70°C	10 μMAX
MAX1481EUB	-40°C to +85°C	10 μMAX
MAX1484CUB	0°C to +70°C	10 μMAX
MAX1484EUB	-40°C to +85°C	10 μMAX
MAX1485CUB	0° C to $+70^{\circ}$ C	10 μMAX
MAX1485EUB	-40°C to +85°C	10 μMAX
MAX1486CUB	0°C to +70°C	10 μMAX
MAX1486EUB	-40°C to +85°C	10 μMAX

Applications

Low-Power RS-422/RS-485 Communications

Level Translators

Hand-Held Equipment

Battery-Powered Equipment

Transceiver for EMI-Sensitive Applications

Industrial-Control Local Area Networks

μΜΑΧ is a registered trademark of Maxim Integrated Products, Inc.

Selection Table

Part	Half/Full Duplex	Data Rate (Mbps)	Slew- Rate Limited	Low- Power Shutdown	Driver Enable	Receiver Enable	Quiescent Current (µA)	Transceivers on Bus	Pin- Package
MAX1481	Full	0.250	Yes	Yes	Yes	Yes	300	256	10 μMAX
MAX1484	Full	12	No	No	Yes	Yes	300	256	10 μMAX
MAX1485	Selectable	0.250	Yes	No	Yes	No	300	256	10 μMAX
MAX1486	Selectable	12	No	No	Yes	No	300	256	10 μMAX

MIXIM

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{CC})	Continuous Power Dissipation 10-Pin µMAX (derate 5.6mW/°C above +70°C)444mW Operating Temperature Ranges MAX148_C0°C to +70°C MAX148_E40°C to +85°C Storage Temperature Range65°C to +160°C
Receiver Input Voltage, Full Duplex (A, B)8V to +12.5V Receiver Output Voltage (RO)0.3V to (V _{CC} + 0.3V)	Storage Temperature Range65°C to +160°C Lead Temperature (soldering, 10sec)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = +5V ±5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +5V and T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS	
DRIVER	•							
	Vod1	No load, Figure 5				5		
Differential Driver Output	Vana	R = 50Ω (RS-422), Fig	ure 5	2.0			V	
	VOD2	R = 27Ω (RS-485), Fig	ure 5	1.5		5		
Change in Magnitude of Differential Output Voltage (Note 2)	ΔV _{OD}	$R = 50\Omega$ or 27Ω , Figure 5				0.2	V	
Driver Common-Mode Output Voltage	Voc	$R = 50\Omega$ or 27Ω , Figure	$R = 50\Omega$ or 27Ω , Figure 5			3	V	
Change in Magnitude of Common-Mode Voltage (Note 2)	ΔV _{OC}	$R = 50\Omega$ or 27Ω , Figure 5				0.2	V	
Input High Voltage	V _{IH1}	DE, DI, RE, H/F		2.0			V	
Input Low Voltage	V _{IL1}	DE, DI, RE, H/F				0.8	V	
Input Current	liN1	DE, DI, RE, H/F				±2	μΑ	
Input Current (Y and Z for Half	l _{IN2}	DE = GND,	V _{IN} = 12V			0.125	mA	
Duplex, A and B for Full Duplex)	IIIN2	$V_{CC} = GND \text{ or } 5.25V$	$V_{IN} = -7V$			-0.1	IIIA	
Output Leakage (Y and Z)	Io	DE = GND	VIN = 12V			10	μA	
(MAX1481/MAX1484 Only)	10	DL - GND	V _{IN} = -7V			-10	μΑ	
Output Leakage (Y and Z)	Io	DE = GND	V _{IN} = 12V			125		
(MAX1485/MAX1486 Only)	10	DL - GND	VIN = -7V			-100	μΑ	
Driver Output Short-Circuit Current (Note 3)	I _{OSD}	-7V ≤ V _{OUT} ≤ 12V		35		250	mA	

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = +5V \text{ and } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
RECEIVER	•		<u>.</u>			
Receiver Differential Threshold Voltage	V _{TH}	-7V ≤ V _{CM} ≤ 12V	-200		200	mV
Receiver Input Hysteresis	ΔVTH			70		mV
Receiver Output High Voltage	Voh	$I_O = -4mA$, $V_{ID} = 200mV$	3.5			V
Receiver Output Low Voltage	Vol	$I_O = 4mA$, $V_{ID} = -200mV$			0.4	V
Three-State Output Current at Receiver	lozr	0.4V ≤ V _O ≤ 2.4V			±1	μΑ
Receiver Input Resistance	RIN	-7V ≤ V _{CM} ≤ 12V	96			kΩ
Receiver Output Short-Circuit Current	Iosr	0V ≤ V _{RO} ≤ V _{CC}			±95	mA
SUPPLY CURRENT			<u>, </u>			1
No-Load Supply Current	Icc	RE = GND, DE = VCC		300	600	μA
Supply Current in Shutdown Mode (MAX1481 Only)	Ishdn	DE = GND, RE = VCC		0.1	10	μА

SWITCHING CHARACTERISTICS (MAX1484/MAX1486)

 $(V_{CC} = 5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted.}$ Typical values are at $V_{CC} = +5V \text{ and } T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	tDPLH	RDIFF = 54Ω , $C_{L1} = C_{L2} = 100pF$,		30	60	ns
Driver input to Output	tdphl	Figures 7 and 9		30	60	113
Driver Output Skew tdph - tdphl	tdskew	$R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$, Figures 7 and 9		5	10	ns
Driver Rise or Fall Time	t _{DR} , t _{DF}	$R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$, Figures 7 and 9	5	15	35	ns
Maximum Data Rate	fmax		12			Mbps
Driver Enable to Output Low	t _{DZL}	C _L = 100pF, S1 closed, Figures 8 and 10		40	70	ns
Driver Enable to Output High	tdzh	C _L = 100pF, S2 closed, Figures 8 and 10		40	70	ns
Driver Disable Time from Low	tDLZ	C _L = 15pF, S1 closed, Figures 8 and 10		40	70	ns
Driver Disable Time from High	tDHZ	C _L = 15pF, S2 closed, Figures 8 and 10		40	70	ns
Receiver Input to Output	t _{RPLH} , t _{RPHL}	Figures 11 and 13		90	150	ns
t _{RPLH} - t _{RPHL} Differential Receiver Skew	t _{RSKD}	Figures 11 and 13		5		ns
Receiver Enable to Output Low	t _{RZL}	C _L = 100pF, S1 closed, Figures 6 and 12		20	50	ns
Receiver Enable to Output High	t _{RZH}	C _L = 100pF, S2 closed, Figures 6 and 12		20	50	ns
Receiver Disable Time from Low	tRLZ	C _L = 100pF, S1 closed, Figures 6 and 12		20	50	ns
Receiver Disable Time from High	trhz	C _L = 100pF, S2 closed, Figures 6 and 12		20	50	ns

SWITCHING CHARACTERISTICS (MAX1481/MAX1485) (continued)

 $(V_{CC} = 5V \pm 5\%, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +5V \text{ and } T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	tdplh	$R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$,		600	1000	ns
Driver input to Output	tDPHL	Figures 7 and 9		600	1000	115
Driver Output Skew tophh - tophh	tDSKEW	$R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$, Figures 7 and 9		10	200	ns
Driver Rise or Fall Time	t _{DR} , t _{DF}	$R_{DIFF} = 54\Omega$, $C_{L1} = C_{L2} = 100pF$, Figures 7 and 9	250		1000	ns
Maximum Data Rate	f _{MAX}		250			kbps
Driver Enable to Output Low	tDZL	C _L = 100pF, S1 closed, Figures 8 and 10			3000	ns
Driver Enable to Output High	tdzh	C _L = 100pF, S2 closed, Figures 8 and 10			3000	ns
Driver Disable Time from Low	tDLZ	C _L = 15pF, S1 closed, Figures 8 and 10			200	ns
Driver Disable Time from High	tDHZ	C _L = 15pF, S2 closed, Figures 8 and 10			200	ns
Receiver Input to Output	t _{RPLH} , t _{RPHL}	Figures 11 and 13		90	150	ns
t _{RPLH} - t _{RPHL} Differential Receiver Skew	trskd	Figures 11 and 13		15		ns
Receiver Enable to Output Low	t _{RZL}	C _L = 100pF, S1 closed, Figures 6 and 12		20	50	ns
Receiver Enable to Output High	trzh	C _L = 100pF, S2 closed, Figures 6 and 12		20	50	ns
Receiver Disable Time from Low	t _{RLZ}	C _L = 100pF, S1 closed, Figures 6 and 12		20	50	ns
Receiver Disable Time from High	t _{RHZ}	C _L = 100pF, S2 closed, Figures 6 and 12		20	50	ns
Time to Shutdown	tshdn	MAX1481 only (Note 4)	50	200	600	ns
Driver Enable from Shutdown to Output High	[†] DZH(SHDN)	MAX1481 only, C _L = 15pF, S2 closed, Figures 8 and 10			3000	ns
Driver Enable from Shutdown to Output Low	tdzl(SHDN)	MAX1481 only, C _L = 15pF, S1 closed, Figures 8 and 10			3000	ns
Receiver Enable from Shutdown to Output High	trzh(shdn)	MAX1481 only, C _L = 100pF, S2 closed, Figures 6 and 12			500	ns
Receiver Enable from Shutdown to Output Low	tRZL(SHDN)	MAX1481 only, C _L = 100pF, S1 closed, Figures 6 and 12			1000	ns

- **Note 1:** All currents into the device are positive; all currents out of the device are negative. All voltages are referenced to device ground unless otherwise noted.
- Note 2: ΔV_{OD} and ΔV_{OC} are the changes in V_{OD} and V_{OC}, respectively, when the DI input changes state.
- **Note 3:** Maximum current level applies to peak current just prior to foldback-current limiting; minimum current level applies during current limiting.
- Note 4: Shutdown is enabled by bringing RE high and DE low. If the enable inputs are in this state for less than 50ns, the MAX1481 is guaranteed not to enter shutdown. If the enable inputs are in this state for at least 600ns, the MAX1481 is guaranteed to have entered shutdown.

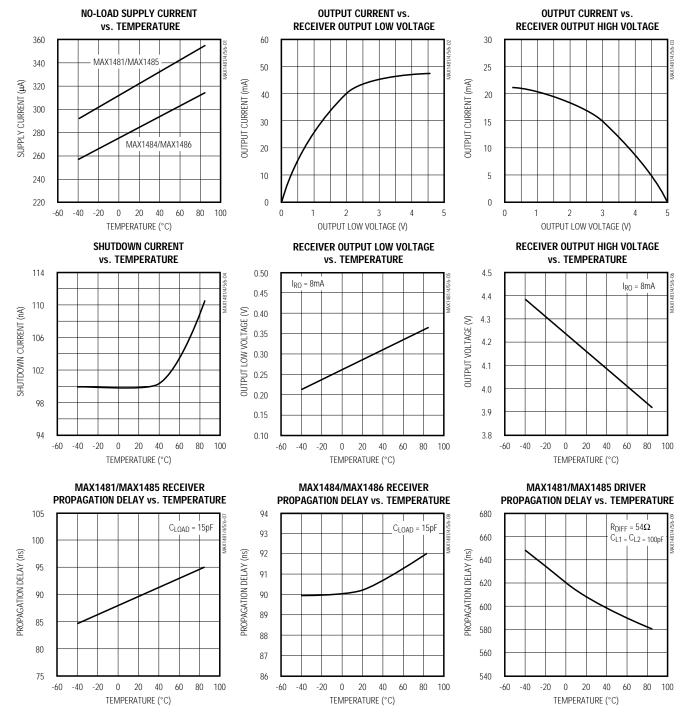
5

Software-Selectable, Half-/Full-Duplex, Slew-Rate-Limited, 12Mbps, RS-485/RS-422 Transceivers in µMAX Package

Typical Operating Characteristics

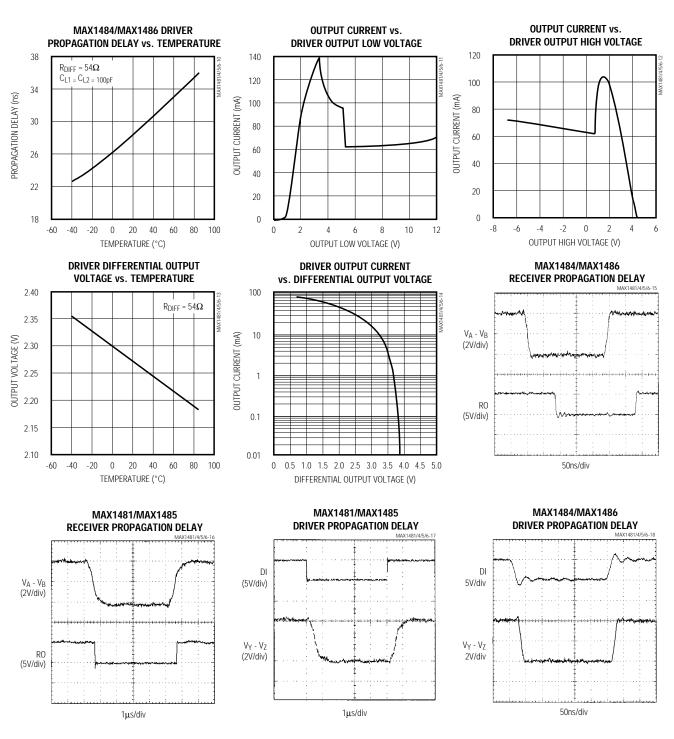
 $(V_{CC} = +5V, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

NIXIN



_Typical Operating Characteristics (continued)

 $(V_{CC} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$



__Pin Description

	PIN			
MAX1481/ MAX1484	MAX1485	/MAX1486	NAME	FUNCTION
WAX1484	H/F = 0	H/F = 1		
1	1	1	RO	Receiver Output. When \overline{RE} is low and if A - B \geq 200mV, RO goes high; if A - B \leq -200mV, RO goes low.
_	2	2	H/F	Half-/Full-Duplex Selector Input. Connect H/F to V _{CC} for half-duplex mode, and connect to GND for full-duplex mode.
2	_	_	RE	Receiver Output Enable Input. Drive RE low to enable RO; RO is high impedance when RE is high. For MAX1481 only, drive RE high and DE low to enter the low-power shutdown mode.
3	3	3	DE	Driver Output Enable Input. Drive DE high to enable driver outputs. These outputs are high impedance when DE is low.
4	4	4	DI	Driver Input. With DE high, a low on DI forces noninverting output low and inverting output high. Similarly, a high on DI forces noninverting output high and inverting output low.
5	5	5	GND	Ground
6	6	_	В	Inverting Receiver Input
_	_	6	В	Receiver Input Resistors*
7	7	_	Z	Inverting Driver Output
_	_	7	Z	Inverting Driver Output and Inverting Receiver Input
8	8	_	Υ	Noninverting Driver Output
_	_	8	Υ	Noninverting Driver Output and Noninverting Receiver Input
9	9	_	А	Noninverting Receiver Input
_	_	9	А	Receiver Input Resistors*
10 10 10		Vcc	Positive Supply; $+4.75V \le V_{CC} \le +5.25V$	

^{*(}MAX1485/MAX1486 only) In half-duplex mode, the driver outputs serve as receiver inputs. The full-duplex receiver inputs (A and B) will still have a 1/8-unit load, but are not connected to the receiver.

_____Function Tables

MAX1481

TRANSMITTING							
	INPUTS	OUTPUTS					
RE	DE	DI	Z	Υ			
Χ	1	1	0	1			
Χ	1	0	1	0			
0	0	Х	High-Z	High-Z			
1	0	Х	High-Z and Shutdown				

RECEIVING							
	INPUTS		OUTPUT				
RE	DE	A-B	RO				
0	X	≥ 0.2V	1				
0	Х	≤ -0.2V	0				
1	1	X	High-Z				
1	0	Х	High-Z and Shutdown				

MAX1484

TRANSMITTING							
	INPUTS	OUTI	PUTS				
RE	DE	DI	Z	Υ			
Х	1	1	0	1			
X	1	0	1	0			
X	0	Χ	High-Z	High-Z			

RECEIVING							
	OUTPUT						
RE	DE	A-B	RO				
0	Х	≥ 0.2V	1				
0	X	≤ -0.2V	0				
1	Х	Х	High-Z				

MAX1485/MAX1486

TRANSMITTING					
INPUTS		OUTPUTS			
DE	DI	Z	Υ		
1	1	0	1		
1	0	1	0		
0	Х	High-Z	High-Z		

RECEIVING						
INPUTS			OUTPUT			
H/F	DE	А-В	Y-Z	RO		
0	Х	≥ 0.2V	Х	1		
0	Х	≤ -0.2V	Х	0		
1	0	Х	≥ 0.2V	1		
1	0	Х	≤ -0.2V	0		

X = Don't care

Note: In shutdown mode, driver and receiver outputs are high impedance.

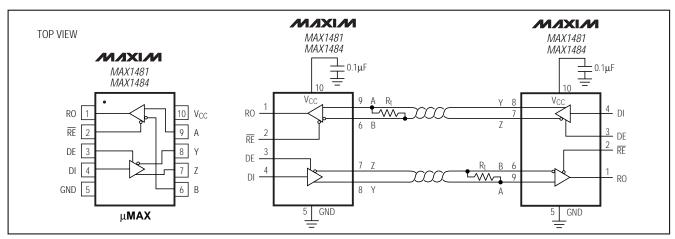


Figure 1. MAX1481/MAX1484 Pin Configuration and Typical Full-Duplex Operating Circuit

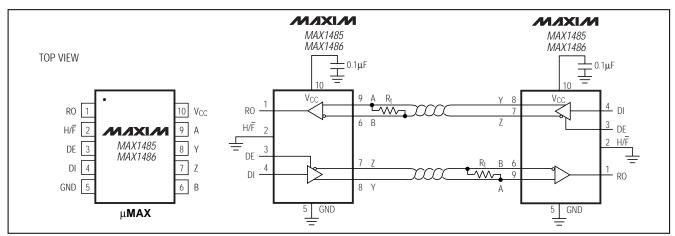


Figure 2. MAX1485/MAX1486 Pin Configuration and Equivalent Typical Full-Duplex Operating Circuit

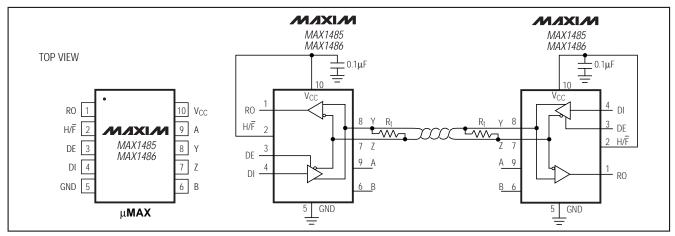


Figure 3. MAX1485/MAX1486 Pin Configuration and Equivalent Typical Half-Duplex Operating Circuit

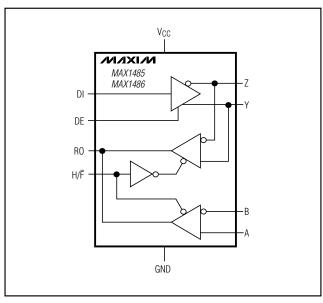


Figure 4. MAX1485/MAX1486 Functional Diagram

Detailed Description

The MAX1481/MAX1484/MAX1485/MAX1486 high-speed transceivers for RS-485/RS-422 communication contain one driver and one receiver. The MAX1481/MAX1485 feature reduced-slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free data transmission up to 250kbps. The MAX1484/MAX1486 driver slew rates are not limited, making transmission speeds up to 12Mbps possible.

These transceivers are designed to operate on a +5V single supply and typically draw $300\mu A$ of supply current when unloaded or fully loaded with the drivers disabled. The MAX1481 has a shutdown mode in which supply current is typically reduced to $0.1\mu A$.

Drivers are output short-circuit current limited and are protected against excessive power dissipation by thermal-shutdown circuitry that places the driver outputs into a high-impedance state.

All devices have a 1/8-unit-load receiver input impedance that allows up to 256 transceivers on the bus. The MAX1481/MAX1484 are designed for full-duplex communications. The H/\overline{F} pin on the MAX1485/MAX1486 allows the user to select between half-duplex or full-duplex operation (Figure 4).

MAX1485/MAX1486 Half-/Full-Duplex Mode Operation

The MAX1484/MAX1485 can operate in full- or half-duplex mode. Drive the H/\overline{F} pin low or connect it to GND for full-duplex operation, or drive it high for half-duplex operation. In full-duplex mode, the pin configuration of the driver and receiver is the same as a MAX1481 (Figure 1).

Applications Information

256 Transceivers on the Bus

The standard RS-485 receiver input impedance is $12k\Omega$ (1-unit load), and the standard driver can drive up to 32-unit loads. The MAX1481/MAX1484/MAX1485/MAX1486 transceivers have a 1/8-unit-load receiver input impedance (96k Ω), allowing up to 256 transceivers to be connected in parallel on one communication line. Connect any combination of these devices and/or other RS-485 transceivers totaling 32-unit loads or less.

Reduced EMI and Reflections

The MAX1481/MAX1485 are slew-rate limited, minimizing EMI and reducing reflections caused by improperly terminated cables. Figure 14 shows the driver output waveform and its Fourier analysis of a 20kHz signal transmitted by a MAX1484. High-frequency harmonic components with large amplitudes are evident. Figure 15 shows the same signal displayed for a MAX1481 transmitting under the same conditions. Figure 15's high-frequency harmonic components are much lower in amplitude compared to Figure 14's, significantly reducing potential EMI.

In general, a transmitter's rise time relates directly to the length of an unterminated stub, which can be driven with only minor waveform reflections. The following equation expresses this relationship conservatively:

Length = $t_{RISE} / (10 \times 1.5 \text{ ns/ft})$

where trisk is the transmitter's rise time.

For example, the MAX1481's rise time is typically 500ns, which results in excellent waveforms with a stub length up to 33 feet. A system may work well with longer unterminated stubs, even with severe reflections, if the waveform settles out before the UART samples them.

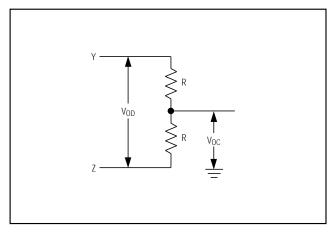


Figure 5. Driver DC Test Load

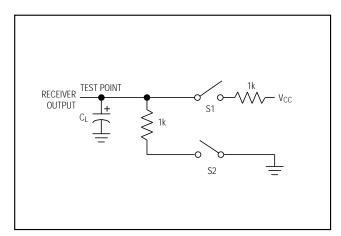


Figure 6. Receiver Enable/Disable Timing Test Load

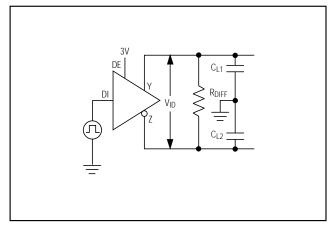


Figure 7. Driver Timing Test Circuit

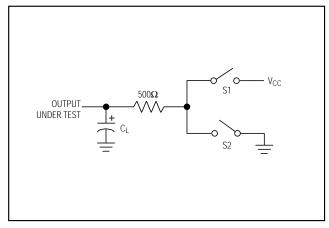


Figure 8. Driver Enable/Disable Timing Test Load

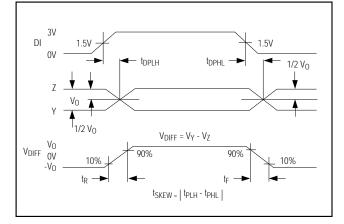


Figure 9. Driver Propagation Delays

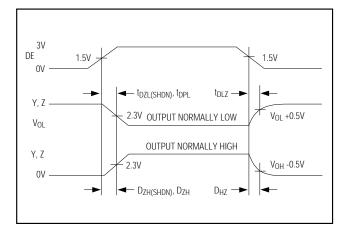


Figure 10. Driver Enable and Disable Times

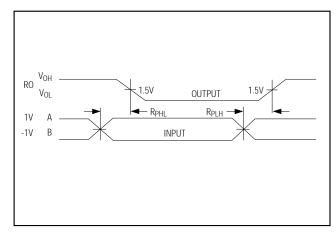


Figure 11. Receiver Propagation Delays

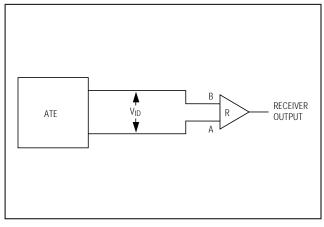


Figure 13. Receiver Propagation Delay Test Circuit

Low-Power Shutdown Mode (MAX1481 only)

Low-power shutdown mode is initiated by bringing both \overline{RE} high and DE low. In shutdown, the MAX1481 typically draws only 0.1µA of supply current.

RE and DE may be driven simultaneously; the parts are guaranteed not to enter shutdown if RE is high and DE is low for less than 50ns. If the inputs are in this state for at least 600ns, the parts are guaranteed to enter shutdown.

Enable times t_{ZH} and t_{ZL} in the *Switching Characteristics* tables assume the part was not in low-power shutdown. Enable times $t_{ZH(SHDN)}$ and $t_{ZL(SHDN)}$ assume the parts were shut down. It takes drivers and receivers

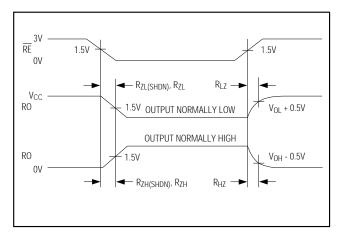


Figure 12. Receiver Enable and Disable Times (MAX1481/MAX1484 only)

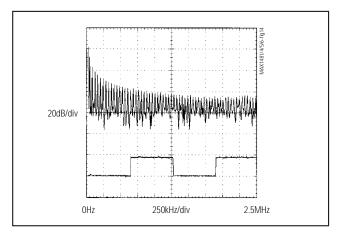


Figure 14. Driver Output Waveform and FFT Plot of MAX1484/MAX1486 Transmitting a 20kHz signal

longer to become enabled from the low-power shutdown mode $(t_{ZH}(SHDN), t_{ZL}(SHDN))$ than from the driver/receiver disable mode (t_{ZH}, t_{ZL}) .

Driver Output Protection

Excessive output current and power dissipation caused by faults or by bus contention are prevented by two mechanisms. A foldback current limit on the output stage provides immediate protection against short circuits over the whole common-mode voltage range (see *Typical Operating Characteristics*). In addition, a thermal shutdown circuit forces the driver outputs into a high-impedance state if the die temperature becomes excessive.

(Figure 16).

Line Length vs. Data Rate

The RS-485/RS-422 standard covers line lengths up to 4000 feet. For line lengths greater than 4000 feet, use the repeater application shown in Figure 16.

Figures 17 and 18 show the system differential voltage for the parts driving 4000 feet of 26AWG twisted-pair wire into 120Ω loads.

Typical Applications

The MAX1485/MAX1486 are designed for bidirectional data communications on multipoint bus transmission

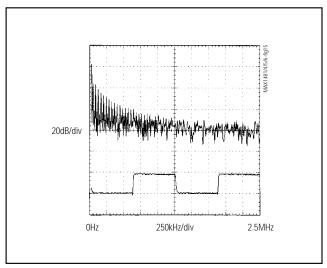


Figure 15. Driver Output Waveform and FFT Plot of MAX1481/MAX1485 Transmitting a 20kHz Signal

off the main line as short as possible. The slew-rate-limited MAX1481/MAX1485 are more tolerant of imperfect termination than the MAX1484/MAX1486.

MAX1481
MAX1481
MAX1484

lines. Figures 19 and 20 show typical network applica-

tions circuits. These parts can also be used as line

repeaters with cable lengths longer than 4000 feet

To minimize reflections, terminate the line at both ends

in its characteristic impedance, and keep stub lengths

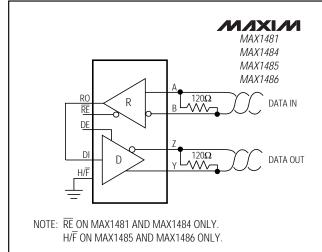


Figure 16. Line Repeater

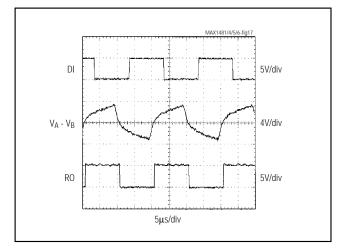


Figure 17. MAX1481/MAX1485 System Differential Voltage at 50kHz Driving 4000 ft. of Unterminated Cable

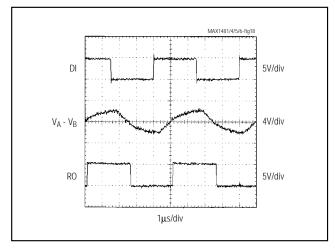


Figure 18. MAX1484/MAX1486 System Differential Voltage at 200kHz Driving 4000 ft. of Unterminated Cable

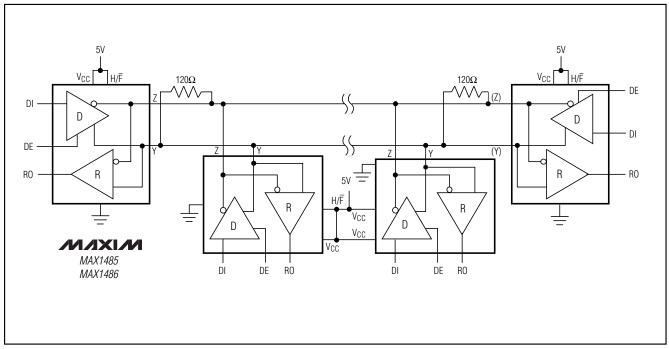


Figure 19. Typical Half-Duplex RS-485 Network

14 ______ *NIXIN*

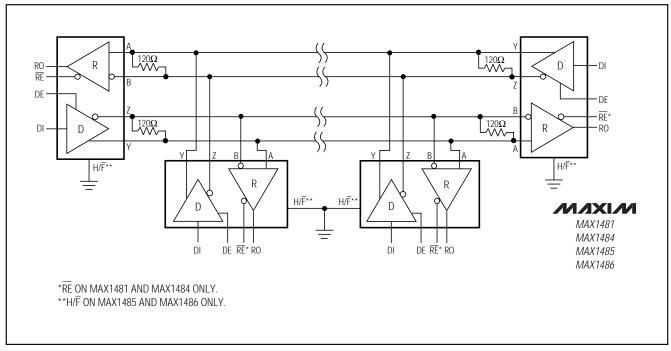


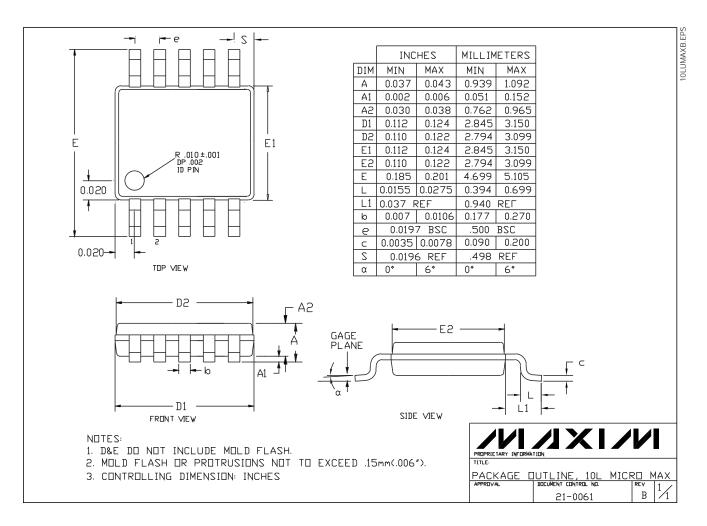
Figure 20. Typical Full-Duplex RS-485 Network

_____Chip Information

TRANSISTOR COUNT: 396

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

16 _____Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for RS-422/RS-485 Interface IC category:

Click to view products by Maxim manufacturer:

Other Similar products are found below:

NSI83085 WS3088EESA-GEC ADM2687EBRIZ-RL7 MAX489CPD+ MAX485ESA+T MAX491EPD+ MAX488EEPA+ MAX3080CPD+ MXL1535EEWI+ SN65LBC173DR DS16F95J/883 MAX490ESA+T LTM2881IY-3#PBF LT1791CN#PBF LTM2881CY-3#PBF LTC2852CDD#PBF LTC2857IMS8-2#PBF LT1791ACN#PBF LTC487CS#PBF MAX1487CUA+T XR3074XID-F XR3082XID-F SP1481EEN-L SN75ALS173NSR ADM3491ARZ-REEL ADM485JN ADM1485ANZ ADM1485JNZ ADM1490EBRMZ ADM489ABRZ ADM1491EBRZ-REEL7 ADM3070EYRZ ADM3073EARZ ADM4850ACPZ-REEL7 ADM4850ARMZ-REEL7 ADM485ANZ ADM485ANZ ADM485ANZ ADM485JNZ ADM485JNZ ADM485ANZ ADM485ARZ ADM3493ARZ ADM4856ARZ-REEL7 ADM487EARZ-REEL7 ADM488ABRZ ADM3493ARZ ADM3495EWYRZ