

## MAX17546EVKITB# Evaluation Kit

## Evaluates: MAX17546 5V Output-Voltage Application

### General Description

The MAX17546 5V output evaluation kit (EV kit) provides a proven design to evaluate the MAX17546 high-voltage, high-efficiency, synchronous step-down DC-DC converter. The EV kit is preset for a 5V output at load currents up to 5A and features a 450kHz switching frequency for optimum efficiency and component size. The EV kit features adjustable input, undervoltage lockout, adjustable soft-start, open-drain  $\overline{\text{RESET}}$  signal, and external frequency synchronization.

### Features

- Operates from a 6.5V to 42V Input Supply
- 5V Output Voltage
- Up to 5A Output Current
- 450kHz Switching Frequency
- Enable/UVLO Input, Resistor-Programmable UVLO Threshold
- Adjustable Soft-Start Time
- MODE Pin to Select Among PWM, PFM, or DCM Modes
- Open-Drain  $\overline{\text{RESET}}$  Output
- External Frequency Synchronization
- Overcurrent and Overtemperature Protection
- Proven PCB Layout
- Fully Assembled and Tested

**Ordering Information** appears at end of data sheet.

### Quick Start

#### Recommended Equipment

- MAX17546EVKITB#
- 6.5V to 42V, 10A DC input power supply
- Load capable of sinking 5A
- Digital voltmeter (DVM)

#### Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation. **Caution: Do not turn on power supply until all connections are completed.**

- 1) Set the power supply at a voltage between 6.5V and 42V. Disable the power supply.
- 2) Connect the positive terminal of the power supply to the VIN PCB pad and the negative terminal to the nearest PGND PCB pad. Connect the positive terminal of the 5A load to the VOUT PCB pad and the negative terminal to the nearest PGND PCB pad.
- 3) Connect the DVM across the VOUT PCB pad and the nearest PGND PCB pad.
- 4) Verify that the shunt is installed across pins 1-2 on jumper JU1 (see [Table 1](#) for details).
- 5) Select the shunt position on JU2 based on the intended mode of operation (see [Table 2](#) for details).
- 6) Turn on the DC power supply.
- 7) Enable the load.
- 8) Verify that the DVM displays 5V.

## Detailed Description

The EV kit provides a proven design to evaluate the MAX17546 high-voltage, high-efficiency, synchronous step-down DC-DC converter. The EV kit is preset for a 5V output from a 6.5V to 42V input at load currents up to 5A and features a 450kHz switching frequency for optimum efficiency and component size.

The EV kit includes an EN/UVLO PCB pad and JU1 to enable the output at a desired input voltage. The SYNC PCB pad allow an external clock to synchronize the device. JU2 allows the selection of a particular mode of operation based on light-load performance requirements. An additional RESET PCB pad is available for monitoring when the converter output is in regulation.

### Soft-Start Input (SS)

The device utilizes an adjustable soft-start function to limit inrush current during startup. The soft-start time is adjusted by the value of the external capacitor from SS to GND (C7). The selected output capacitance (C<sub>SEL</sub>) and output voltage (V<sub>OUT</sub>) determine the minimum value of C7, as shown by the following equation:

$$C7 \geq 28 \times 10^{-6} \times C_{SEL} \times V_{OUT}$$

The soft-start time (t<sub>SS</sub>) is related to C7 by the following equation:

$$t_{SS} = C7 / (5.55 \times 10^{-6})$$

For example, to program a 2.2ms soft-start time, C7 should be 12nF.

### Regulator Enable/Undervoltage-Lockout Level (EN/UVLO)

The device offers an adjustable-input, undervoltage-lockout level. For normal operation, a shunt should be

installed across pins 1-2 on JU1. To disable the output, install a shunt across pins 2-3 on JU1 and pull the EN/UVLO pin to GND. See [Table 1](#) for JU1 settings.

Set the voltage at which the device turns on with the resistive voltage-divider R1/R2 connected from VIN<sub>+</sub> to SGND. Connect the center node of the divider to EN/UVLO. Choose R1 to be 3.32MΩ and then calculate R2 as follows:

$$R2 = \frac{R1 \times 1.215}{VINU - 1.215}$$

where VINU is the voltage at which the device is required to turn on.

### Mode/SYNC Selection (MODE)

The device's MODE pin can be used to select among the PWM, PFM, or DCM modes of operation. The logic state of the MODE pin is latched when the V<sub>CC</sub> and EN/UVLO voltages exceed the respective UVLO rising thresholds and all internal voltages are ready to allow LX switching. State changes on the MODE pin are ignored during normal operation. Refer to the MAX17546 IC data sheet for more information on the PWM, PFM, and DCM modes of operation.

[Table 2](#) lists JU2 jumper settings that can be used to configure the desired mode of operation. The internal oscillator of the device can be synchronized to an external clock signal on the SYNC pin. The external synchronization clock frequency must be between 1.1f<sub>SW</sub> and 1.4f<sub>SW</sub>, where f<sub>SW</sub> is the frequency of operation set by R5. The minimum external clock high pulse width should be greater than 50ns, while the minimum external clock low pulse width should be greater than 160ns.

**Table 1. Regulator Enable (EN/UVLO) Description (JU1)**

SHUNT POSITION	EN/UVLO PIN	MAX17546_OUTPUT
1-2*	Connected to VIN	Enabled
Not installed	Connected to the center node of resistor-divider R1 and R2	Enabled, UVLO level set through the R1 and R2 resistors
2-3	Connected to SGND	Disabled

\*Default position.

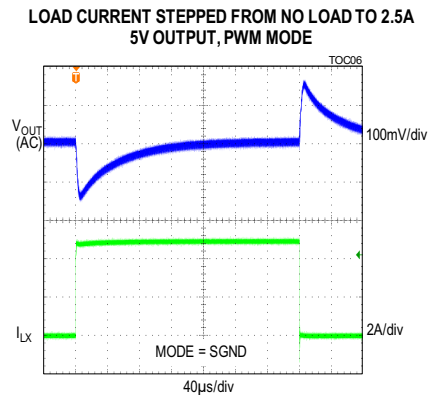
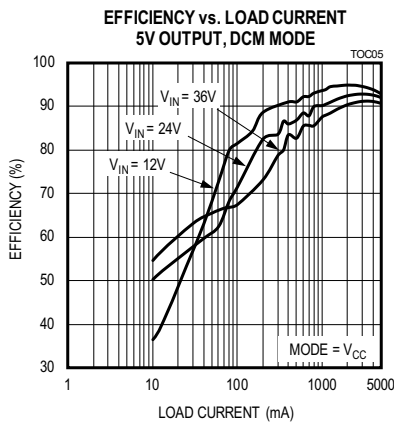
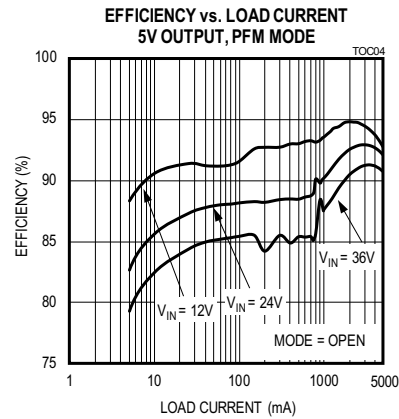
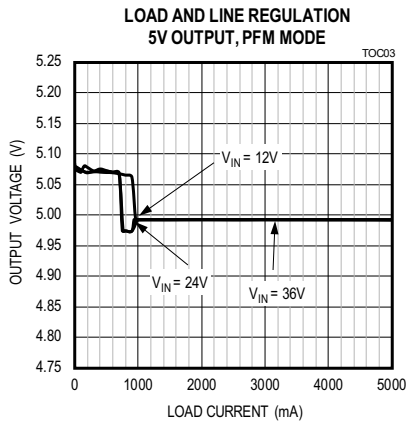
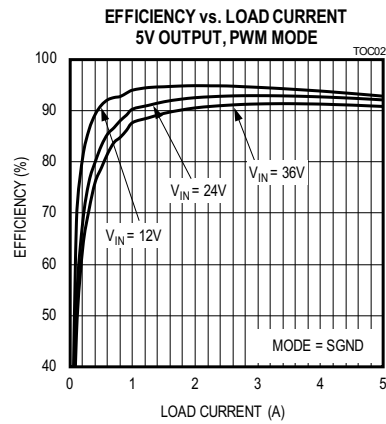
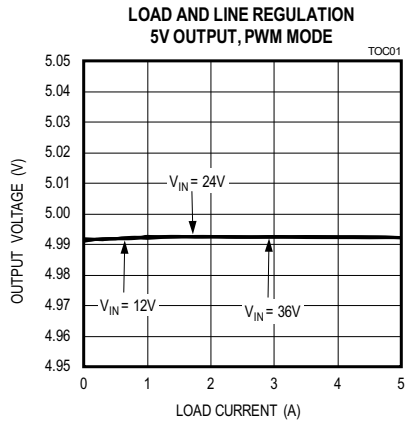
**Table 2. MODE Description (JU2)**

SHUNT POSITION	MODE PIN	MAX17546_MODE
Not installed*	Unconnected	PFM mode of operation
1-2	Connected to SGND	PWM mode of operation
2-3	Connected to VCC	DCM mode of operation

\*Default position.

EV Kit Performance Report

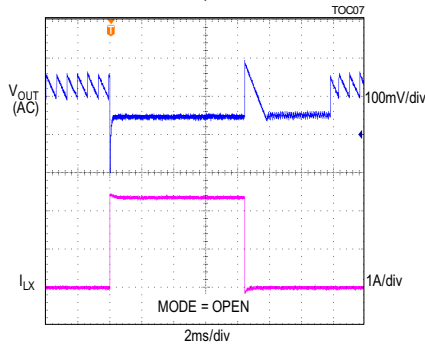
(Input voltage = 24V, unless otherwise noted.)



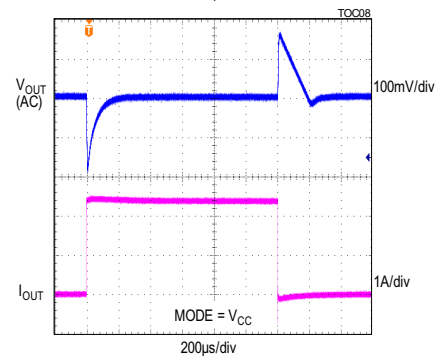
**EV Kit Performance Report (continued)**

(Input voltage = 24V, unless otherwise noted.)

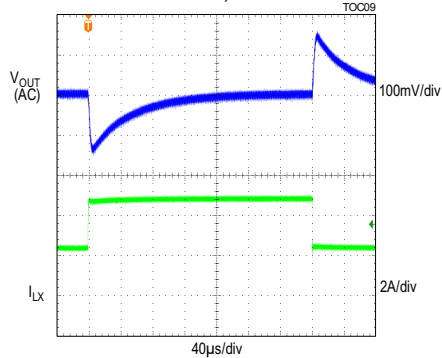
LOAD CURRENT STEPPED FROM 5mA TO 2.5A  
5V OUTPUT, PFM MODE



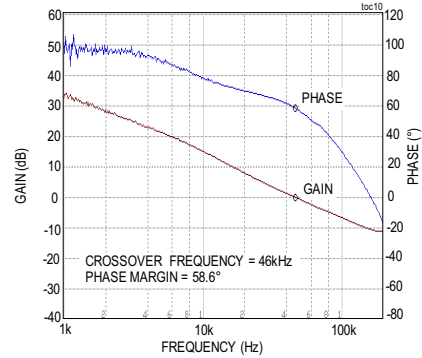
LOAD CURRENT STEPPED FROM 50mA TO 2.5A  
5V OUTPUT, DCM MODE



LOAD CURRENT STEPPED FROM 2.5A TO 5A  
5V OUTPUT, PWM MODE



BODE PLOT, 5V OUTPUT  
5A LOAD CURRENT



**Component Suppliers**

SUPPLIER	WEBSITE
Coilcraft, Inc.	www.coilcraft.com
TDK Corp.	www.tdk.com
Murata Americas	www.murata.com
NICHICON	www.nichicon.co.jp
Vishay	www.vishay.com

**Note:** Indicate that you are using the MAX17546 when contacting these component suppliers.

**Ordering Information**

PART	TYPE
MAX17546EVKITB#	EV Kit

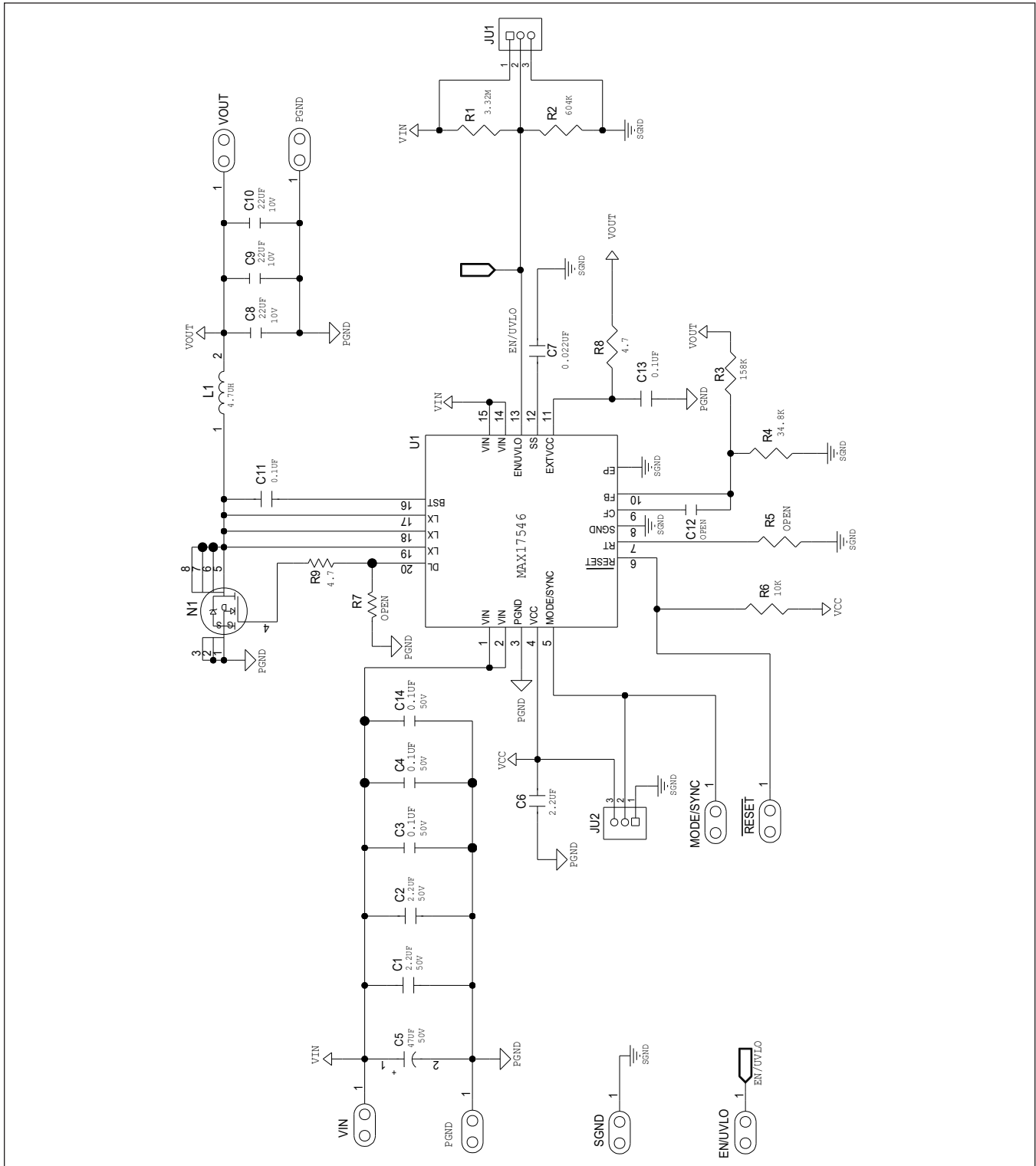
#Denotes RoHS compliant.

**MAX17546 5V Output Evaluation Kit Bill of Materials**

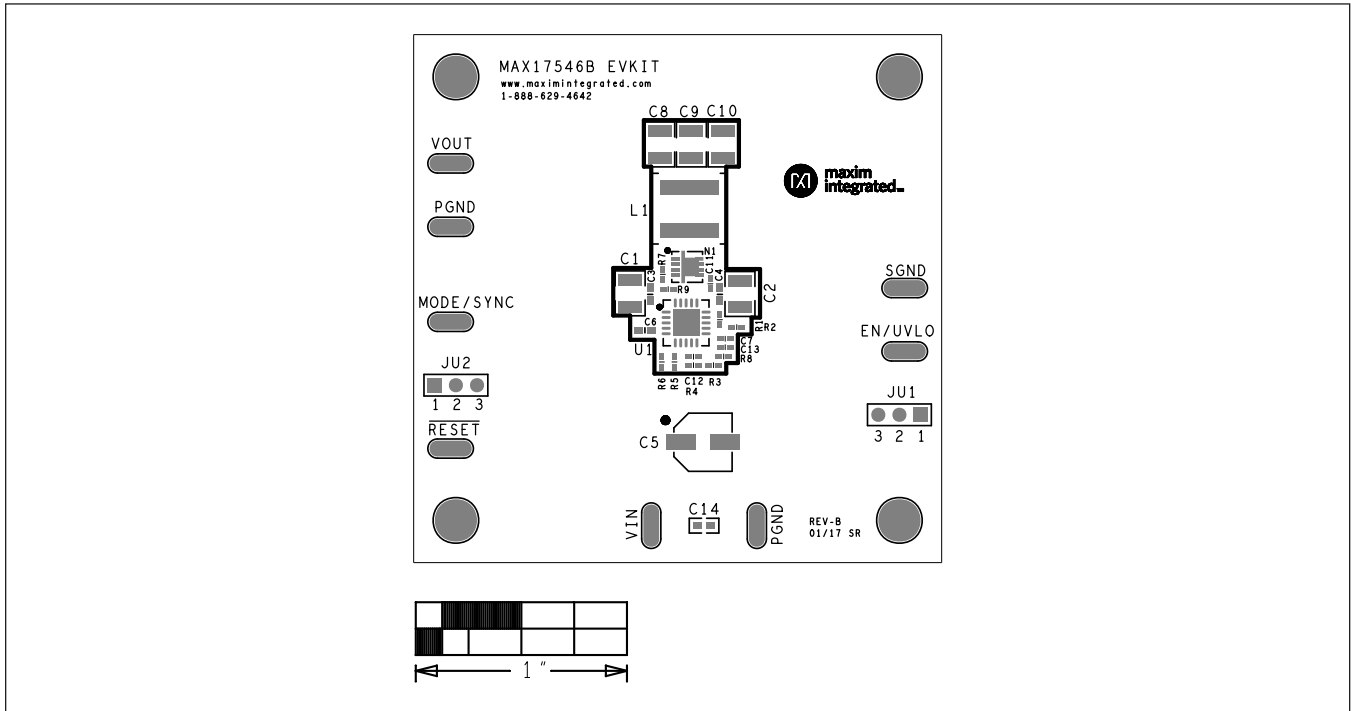
Serial No.	Description	Quantity	Designator	Part Number
1	2.2µF ±10%, 50V X7R ceramic capacitor (1210)	2	C1, C2	TDK C3225X7R1H225K
2	0.1µF ±10%, 50V X7R ceramic capacitor (0603)	3	C3,C4,C13	MURATA GCM188R71H104K
3	47µF 50V aluminum electrolytic (D=10mm)	1	C5	NICHICON UUD1H470MCL
4	2.2µF ±10%, 10V X7R ceramic capacitor (0603)	1	C6	MURATA GRM188R71A225K
5	22000pF ±10%, 50V X7R ceramic capacitor (0402)	1	C7	MURATA GRM155R71H223K
6	22µF ±10%, 10V X7R ceramic capacitor (1210)	3	C8, C9, C10	MURATA GRM32ER71A226K
7	0.1µF ±10%, 50V X7R ceramic capacitor (0402)	1	C11	MURATA GRM155R71H104KE14
8	Not installed, OPEN (0402)	0	C12	
9	0.1µF ±10%, 50V X7R ceramic capacitor (0402)	1	C13	TDK C1005X7R1H104K050BB
10	3-pin header (36-pin header 0.1" centers )	2	JU1, JU2	Sullins: PTC36SAAN
11	4.7uH Inductor (8.1mm x 8.6mm x 8mm)	1	L1	Coilcraft XAL8080-472
12	MOSFET (80V, 30A)	1	N1	VISHAY SILICONIX -SIS468DN-T1-GE3
13	3.32M ohm ±1%, resistor (0603)	1	R1	
14	604k ohm ±1%, resistor (0603)	1	R2	
15	158k ohm ±1%, resistor (0402)	1	R3	
16	34.8k ohm ±1%, resistor (0402)	1	R4	
17	Not installed, OPEN (0402)	0	R5	
18	10k ohm ±1%, resistor (0402)	1	R6	
19	Not installed, OPEN (0402)	0	R7	
20	4.7 ohm ±1%, resistor (0402)	2	R8,R9	
21	Buck Converter MAX17546ATP+	1	U1	MAX17546ATP+
22	Shunt	2	See Jumper Table	SULLINS STC02SYAN

JUMPER TABLE	
JUMPER	SHUNT POSITION
JU1	1-2
JU2	1

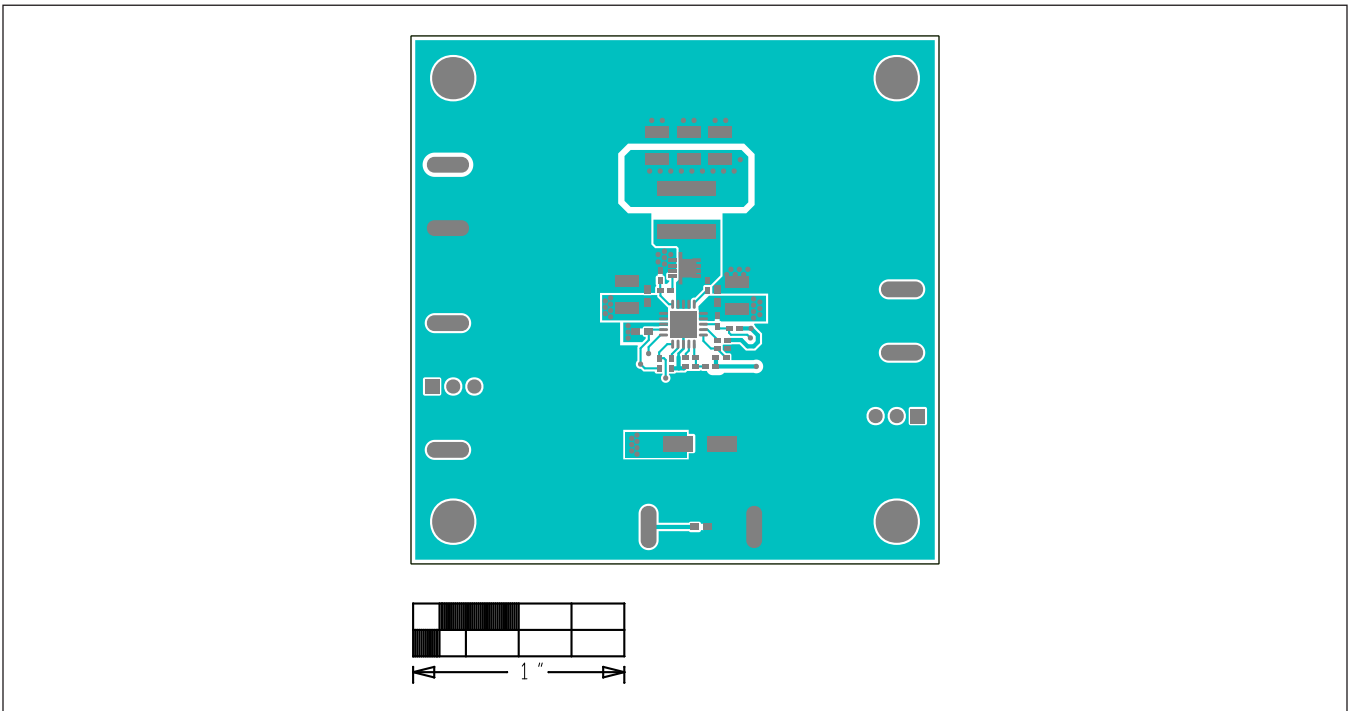
MAX17546 5V Output Evaluation Kit Schematic



MAX17546 5V Output Evaluation Kit PCB Layout

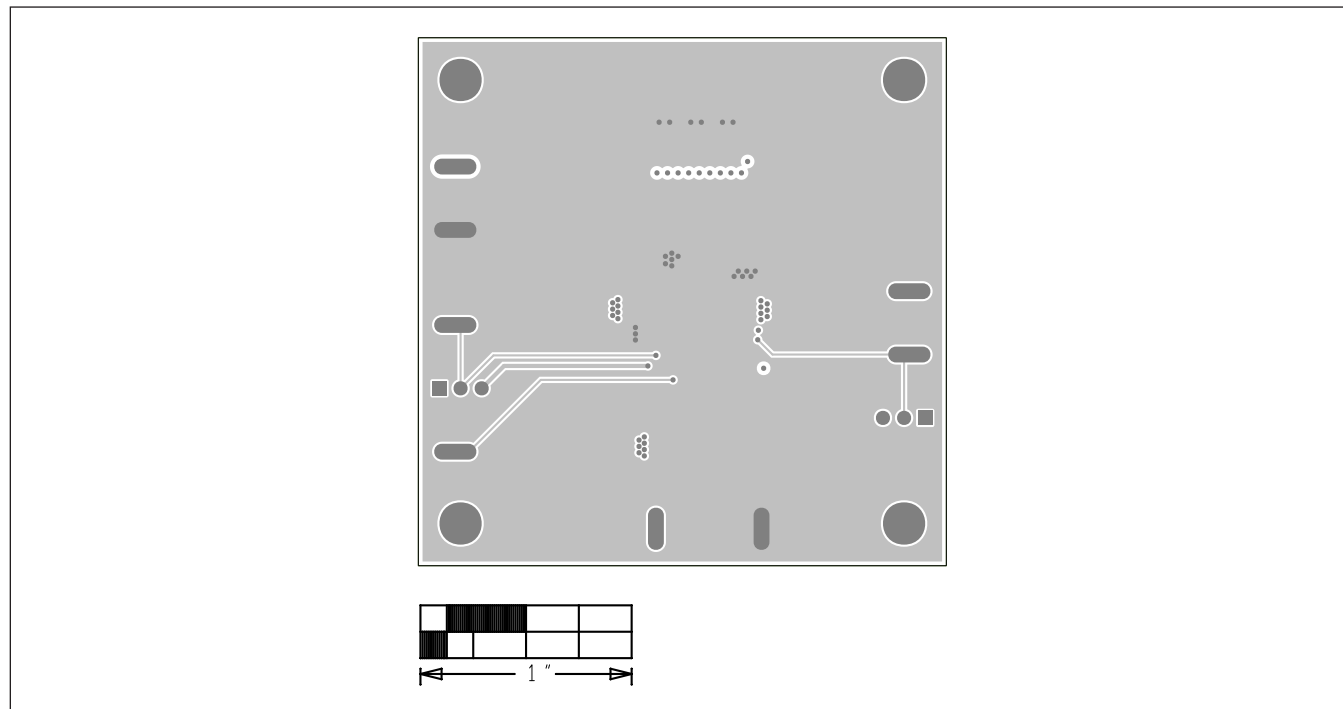


MAX17546 5V Output Evaluation Kit PCB Layout—Silk Top

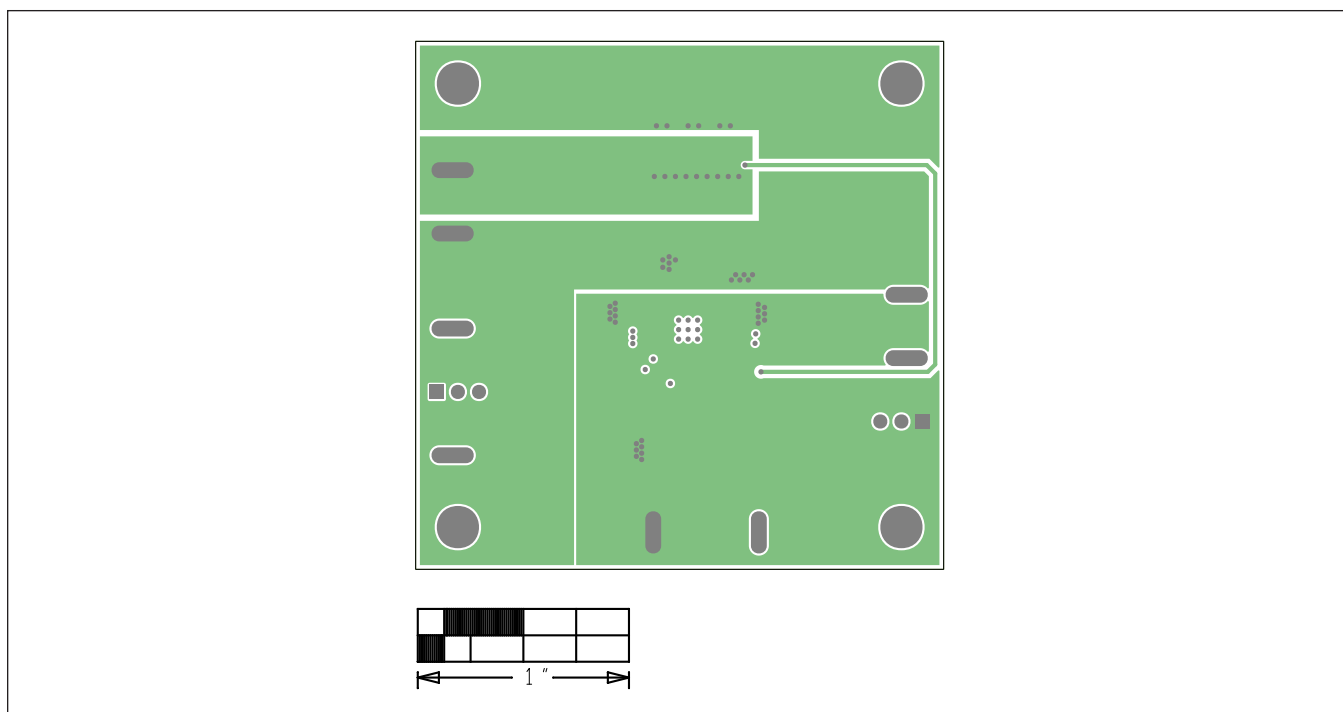


MAX17546 5V Output Evaluation Kit PCB Layout—Top Layer

**MAX17546 5V Output Evaluation Kit PCB Layout (continued)**



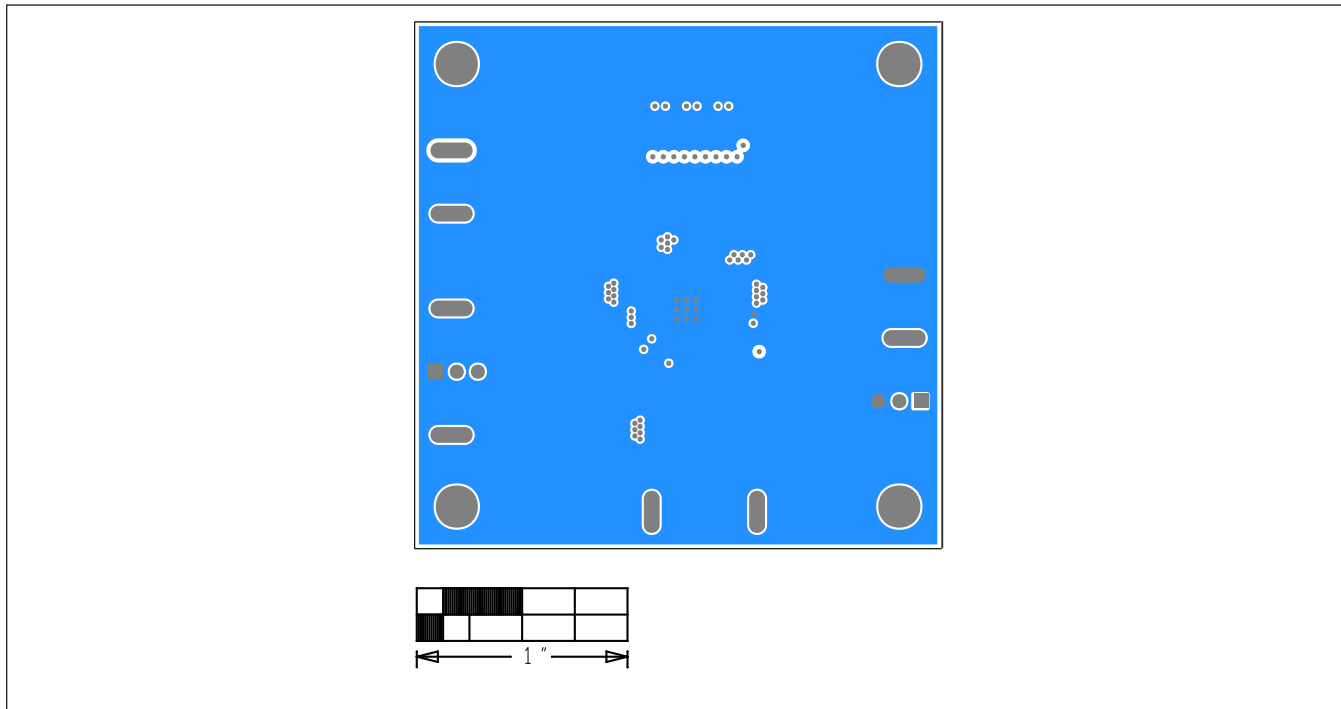
MAX17546 5V Output Evaluation Kit PCB Layout—Layer 2 Ground



MAX17546 5V Output Evaluation Kit PCB Layout—Layer 3 Power



MAX17546 5V Output Evaluation Kit PCB Layout (continued)



MAX17546 5V Output Evaluation Kit PCB Layout—Bottom Layer

## Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/15	Initial release	—
1	8/17	Updated the <i>Quick Start</i> and <i>Soft-Start Input (SS)</i> sections, and <i>Component Suppliers</i> table. Added a note to and updated Table 2. Replaced <i>Bill of Materials</i> , <i>Schematic</i> , and <i>PCB Layout</i> .	1–2, 4–9
2	4/18	Updated the Title, <i>Quick Start</i> section, Table 2, <i>Component Suppliers</i> table and Jumper table, and replaced TOC10.	1–10

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