

Low-Cost, SC70, Voltage-Output, High-Side Current-Sense Amplifier

General Description

The MAX4073 low-cost, high-side current-sense amplifier features a voltage output that eliminates the need for gain-setting resistors making it ideal for cell phones, notebook computers, PDAs, and other systems where current monitoring is crucial. High-side current monitoring does not interfere with the ground path of the battery charger making the MAX4073 particularly useful in battery-powered systems. The input common-mode range of +2V to +28V is independent of the supply voltage. The MAX4073's wide 1.8MHz bandwidth makes it suitable for use inside battery-charger control loops.

The combination of three gain versions and a selectable external-sense resistor sets the full-scale current reading. The MAX4073 offers a high level of integration, resulting in a simple and compact current-sense solution.

The MAX4073 operates from a +3V to +28V single supply and draws only 0.5mA of supply current. This device is specified over the automotive operating temperature range (-40°C to +125°C) and is available in a space-saving 5-pin SC70 package (half the size of the SOT23).

For a similar device in a 6-pin SOT23 with a wider common-mode voltage range (0 to +28V), see the MAX4173 data sheet.

_Applications

Cell Phones

Notebook Computers

Portable/Battery-Powered Systems

Smart Battery Packs/Chargers

PDAs

Power Management Systems

PA Bias Control

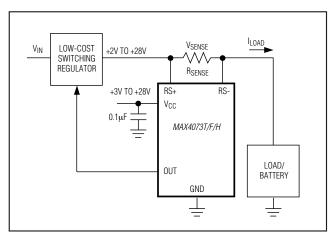
General System/Board-Level Current Monitoring

Precision Current Sources

Features

- **♦ Low-Cost, Compact, Current-Sense Solution**
- **♦ Three Gain Versions Available**
 - +20V/V (MAX4073T)
 - +50V/V (MAX4073F)
 - +100V/V (MAX4073H)
- ♦ ±1.0% Full-Scale Accuracy
- ♦ 500µA Supply Current
- ♦ Wide 1.8MHz Bandwidth
- ♦ +3V to +28V Operating Supply
- ♦ Wide +2V to +28V Common-Mode Range Independent of Supply Voltage
- ♦ Automotive Temperature Range (-40°C to +125°C)
- ♦ Available in Space-Saving 5-Pin SC70 Package

Typical Operating Circuit



Pin Configurations appear at end of data sheet.

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE	GAIN (V/V)	TOP MARK
MAX4073TAXK+T	-40°C to +125°C	5 SC70	20	ACM
MAX4073TAUT+T	-40°C to +125°C	6 SOT23	20	AAUE
MAX4073FAXK+T	-40°C to +125°C	5 SC70	50	ACN
MAX4073FAUT+T	-40°C to +125°C	6 SOT23	50	AAUF
MAX4073HAXK+T	-40°C to +125°C	5 SC70	100	ACO
MAX4073HAUT+T	-40°C to +125°C	6 SOT23	100	AAUG

⁺Denotes lead(Pb)-free/RoHS-compliant package.

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ABSOLUTE MAXIMUM RATINGS

VCC to GND -0.3V to +30V RS+, RS- to GND -0.3V to +30V OUT to GND -0.3V to (V _{CC} + 0.3V) Output Short-Circuit to GND Continuous Differential Input Voltage (V _{RS+} - V _{RS-}) ±5V Current Into Any Pin ±20mA	Operating Temperature Range40°C to +125°C Junction Temperature+150°C Storage Temperature Range65°C to +150°C Lead Temperature (soldering, 10s)+300°C Soldering Temperature+260°C
Continuous Power Dissipation (T _A = +70°C) 5-Pin SC70 (derate 2.27mW/°C above +70°C)200mW 6-Pin SOT23 (derate 8.7mW/°C above +70°C)696mW	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{RS+} = +2V \text{ to } +28V, V_{SENSE} = (V_{RS+} - V_{RS-}) = 0, V_{CC} = +3V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)$

PARAMETER	SYMBOL	C	ONDITIONS	MIN	TYP	MAX	UNITS	
Operating Voltage Range	Vcc	(Note 2)		3		28	V	
Common-Mode Input Range	VCMR	(Note 3)		2		28	V	
Common-Mode Rejection	CMR	V _{SENSE} = 100m ³	V, V _{CC} = 12V		90		dB	
Supply Current	Icc	V _{CC} = 28V			0.5	1.2	mA	
Leakage Current	I _{RS+} /I _{RS-}	V _{CC} = 0V, V _{RS+}	= 28V		0.05	1	μΑ	
Input Biog Current	I _{RS+}				20	60		
Input Bias Current	I _{RS-}				40	120	μΑ	
Full-Scale Sense Voltage	V _{SENSE}	V _{SENSE} = (V _{RS+}	- V _{RS-})		150		mV	
		V _{SENSE} = 100m ³	$V, V_{CC} = 12V, V_{RS+} = 2V$		±1.0			
		VSENSE = 100mV, V _{CC} = 12V, V _{RS+} = 12V, T _A = +25°C VSENSE = 100mV, V _{CC} = 12V, V _{RS+} = 12V, T _A = T _{MIN} to T _{MAX}			±1.0	±5.0		
Total OUT Voltage Error (Note 4)						±7.0	%	
Total OOT Vollage Effor (Note 4)		$V_{SENSE} = 100 \text{mV}, V_{CC} = 28 \text{V}, V_{RS+} = 28 \text{V}, T_A = +25 ^{\circ}\text{C}$			±1.0	±5.0		
		V _{SENSE} = 100mV, V _{CC} = 28V, V _{RS+} = 28V, T _A = T _{MIN} to T _{MAX}				±8.5		
		V _{SENSE} = 6.25mV (Note 5); V _{CC} = 12V, V _{RS+} = 12V			±7.5			
Extrapolated Input Offset Voltage	Vos	V _{CC} = V _{RS+} = 12V, V _{SENSE} > 10mV			1.0		mV	
OUT High Voltage	(VCC - VOH)	Vsense = 150mV			1.2	V		

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ELECTRICAL CHARACTERISTICS (continued)

 $(V_{RS+} = +2V \text{ to } +28V, V_{SENSE} = (V_{RS+} - V_{RS-}) = 0, V_{CC} = +3V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)$

PARAMETER	SYMBOL	C	ONDITIONS	MIN	TYP	MAX	UNITS
			MAX4073T, V _{SENSE} = 100mV		1.8		
		V _{CC} = 12V,	MAX4073F, V _{SENSE} = 100mV		1.7	MHz	MHz
Bandwidth	BW	$V_{RS+} = 12V,$ $C_{LOAD} = 5pF$	MAX4073H, VSENSE = 100mV		1.6		
			MAX4073T/F/H VSENSE = 6.25mV (Note 5)		600		kHz
		MAX4073T			20		
Gain	Ay	MAX4073F		50			V/V
		MAX4073H			100		
	ΔΑV	VCC = 12V, VRS+ = 12V, VSENSE = 10mV to 150mV, MAX4073T/F	T _A = +25°C		±1.0	±4.5	±4.5 ±6.5 %
Gain Accuracy			TA = TMIN to TMAX			±6.5	
dam / local doy		VCC = 12V, VRS+ = 12V, VSENSE = 10mV to 100mV, MAX4073H	TA = +25°C		±1.0	±4.5	
			TA = TMIN to TMAX			±6.5	
OUT Settling Time to 1% of Final		VCC = 12V VRS+ = 12V	VSENSE = 6.25mV to 100mV		400		ns
Value		$C_{LOAD} = 5pF$	VSENSE = 100mV to 6.25mV		800		110
Output Resistance	Rout				12		kΩ
			V _{SENSE} = 60mV, MAX4073T	70	78		
Power-Supply Rejection Ratio	PSRR	Vcc = 3V to 28V	V _{SENSE} = 24mV, MAX4073F	70	85		dB
			V _{SENSE} = 12mV, MAX4073H	70	90		<u> </u>
Power-Up Time (Note 6)		$C_{LOAD} = 5pF, V$	SENSE = 100mV		5		μs
Saturation Recovery Time (Note 7)		V _{CC} = 12V, V _{RS} C _{LOAD} = 5pF	₊ = 12V,		5		μs

Note 1: All devices are 100% production tested at $T_A = +25$ °C. All temperature limits are guaranteed by design.

Note 2: Inferred from PSRR test.

Note 3: Inferred from OUT Voltage Error test.

Note 4: Total OUT Voltage Error is the sum of the gain and offset errors.

Note 5: 6.25mV = 1/16 of 100mV full-scale sense voltage.

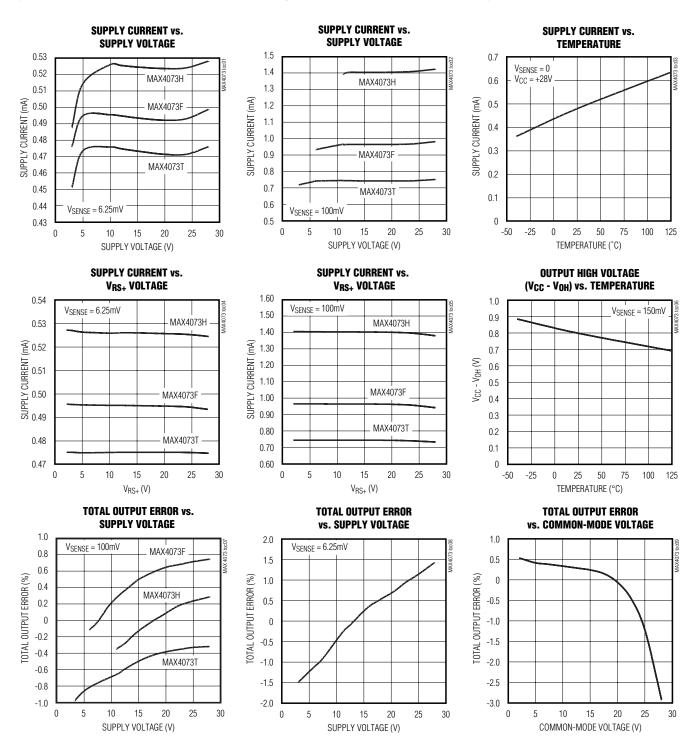
Note 6: Output settles to within 1% of final value.

Note 7: The device will not experience phase reversal when overdriven.

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Typical Operating Characteristics

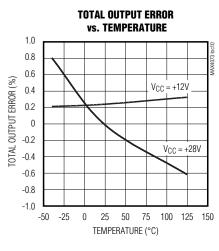
 $(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = 100 \text{mV}, C_L = 5 \text{pF}, T_A = +25 ^{\circ}\text{C}, unless otherwise noted.})$

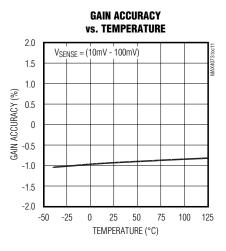


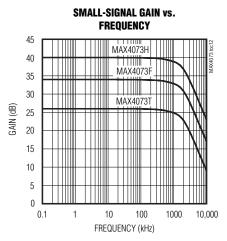
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Typical Operating Characteristics (continued)

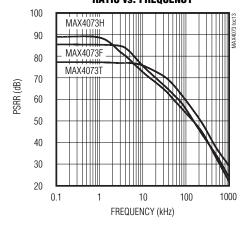
(VCC = +12V, VRS+ = +12V, VSENSE = 100mV, CL = 5pF, TA = +25°C, unless otherwise noted.)



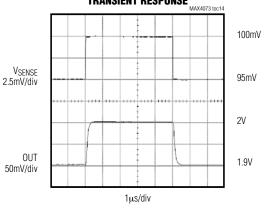




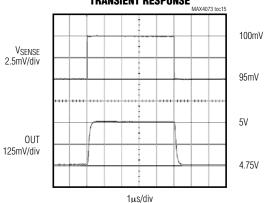
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



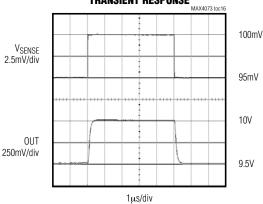
MAX4073T SMALL-SIGNAL TRANSIENT RESPONSE



MAX4073F SMALL-SIGNAL TRANSIENT RESPONSE



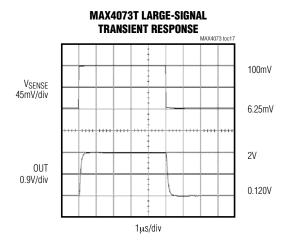
MAX4073H SMALL-SIGNAL TRANSIENT RESPONSE

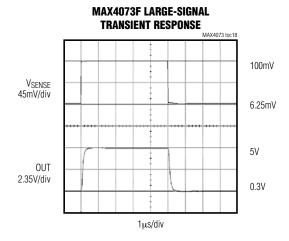


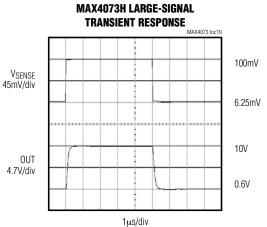
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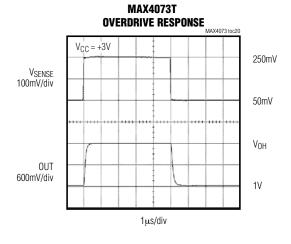
Typical Operating Characteristics (continued)

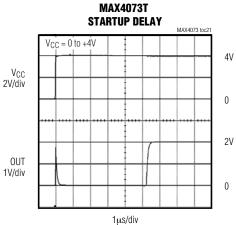
 $(V_{CC} = +12V, V_{RS+} = +12V, V_{SENSE} = 100mV, C_L = 5pF, T_A = +25^{\circ}C, unless otherwise noted.)$











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Pin Description

P	PIN	NAME	FUNCTION	
SOT23	SC70			
1, 2	2	GND	Ground	
3	3	VCC	Supply Voltage Input. Bypass to GND with a 0.1µF capacitor.	
4	4	RS+	Power-Side Connection to the External Sense Resistor	
5	5	RS-	Load-Side Connection to the External Sense Resistor	
6	1	OUT	Voltage Output. V_{OUT} is proportional to V_{SENSE} . Output impedance is approximately $12k\Omega$.	

Detailed Description

The MAX4073 high-side current-sense amplifier features a +2V to +28V input common-mode range that is independent of supply voltage. This feature allows the monitoring of current out of a battery as low as +2V and also enables high-side current sensing at voltages greater than the supply voltage (V_{CC}).

The MAX4073 operates as follows: current from the source flows through RSENSE to the load (Figure 1). Since the internal-sense amplifier's inverting input has high impedance, negligible current flows through RG2 (neglecting the input bias current). Therefore, the sense amplifier's inverting-input voltage equals VSOURCE - (ILOAD)(RSENSE). The amplifier's open-loop gain forces its noninverting input to the same voltage as the inverting input. Therefore, the drop across RG1 equals (ILOAD)(RSENSE). Since IRG1 flows through RG1, IRG1 = (ILOAD)(RSENSE) / RG1. The internal current mirror multiplies IRG1 by a current gain factor, β , to give $IRGD = \beta \times IRG1$. Solving $IRGD = \beta \times (ILOAD)(RSENSE)$ / RG1. Assuming infinite output impedance, Vout = (IRGD) (RGD). Substituting in for IRGD and rearranging, VOUT = $\beta \times (RGD/RG1)(RSENSE \times ILOAD)$. The parts gain equals $\beta \times RGD/RG1$. Therefore, Vout = (GAIN) (RSENSE) (ILOAD), where GAIN = 20V/V for MAX4073T, GAIN = 50V/V for MAX4073F, and GAIN = 100V/V for MAX4073H.

Set the full-scale output range by selecting RSENSE and the appropriate gain version of the MAX4073.

Applications Information

Recommended Component Values

The MAX4073 senses a wide variety of currents with different sense resistor values. Table 1 lists common resistor values for typical operation of the MAX4073.

Choosing RSENSE

To measure lower currents more accurately, use a large value for RSENSE. The larger value develops a

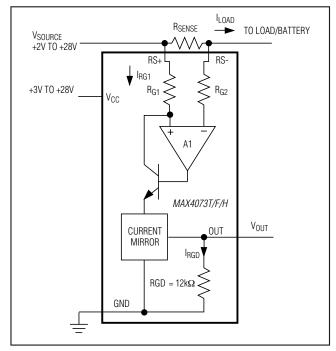


Figure 1. Functional Diagram

higher-sense voltage that reduces offset voltage errors of the internal op amp. Typical sense voltages range between 10mV and 150mV.

In applications monitoring very high currents, RSENSE must be able to dissipate the I^2R losses. If the resistor's rated power dissipation is exceeded, its value may drift or it may fail altogether, causing a differential voltage across the terminals in excess of the absolute maximum ratings ($\pm 5V$).

If ISENSE has a large high-frequency component, minimize the inductance of RSENSE. Wire-wound resistors have the highest inductance, metal-film resistors are

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somewhat better, and low-inductance metal-film resistors are best suited for these applications.

For $V_{SENSE} = 100$ mV, full-scale output voltage can be 2V, 5V, or 10V depending on the gain. For proper operation, ensure V_{CC} exceeds the full-scale output voltage by 1.2V (see Output High Voltage ($V_{CC} - V_{OH}$) vs. Temperature in the *Typical Operating Characteristics*).

Using a PCB Trace as RSENSE

If the cost of RSENSE is an issue and accuracy is not critical, use the alternative solution shown in Figure 2. This solution uses copper PC board traces to create a sense resistor. The resistivity of a 0.1-inch-wide trace of 2-ounce copper is approximately $30 m \Omega/{\rm ft}$. The resistance-temperature coefficient of copper is fairly high (approximately 0.4%/°C), so systems that experience a wide temperature variance must compensate for this effect. In addition, do not exceed the maximum power dissipation of the copper trace.

For example, the MAX4073T (with a maximum load current of 10A and an RSENSE of $5m\Omega$) creates a full-scale VSENSE of 50mV that yields a maximum VOUT of 1V. RSENSE in this case requires about 2 inches of 0.1 inchwide copper trace.

Output Impedance

The output of the MAX4073 is a current source driving a $12k\Omega$ resistance. Resistive loading added to OUT reduces the output gain of the MAX4073. To minimize output errors for most applications, connect OUT to a high-impedance input stage. When output buffering is required, choose an op amp with a common-mode input range and an output voltage swing that includes ground when operating with a single supply. The op amp's supply voltage range should be at least as high as any voltage the system may encounter.

The percent error introduced by output loading is determined with the following formula:

$$\%_{\text{ERROR}} = 100 \left(\frac{R_{\text{LOAD}}}{12k\Omega + R_{\text{LOAD}}} - 1 \right)$$

where R_{LOAD} is the external load applied to OUT.

Current Source Circuit

Figure 3 shows a block diagram using the MAX4073 with a switching regulator to make a current source.

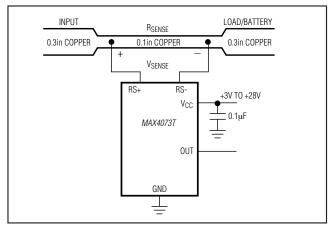


Figure 2. MAX4073T Connections Showing Use of PC Board

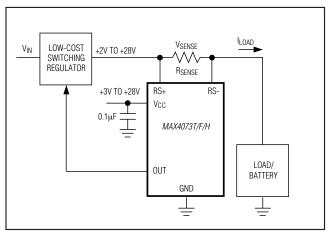


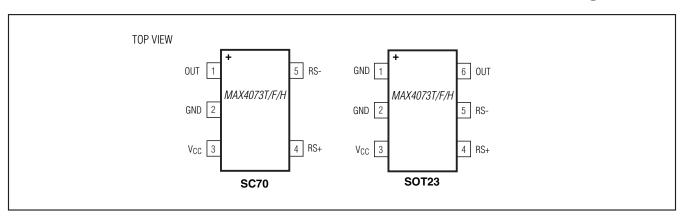
Figure 3. Current Source

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Table 1. Recommended Component Values

FULL-SCALE LOAD CURRENT ILOAD (A)	CURRENT-SENSE RESISTOR R _{SENSE} (mΩ)	GAIN	FULL-SCALE OUTPUT VOLTAGE (FULL-SCALE V _{SENSE} = 100mV) Vout (V)
		20	2.0
0.1	1000	50	5.0
		100	10.0
		20	2.0
1	100	50	5.0
		100	10.0
		20	2.0
5	20	50	5.0
		100	10.0
		20	2.0
10	10	50	5.0
		100	10.0

Pin Configurations



Chip Information

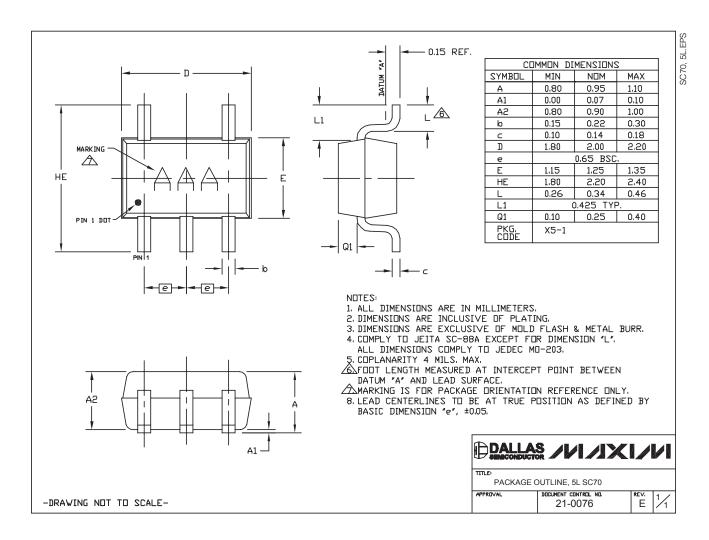
PROCESS: Bipolar

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Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/package. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

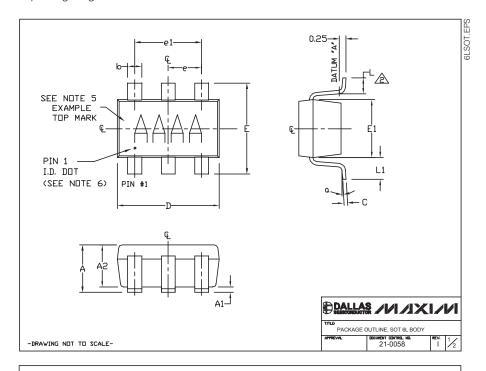
PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
5 SC70	X5+1	<u>21-0076</u>	90-0188
6 SOT23	U6+4	<u>21-0058</u>	<u>90-0175</u>



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Package Information (continued)

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NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.

£ FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE.

- 3. PACKAGE DUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR. MOLD FLASH, PROTRUSION OR METAL BURR SHOULD NOT EXCEED 0.25mm.
- 4. PACKAGE DUTLINE INCLUSIVE OF SOLDER PLATING.
- 5. PIN 1 IS LOWER LEFT PIN WHEN READING TOP MARK FROM LEFT TO RIGHT. (SEE EXAMPLE TOP MARK)
- 6. PIN 1 I.D. DOT IS 0.3mm Ø MIN. LOCATED ABOVE PIN 1.
- 7. MEETS JEDEC MOL78, VARIATION AB.
- 8. SOLDER THICKNESS MEASURED AT FLAT SECTION OF LEAD BETWEEN 0.08mm AND 0.15mm FROM LEADTIP.
- 9. LEAD TO BE COPLANAR WITHIN 0.1mm.
- 10. NUMBER OF LEADS SHOWN ARE FOR REFERENCE ONLY.
- 11. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

SYMBOL	MIN	NOMINAL	MAX			
Α	0.90	1.25	1.45			
A1	0.00	0.05	0.15			
A2	0.90	1.10	1.30			
b	0.35	0.40	0.50			
С	0.08	0.15	0.20			
D	2,80	2.90	3.00			
E	2.60	2.80	3.00			
E1	1.50	1.50 1.625 1.75				
L	0.35	0.45	0.60			
L1 0,60 REF.						
ei		1.90 BSC				
e		0.95 BS0	<u>`</u> .			
a	0.	2.5*	10°			
PKG CDDES:						
U6-1, U6-2, U6-4, U6C-8, U6SN-1, U6CN-2, U6S-3, U6F-5, U6F-6, U6FH-5, U6FH-6						

DALLAS // IXI//
TITLE
PACKAGE OUTLINE, SOT 6L BODY

-DRAWING NOT TO SCALE-

Low-Cost, SC70, Voltage-Output, High-Side Current-Sense Amplifier

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	5/01	Initial release	_
2	8/12	Added lead-free notation to Ordering Information.	1



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time. The parametric values (min and max limits) shown in the Electrical Characteristics table are guaranteed. Other parametric values quoted in this data sheet are provided for guidance.

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AD8214ARMZ AD8214ARMZ-R7 AD8290ACPZ-R2 AD8290ACPZ-R7 AD22057RZ AD8215YRZ AD8210YRZ AD22057RZ-RL

AD8210YRZ-REEL7 AD8215WYRZ ADM4073FWRJZ-REEL7 LT1999HMS8-50F#WPBF LT1999HMS8-10F#WPBF LTC6102HVIMS8#PBF LTC6101AIMS8#PBF LTC6102CMS8-1#PBF MAX4080TASA+T LT1787HS8#PBF LT1620CS8#PBF LTC6115HMS#PBF LTC6115IMS#PBF